2006-2344: A POWER SYSTEMS ANALYSIS PROJECT

Timothy Skvarenina, Purdue University

Dr. Skvarenina received the BSEE and MSEE degrees from the Illinois Institute of Technology and the Ph.D. in electrical engineering from Purdue University. He served 21 years in the U.S. Air Force, in a variety of engineering and teaching positions. In the fall of 1991, he joined the faculty of the College of Technology at Purdue University where he currently holds the rank of Professor and teaches undergraduate courses in electrical machines and power systems and serves as the department assessment coordinator. He has authored or coauthored over 30 papers in the areas of power systems, pulsed power systems, power electronics, and engineering education. He is the primary author of one textbook, "Electrical Power and Controls", and is the Editor-in-Chief of the CRC Power Electronics Handbook. He is a senior member of the IEEE; a member of the American Society for Engineering Education (ASEE), Tau Beta Pi, and Eta Kappa Nu; and a registered professional engineer in the state of Colorado. He has served as Chair of the Central Indiana Chapter of the IEEE Power Engineering Society, Chair of the ASEE Energy Conversion & Conservation Division, and as a Vice President, and member of the board of directors, of ASEE. He has also served as an evaluator for four ABET TC2K visits.

A Power Systems Analysis Project

Abstract

This paper describes a six-week laboratory project in which students analyze a small power system. The analysis is done with PowerWorld and is supplemented with MatLab calculations. The students were provided information concerning a small (seven bus, two generator) power system. They were required to assemble all of the information into tables, both in actual values as well as in per unit on a common base, before beginning the analysis. Since the system was small, students could manually calculate Ybus and Zbus values and compare them to the PowerWorld results. Any discrepancies had to be resolved in order to get the proper results. Once the system was correctly entered into PowerWorld, a power flow and fault analyses were done. Each week the students were required to produce interim products which were graded to insure they were on track. At the end, they were required to produce a formal report with all of their results. This project contributes to several of the ABET a-k outcomes, both for technology and engineering. Producing the report contributes to their communication skills, the analysis and Correcting of the system contributes to their design skills, and the use of PowerWorld and MatLab, which are both commercial products, contributes to their ability to use current tools of the trade.

Introduction

The Engineering Accreditation Commission (EAC) and the Technology Accreditation Commission (TAC) of ABET have both moved to outcomes-based accreditation of engineering and technology programs, via EC 2000 and TC2K, respectively. Both EC 2000 and TC2K require that every accredited program develop a set of program outcomes, which must insure that students have demonstrated the achievement of eleven outcomes, the so-called "a" to "k" lists. Table 1 shows a listing of the "a" to "k" outcomes for EC 2000 and TC2K.^{1, 2} With the change from previous accreditation criteria, ABET has gone away from the so-called "bean counting" that required certain numbers of credit hours in various categories, such as mathematics, sciences, social sciences, and humanities. Instead each program must evaluate and assess its curriculum on a continuous basis to show that graduates are demonstrating the required outcomes.

Although the outcomes are different for the two sets of criteria, they have a number of similarities. In particular, both the EAC and TAC require an "ability to communicate effectively," (item g. for both). Other similarities are noted between EAC item k. and TAC item a., which require the use or mastery of techniques, skills, and modern tools; EAC item c. and TAC item d., which both deal with design; EAC item b. and TAC item c., which both include interpretation of data; and EAC item d. and TAC item f., which deal with formulating and solving technical problems. The author has found that contributions can be made to all of these criteria with a power systems analysis project. This paper provides a brief description of the

Electrical and Computer Engineering Technology (ECET) curriculum and assessment plan at Purdue University and ECET 331, "Generation and Transmission of Electrical Power,"which is a junior/senior elective course. The paper then provides a detailed description of a six-week power systems analysis project done as part of the laboratory portion of ECET 331.

EAC	Required Outcomes (Criterion 3)	TAC Required Outcomes (Criterion 2)
(a) (b)	an ability to apply knowledge of mathematics, science, and engineering an ability to design and conduct experiments, as well as to analyze and interpret data	 a. an appropriate mastery of the knowledge, techniques, skills and modern tools of their disciplines, b. an ability to apply current knowledge and adapt to
(c)	an ability to design a system, component, or process to meet desired needs	emerging applications of mathematics, science, engineering and technology,
(d)	an ability to function on multi-disciplinary teams	c. an ability to conduct, analyze and interpret experiments and apply experimental results to
(e)	an ability to identify, formulate, and solve engineering problems	improve processes, d. an ability to apply creativity in the design of
(f)	an understanding of professional and ethical responsibility	systems, components or processes appropriate to program objectives,
(g)	an ability to communicate effectively	e. an ability to function effectively on teams,f. an ability to identify, analyze and solve technical
(h)	the broad education necessary to understand the impact of engineering solutions in a global and societal context	problems,g. an ability to communicate effectively,h. a recognition of the need for, and an ability to
(i)	a recognition of the need for, and an ability to engage in life-long learning	engage in lifelong learning, i. an ability to understand professional, ethical and
(j)	a knowledge of contemporary issues	social responsibilities, j. a respect for diversity and a knowledge of
(k)	an ability to use the techniques, skills, and modern engineering tools necessary for	contemporary professional, societal and global issues,
	engineering practice.	k. a commitment to quality, timeliness, and continuous improvement.

Table 1.Comparison of EC 2000 and TC2K Outcomes "a" to "k"

The Purdue ECET Curriculum

The ECET program at Purdue is a 2+2 curriculum; i.e., students spend two years working toward an Associate of Science degree in EET, followed by two more years to obtain a B.S. degree. At the Purdue West Lafayette campus, very few students stop at the A.S. point. The curriculum, like most engineering technology programs, is a very hands-on program with labs accompanying almost every ECET course. For many years, the Purdue ECET program has taken pride in being one of only a few EET programs in the United States with a strong electric power program. The plan of study includes a required electrical power and controls course that has been previously described.³ Junior/senior electives include a course oriented toward electric utility generation and transmission (ECET 331)), an electrical distribution course,⁴ and two controls courses. Despite the tradition, the power courses sometimes come into question. One way of defending the power courses is to make sure they contribute to the program outcomes, which reflect the ABET a-k items in Table 1.

As the department prepared for its first accreditation under TC2K, we developed program

outcomes. Later, as the process continued, it was decided that every course in the curriculum should contribute to some of the outcomes. In order to strengthen the case for ECET 331, I decided to incorporate a multi-week project as part of the lab, which would help meet several of the program outcomes.

ECET 331

ECET is titled, "Electrical Generation and Transmission." As such it covers power systems topics including construction, operation, and steady-state analysis of the synchronous machine; calculation of resistance, inductance, and capacitance of transmission lines, analysis of transmission line performance; power flow; and fault studies. A detailed listing of the topics and the hours allocated to each topic is shown in Table 2.

Introduction, energy usage (1)	Transmission line capacitance calculation (2)
The power system (1)	Transmission line performance (3)
Power circuit fundamentals (1)	Power flow analysis (2)
Synchronous machine: construction &	Introduction to fault current analysis, three-
operation (1)	phase faults, circuit breakers, and fuses (4)
Sync. mach., equiv. Circuit/phasor diagram	Symmetrical components/sequence networks
(1)	(2)
Sync. Mach. performance and V-curves (1)	Effect of transformers on fault currents (1)
Synchronizing & transient performance (1)	Unbalanced fault calculations (2)
Load sharing between generators (1)	Relays, applications, & coordination (5)
Transformer equiv. ckts., and phase shift (1)	Over-voltage protection & surge arrestors (2)
Per-unit system & calculations (2)	Substation grounding (2 hrs)
Phase-shifting and autotransformers (1)	Summary and review (1 hr)
Transmission line construction, resistance (1)	Exams (3)
Transmission line inductance calculation (2)	

Table 2.Topics Covered and Hours Allotted in ECET 331

One problem, of course, with this type of material is the laboratory. At Purdue, we are fortunate to have some small synchronous machines, transmission line simulators, phase-shifting transformers, and other equipment. As a result approximately seven hardware-based lab experiments were developed for the course, which still left eight weeks in the semester. Over the last decade, these weeks were filled with computer studies utilizing various freeware packages, as well as an academic version of PowerWorld.⁵⁻⁷

PowerWorld began as a power flow program, but has evolved into a much more comprehensive power systems analysis package. In particular, it is also capable of conducting a variety of fault studies, both balanced and unbalanced. As a result, I decided to replace the several independent computer analyses with a more comprehensive project, using PowerWorld, that would take the student through the process of putting data into proper form, conducting power flow studies and correcting the system where required, conducting fault analysis studies, and documenting their work in a comprehensive report. The inclusion of this project makes a strong case for helping the students to meet program outcomes dealing with written communication, design, and data analysis. The project was done during the laboratory portion of the course, with outside time for writing up the results. Regular homework assignments were still done throughout the six weeks of the project.

The Power System Analysis Project

Figure 1 shows a one-line diagram of the system that was chosen for the project. It was taken from the course textbook.⁹ Table 3 shows the parameters and other system modeling information for the system. This system was chosen for several reasons. Obviously it was in the textbook, but it also is complex enough to demonstrate some of the problems that occur in a power system, while being simple enough to allow some calculations by hand to verify the computer results. In addition, the free trial/academic version of PowerWorld is limited to twelve buses. This means the students can download the current version and use it at home as well as in the lab.



Figure 1. System One-Line Diagram for Power Systems Analysis Project

Observation of the system data shows that it is in a variety of forms; i.e., some of the data are in actual values, some in per-unit, and some in ohms/km. Thus, the first week's assignment for the students was to create data tables putting the data into the form required for the PowerWorld program. These tables were turned in as an interim product, so they could be verified. Errors were annotated and the students were required to fix them before continuing. Tables A1 to A7 in the Appendix show the results expected from week 1. Once the data was in the proper form, students were required to manually calculate Ybus, neglecting the charging capacitance of the transmission lines, which is very straightforward.⁹

During the second week, students entered their system into PowerWorld. PowerWorld is a graphically-oriented program that allows the user to draw the system and enter data via dialog boxes. Figure 2 shows the solved case for the given system. Looking at the results in Figure 2, some problems are evident. First, transformer T1 is overloaded (for purposes of this study, it was assumed that it is permissible to load the transformers to 100%). Secondly, there are low voltages at all the buses except the generator buses. Finally, generator G2 is at its hard limit for providing reactive power and the bus voltage has dropped from the desired 1.0 to 0.97 pu.

$\frac{\text{Generator Information}}{\text{G1: 100 MVA 13.8 kV X''} = 0.12 \text{ mu}}$	<u>Transmission Line Ratings</u> $z_{t} = (0.08 \pm i.0.5) \Omega/km$
$X_2 = 0.14 \text{ pu}, X_0 = 0.05 \text{ pu}$	$z_0 = (0.2 + j 1.5) \Omega/km;$
G2: 200 MVA, 15.0 kV, X" = 0.12 pu,	$y_1 = 3.3(10)^{-6}$ S/km
$X_2 = 0.14 \text{ pu}, X_0 = 0.05 \text{ pu}$	Maximum $MVA = 400$ for all lines
Generator neutrals are solidly grounded	L1 = 15 km;
	L2 = 35 km;
Transformer Information	L3 = 40 km;
T1: 100 MVA, 13.8–230 kV, Δ–Y,	L4 = 15 km;
X = 0.1 pu	L5 = 50 km
T2: 200 MVA, 15.0–230 kV, Δ–Y,	
X = 0.1 pu	Power Flow Data
Transformer neutrals are solidly grounded	Bus 1: Slack bus
	Bus 2, 3, 4, 5, 6: Load buses
System Base Quantities	Bus 7: Generator (PV) bus; $V = 15 \text{ kV}$;
$S_{base} = 100 \text{ MVA} \text{ (three-phase)}$	P = 180 MW; -87 MVAR < Q < 87 MVAR
$V_{base} = 13.8 \text{ kV}$ (line-to-line) at Bus 1	

Table 3.System Parameters and Modeling Information



Figure 2. Power Flow Results for Base Case of System Shown in Figure 1

Once they had their power flow simulation running, students were required to verify that they

had entered their data correctly. PowerWorld provides a method to view Ybus, with and without charging capacitances, so students could compare their manually-calculated Ybus to the values shown in the program. Table A8 shows the positive-sequence Ybus values. Once their results were verified, students were assigned the task of correcting the system deficiencies. In particular, no device was to be loaded at greater than 100% of its rating and all bus voltages were to be greater than 0.95 pu.

In looking at the system as originally simulated, it is evident that the low voltages are due to the large lagging loads at buses 2 through 6. Thus, one possible method to correct the system deficiencies would be to add one or more capacitor banks. Figure 3 shows the power flow results for the system, with the addition of a large capacitor bank at bus 4. With this addition, both transformers are below 100% load and all of the system voltages are greater than 0.96 pu. Some students put in more than one bank and some used larger amounts.



Figure 3. Power Flow Results for Corrected System

The third week of the project was devoted to balanced, three-phase fault analysis. PowerWorld provides a variety of options for fault analysis, as shown in Figures 4 and 5. Figure 4 shows the main setup tab for fault analysis. As shown, a balanced, three-phase fault has been selected at bus 1. Figure 5 shows the "Fault Options" tab. This tab allows the insertion of fault impedance (zero was used here). A "Flat Classical" pre-fault profile was chosen, along with line charging set to zero. These choices make it relatively simple for the student to verify the calculations. As is well known, the three-phase fault currents are obtained by dividing the pre-fault voltage (1.0 pu in this case) by the diagonal elements of Zbus, which are themselves the Thevenin

oose the Fau	ulted Bus		Fault Locatio	n Fault Type	e	Data Type Sho	wn		Fault Current	t
Sort by Nam	e e s	ort by Number	Bus Fault	C Line-tr	Line-to-Ground o-Line	Current Units	C Amps		Magnitude:	
() [13.8 KV]) USC A	ea/20ne rinter:	C In-Line Fi	ault C Double	se Balanced e Line-to-Ground	Oneline Display		Phase C	11.874 p.u.	
(3) [230 KV	1		Location *	Vo			C Dhave D		-89.30	dea.
(4) [230 KV (5) [230 KV]] Generators	Loads Swit	rched Shunts			(All Phases	i Phase b		10000	
(4) [230 KV (5) [230 KV (5) [230 KV (5) Lines	Generators	Loads Swit	Ched Shunts	Phase Volt B	Phase Volt C	Phase Ang A	Phase Ang B	Phase Ang C		
(4) [230 KV (5) [230 KV (5) [230 KV Lines Nu 1	Generators	Loads Swit	Ched Shunts Phase Volt A 0.00000	Phase Volt B 0.00000	Phase Volt C 0.00000	Phase Ang A 0.00	Phase Ang B 0.00	Phase Ang C 0.00		
(4) [230 KV (5) [230 KV (5) [230 KV Lines Nu 1 2	Generators	Loads Swit	thed Shunts Phase Volt A 0.00000 0.35429	Phase Volt B 0.00000 0.35429	Phase Volt C 0.00000 0.35429	Phase Ang A 0.00 2.34	Phase Ang B 0.00 -117.66	Phase Ang C 0.00 122.34		
(4) [230 KV (5) [230 KV es Lines Nu 1 2 3	Generators J Generators J 2 2 3 3	Loads Swit	Ched Shunts Phase Volt A 0.00000 0.35429 0.40461	Phase Volt B 0.00000 0.35429 0.40461	Phase Volt C 0.00000 0.35429 0.40461	Phase Ang A 0.00 2.34 1.20	Phase Ang B 0.00 -117.66 -118.80	Phase Ang C 0.00 122.34 121.20		
(4) [230 KV (5) [2	Generators Josephine Josephine Josephin Josephine Josephine Josephine Josephine Josep	Loads Swit	♥ 0 € wheed Shunts 0.00000 0.35429 0.40461 0.52242	Phase Volt B 0.00000 0.35429 0.40461 0.52242	Phase Volt C 0.00000 0.35429 0.40461 0.52242	Phase Ang A 0.00 2.34 1.20 -0.60	Phase Ang B 0.00 -117.66 -118.80 -120.60	Phase Ang C 0.00 122.34 121.20 119.40		
(4) [230 KV (5) [2	Generators Jmber 2 2 3 3 4 4 5 5	Loads Swit	■ 0 ◆ Index Shunts Phase Volt A 0.00000 0.35429 0.40461 0.52242 0.58669	Phase Volt B 0.00000 0.35429 0.40461 0.52242 0.58669	Phase Volt C 0.00000 0.35429 0.40461 0.52242 0.58669	Phase Ang A 0.00 2.34 1.20 -0.60 -1.28	Phase Ang B 0.00 -117.66 -118.80 -120.60 -121.28	Phase Ang C 0.00 122.34 121.20 119.40 118.72		
(4) [230 KV (5) [2	Generators amber 2 2 3 3 4 4 5 5 6 6	Loads Swit	■ 0 ◆ Indextex Shunts Phase Volt A 0.00000 0.35429 0.40461 0.52242 0.58669 0.61081	Phase Volt B 0.00000 0.35429 0.40461 0.52242 0.58669 0.61081	Phase Volt C 0.00000 0.35429 0.40461 0.52242 0.58669 0.61081	Phase Ang A 0.00 2.34 1.20 -0.60 -1.28 -1.49	Phase Ang B 0.00 -117.66 -118.80 -120.60 -121.28 -121.49	Phase Ang C 0.00 122.34 121.20 119.40 118.72 118.51		

Figure 4. PowerWorld Fault Analysis Dialog Box

oad Data Sav	x : 0.00000		Pre Fault Profile C Solved Power Fl C Flat IEC-909 Flat Classical	low Frof	file Options C Parameters Nage 1.000 en. PF 0.00	0 pu deg	Image: Figure 1 Image: Figure 1 Image: Figure 1 Image: Figure 1	ratios set to 1.0 ging set to 0 ents ence 0 ences 0		
L1 From Bus	L1 To Bus	L1 Ckt ID	L2 From Bus	L2 To Bus	L2 Ckt ID	Mutual R	Mutual X	L1 Mut. Start	L1 Mut. End	L2 Mut. St
	Defined				-				÷	
None										

Figure 5. PowerWorld Fault Options Dialog Box

equivalents at each bus. Students were required to calculate the Thevenin equivalent at several buses and compare them to the Zbus values. PowerWorld does not display the values of Zbus, but it does allow the export of Ybus values as a MatLab m-file. The Ybus m-file was entered into MatLab and the inverse was found to yield the Zbus values. Table A12 in the appendix shows the positive-sequence Zbus. Once they had agreement between their manual calculations and Zbus, students were required to calculate the fault currents using the diagonal elements of Zbus and compare them to the values produced by PowerWorld. When done correctly, the values agree almost exactly. This process helps the student to understand how the program calculates fault currents, and provides methods for them to check their results. Note, for

example, Zbus(1,1) in Table A12 has a value of (0.001 + j0.0842). Putting that into polar form yields a magnitude of 0.0842, and taking the inverse yields a fault current of (1/0.0842) = 11.876, which compares almost exactly to the value of 11.874 shown in the upper right corner of Figure 4.

The final, and probably most complex, in-lab part of the project was to conduct unbalanced fault analyses at every bus in the system for each of the three unbalanced fault types shown in Figure 4. Calculation of unbalanced faults by hand requires the use of symmetrical components; however, the fault is relatively easy to calculate by hand, once the Thevenin equivalent impedances are known for the positive, negative, and zero sequence circuits. For this part of the project, the students were required to draw the sequence circuits and then to calculate the Thevenin impedance at bus 4. As shown in Tables A10 and A11, PowerWorld provides Ybus matrices for the negative and zero sequence circuits. The students exported those to MatLab mfiles and calculated Zbus for each sequence. Table A13 and A14 show the negative and zero sequency Zbuses. With these, they were able to compare their manually-calculated values with the computer-derived values. They also were able to calculate the line-to-neutral unbalanced fault current at bus 4 and compare it to the PowerWorld results. As with the balanced fault, very good comparisons resulted when the proper procedures were followed. Once verified, the students ran PowerWorld to calculate all three unbalanced faults at each bus. As shown in Figure 4, PowerWorld shows the voltages at each bus in the box at the bottom. In addition, there are tabs for generators and lines, which show the currents in each phase for each device. The students were directed to export all of the data to Excel for use in their project report. For the final portion of the project, students were required to select oil circuit breakers from a table in the textbook to protect the transmission lines.

From the work done in lab, students should learn how to analyze their data and to verify it, which contributes directly to ABET outcomes. In correcting the power system, they made design decisions, also contributing to ABET outcomes. To complete the project, the students were required to write a formal report, which accounted for about 40% of their lab grade. Since the lab counts 25% of the course grade, the report essentially counted for 10% of the final grade.

The Final Report

Table 4 shows the requirements given to the students for their final report. The reports were graded for both technical content and grammar/format. Due to the constant checks made along the way, virtually every student (about 50 in the last two years) was able to obtain data that was reasonably close to my values. The format was specified in some detail to insure that the students knew what was expected of them. Nevertheless, there was significant variation in the level of completeness and adherence to the specified format. Having the students do a detailed, lengthy report contributes directly to the ABET requirement for communication skills, and samples of the reports were used in the displays for our ABET visit.

Table 4.Requirements for Final Report

Prepare a **formal**, final report for the power system analysis project (labs 8 through 12). It should incorporate all of the work done in those labs. All figures should have figure numbers and captions and tables should have table numbers and captions. The report should be organized as follows:

Introduction:

Describe the system being studied (note a jpg image of the system can be downloaded from the 331 webpage) and the studies that were to be performed using PowerWorld.

Procedures:

Describe what you did for the power flow study (labs 8 and 9) and for the fault analysis study (labs 10 - 12). Describe how you prepared your data for entry into PowerWorld, and include your data tables.

Results:

Provide results from the power flow study of the base system with figures and tables.

Provide results of the fault studies. Include a table of fault currents showing the fault current for each type of fault at each bus, and include the all of the short-circuit analysis results as an appendix to your report.

Analysis and Recommendations:

Describe deficiencies noted from the original power flow and your suggestions to correct them. Show the results of your corrected system. Include appropriate figures and tables.

Indicate circuit breaker sizes and ratings as requested in lab 11. Explain your reasoning in selecting the size and ratings that you chose.

Calculations and Verification:

Include your sequence diagrams for the system and show the comparison of your manually-calculated Ybus values to those obtained from Power World. Using your sequence diagrams, calculate the Thevenin equivalent impedances in all three sequences from bus 4 to ground. Compare your values to those you obtained from MatLab. Calculate the three-phase fault current at bus 4 using the positive-sequence Thevenin impedance and calculate the line to ground fault current at bus 4 using the Thevenin impedances of all three sequences. Compare your results to the data from table 1.

Summary and Conclusions

Describe your confidence in your simulation of the power system; e.g., would you go to your boss and present a case for installing capacitor banks or upgrading the circuit breakers because of the results you obtained.

Appendices

Conclusion

With the availability of a powerful power systems analysis package in a free demonstration/ education version, it has become practical to implement a detailed power systems analysis project in an introductory power systems course. The project reported in this paper required students to correctly assemble their data, enter it into PowerWorld, conduct power-flow studies, correct the power system to overcome observed deficiencies, conduct balanced and unbalanced fault analyses, and to write a detailed final report on the project. Throughout the project students were required to verify the information coming from the simulation by comparing it to manual calculations. This helped to insure they were obtaining the correct results and taught them to verify computer results as much as possible. This multi-week project and final report contribute directly to several ABET outcomes including system design, analysis of data, and communication skills. For future offerings, it might be desirable to have the students do economic evaluations of alternatives to correct the power system, such as comparison of the cost of a new line versus capacitor banks.

References

- 1. ABET, Inc.. (2005). CRITERIA FOR ACCREDITING ENGINEERING PROGRAMS, Effective for Evaluations During the 2006-2007 Accreditation Cycle. Baltimore, MD.
- 2. ABET, Inc.. (2005). CRITERIA FOR ACCREDITING ENGINEERING TECHNOLOGY PROGRAMS, Effective for Evaluations During the 2006-2007 Accreditation Cycle. Baltimore, MD.
- 3. Skvarenina, T.L. and DeWitt, W.E. (1998). Development of an EET Electrical Power and Controls Course, ASEE Annual Conference Proceedings (CDROM)
- 4. DeWitt, W. and Skvarenina, T. (1995) "An EET Industrial Power Distribution Course, ASEE Annual Conference Proceedings, 1418-1421
- 5. Overbye, T.J., Sauer, P.W., Marzinzik, and C.M., Gross, G. (1995). A user-friendly simulation program for teaching power system operations. IEEE Transactions on Power Systems. Vol 10, #4. Pages 1725 1733.
- 6. Overbye, T.J., Gross, G., Laufenberg, and M.J., Sauer, P.W. (1997). Visualizing power system operations in an open market. IEEE Computer Applications in Power. Vol 10, #1, Pages 53 58.
- Overbye, T.J. and Weber, J.D. (2001). Visualizing the electric grid. IEEE Spectrum. Vol 38, #2. Pages 52 -58.
- 8. Skvarenina, T.L. (2000). Developing a Department-Wide Learning Assessment Program. Frontiers in Education, 2000 Conference Proceedings, pages T1A-11 to T1A-16 (6 pages)
- 9. Glover, J. and Sarma, M. (2002). Power System Analysis and Design, 3rd Edition. Brooks/Cole. Pacific Grove, California.

Appendix

Table A1. Bus Data, Actual Values

Bus	Туре	kV	delta	Gen P (MW)	Gen Q	Load P	Load Q	Gen Min	Gen Max
					(MVAR)	MW	MVAR	MVAR	MVAR
1	Slack	13.8							
2	Load	230				50	30		
3	Load	230				50	30		
4	Load	230				50	30		
5	Load	230				50	30		
6	Load	230				50	30		
7	Gen	15		180					

Table A2. Bus Data, Per-Unit Values

Bus	Туре	V PU	delta	Gen P (PU)	Gen Q	Load P	Load Q PU	Gen Min Q	Gen Max
					(PU)	PU		PU	Q PU
1	Slack	1	0						
2	Load					0.5	0.3		
3	Load					0.5	0.3		
4	Load					0.5	0.3		
5	Load					0.5	0.3		
6	Load					0.5	0.3		
7	Gen	1		1.8					

Table A3. Bus Knowns and Unknowns

Bus	Input Data	Unknowns
1	V & delta	Pgen & Qgen
2	P & C	V & delta
3	P & C	V & delta
4	P & C	V & delta
5	P & C	V & delta
6	P & C	V & delta
7	V & Pger	Q & delta

Table A4. Transmission Line Parameters, Actual Values

		Series Imped	ance	Shur				
Bus-Bus	length	R ohms	X ohms	G (S)	B (S)	Max MVA		
2-3	15	1.2	7.5		0.0000495	400		
3-4	35	2.8	17.5		0.0001155	400		
4-5	40	3.2	20		0.000132	400		
4-6	15	1.2	7.5		0.0000495	400		
5-6	50	4	25		0.000165	400		

		Series Imped	ance	Shur		
Bus-Bus	length	R ohms	X ohms	G (S)	B (S)	Max MVA
2-3	15	0.002268	0.01418		0.02619	4
3-4	35	0.005293	0.03308		0.06110	4
4-5	40	0.006049	0.03781		0.06983	4
4-6	15	0.002268	0.01418		0.02619	4
5-6	50	0.007561	0.04726		0.08729	4

Table A5. Transmission Line Parameters, Per-Unit Values

Table A6. Transformer Parameters, Actual Values

	Equiv. Imp	bedance	Shunt	Admittance		
Bus-Bus	R ohms	X ohms	G (S)	B (S)	Max MVA	Max tap
1-2	0	52.9			100	
6-7	0	52.9			200	

Table A7. Transformer Parameters, Per_unit Values

	Equiv. Impedance Shunt Admittance					
Bus-Bus	R ohms	X ohms	G (S)	B (S)	Max MVA	Max tap
1-2	0	0.1			1	
6-7	0	0.1			2	

Table A8.	ole A8. Positive Sequence Ybus									
Number	Bus 1	Bus 2	Bus 3	Bus 4	Bus 5	Bus 6	Bus 7			
1	0.00 - j18.33	0.00 + j10.00								
2	0.00 + j10.00	11.01 - j78.76	-11.01 + j68.76							
3		-11.01 + j68.76	15.72 - j98.24	-4.71 + j29.48						
4			-4.71 + j29.48	12.14 - j75.90	-4.13 + j25.79	-3.3 + j20.63				
5				-4.13 + j25.79	15.13 - j94.55	-11.01 + j68.76				
6				-3.3 + j20.63	-11.01 + j68.76	14.31 - j109.39	0.00 + j20.00			
7						0.00 + j20.00	0.00 - j36.67			
Table A9.	ble A9. Negative Sequence Ybus									
Number	Bus 1	Bus 2	Bus 3	Bus 4	Bus 5	Bus 6	Bus 7			
1	0.00 - j17.14	0.00 + j10.00								
2	0.00 + j10.00	11.01 - j78.76	-11.01 + j68.76							
3		-11.01 + j68.76	15.72 - j98.24	-4.71 + j29.48						
4			-4.71 + j29.48	12.14 - j75.90	-4.13 + j25.79	-3.3 + j20.63				
5				-4.13 + j25.79	15.13 - j94.55	-11.01 + j68.76				
6				-3.3 + j20.63	-11.01 + j68.76	14.31 - j109.39	0.00 + j20.00			
7						0.00 + j20.00	0.00 - j34.29			
Table A10.	Zero Sequen	ce Ybus								
Number	Bus 1	Bus 2	Bus 3	Bus 4	Bus 5	Bus 6	Bus 7			
1	0.00 - j20.00	0.00 + j0.00								
2	0.00 + j0.00	3.08 - j33.10	-3.08 + j23.10							
3		-3.08 + j23.10	4.40 - j33.00	-1.32 + j9.90						
4			-1.32 + j9.90	3.40 - j25.49	-1.16 + j8.66	-0.92 + j6.93				
5				-1.16 + j8.66	4.23 - j31.76	-3.08 + j23.10				
6				-0.92 + j6.93	-3.08 + j23.10	4.00 - j50.03	0.00 + j0.00			
7						0.00 + j0.00	0.00 - j40.00			

Table A12.Positive-Sequence Zbus Values

0.001 + 0.0842j 0.0019 + 0.0544j 0.0013 + 0.0501j0 + 0.0402j-0.0007 + 0.0348j -0.001 + 0.0328j -0.0005 + 0.0179j0.0019 + 0.0544j 0.0034 + 0.0997j 0.0024 + 0.0919j 0 + 0.0737j-0.0013 + 0.0639j -0.0018 + 0.0601j -0.001 + 0.0328j $0.0013 + 0.0501j \quad 0.0024 + 0.0919j \quad 0.0035 + 0.0978j \quad 0.0008 + 0.0785j \quad -0.0007 + 0.068j \quad -0.0013 + 0.064j \quad -0.0007 + 0.0349j \quad -0.0013 + 0.064j \quad -0.0007 + 0.0349j \quad -0.0007 + 0$ $zbus_1 =$ 0 + 0.0402j0 + 0.0737j0.0008 + 0.0785j 0.0025 + 0.0896j 0.0006 + 0.0776j -0.0001 + 0.0731j -0 + 0.0399j -0.0007 + 0.0348j - 0.0013 + 0.0639j - 0.0007 + 0.068j0.0006 + 0.0776j 0.0022 + 0.0883j 0.0005 + 0.0781j 0.0003 + 0.0426j-0.001 + 0.0328j - 0.0018 + 0.0601j - 0.0013 + 0.064j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0001 + 0.0731j - 0.0005 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0004 + 0.0436j - 0.0004 + 0.0436j - 0.0004 + 0.0436j - 0.0008 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0008 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0008 + 0.0781j - 0.0008 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0008 + 0.0781j - 0.0008 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0008 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0008 + 0.0781j - 0.0008 + 0.0781j - 0.0008 + 0.0799j - 0.0004 + 0.0436j - 0.0008 + 0-0.0005 + 0.0179j -0.001 + 0.0328j -0.0007 + 0.0349j0.0003 + 0.0426j 0.0004 + 0.0436j 0.0002 + 0.051j -0 + 0.0399j

Table A13.Negative-Sequence Zbus Values

```
zbus_2 = \begin{pmatrix} 0.00119 + 0.09469i & 0.00204 + 0.06229i & 0.00143 + 0.05768i & -0 + 0.04692i & -0.00078 + 0.04106i & -0.00107 + 0.03887i & -0.00062 + 0.02267i \\ 0.00204 + 0.06229i & 0.0035 + 0.10677i & 0.00245 + 0.09886i & -0 + 0.08042i & -0.00134 + 0.07038i & -0.00184 + 0.06662i & -0.00107 + 0.03885i \\ 0.00143 + 0.05768i & 0.00245 + 0.09886i & 0.00353 + 0.10468i & 0.00076 + 0.08517i & -0.00076 + 0.07455i & -0.00131 + 0.07057i & -0.00077 + 0.04116i \\ -0 + 0.04692i & -0 + 0.08042i & 0.00076 + 0.08517i & 0.00253 + 0.09625i & 0.00061 + 0.08428i & -0.0001 + 0.07979i & -0.00006 + 0.04654i \\ -0.00078 + 0.04106i & -0.00134 + 0.07038i & -0.00076 + 0.07455i & 0.00061 + 0.08428i & 0.00221 + 0.09498i & 0.00056 + 0.08481i & 0.00032 + 0.04946i \\ -0.00107 + 0.03887i & -0.00184 + 0.06662i & -0.00131 + 0.07057i & -0.0001 + 0.07979i & 0.00056 + 0.08481i & 0.00022 + 0.02869i & 0.00048 + 0.05056i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0.04946i & 0.00048 + 0.05056i & 0.00028 + 0.05865i \\ -0.00062 + 0.02267i & -0.00107 + 0.03885i & -0.00077 + 0.04116i & -0.00066 + 0.04654i & 0.00032 + 0
```

Table A14.Zero-Sequence Zbus Values

zbus ₀ =	0.05i	0	0	0	0	0	0
	0	0.00213 + 0.07285i	0.0015 + 0.06118i	0.00002 + 0.03395i	-0.00078 + 0.01913i	-0.00108 + 0.01357i	0
	0	0.0015 + 0.06118i	0.00561 + 0.08712i	0.00196 + 0.04839i	-0.00003 + 0.02731i	-0.00076 + 0.01941i	0
	0	0.00002 + 0.03395i	0.00196 + 0.04839i	0.00647 + 0.08209i	0.00172 + 0.04641i	-0.00003 + 0.03302i	0
	0	-0.00078 + 0.01913i	-0.00003 + 0.02731i	0.00172 + 0.04641i	0.00483 + 0.073i	0.00036 + 0.04044i	0
	0	-0.00108 + 0.01357i	-0.00076 + 0.01941i	-0.00003 + 0.03302i	0.00036 + 0.04044i	0.00052 + 0.04321i	0
	0	0	0	0	0	0	0.025i)