

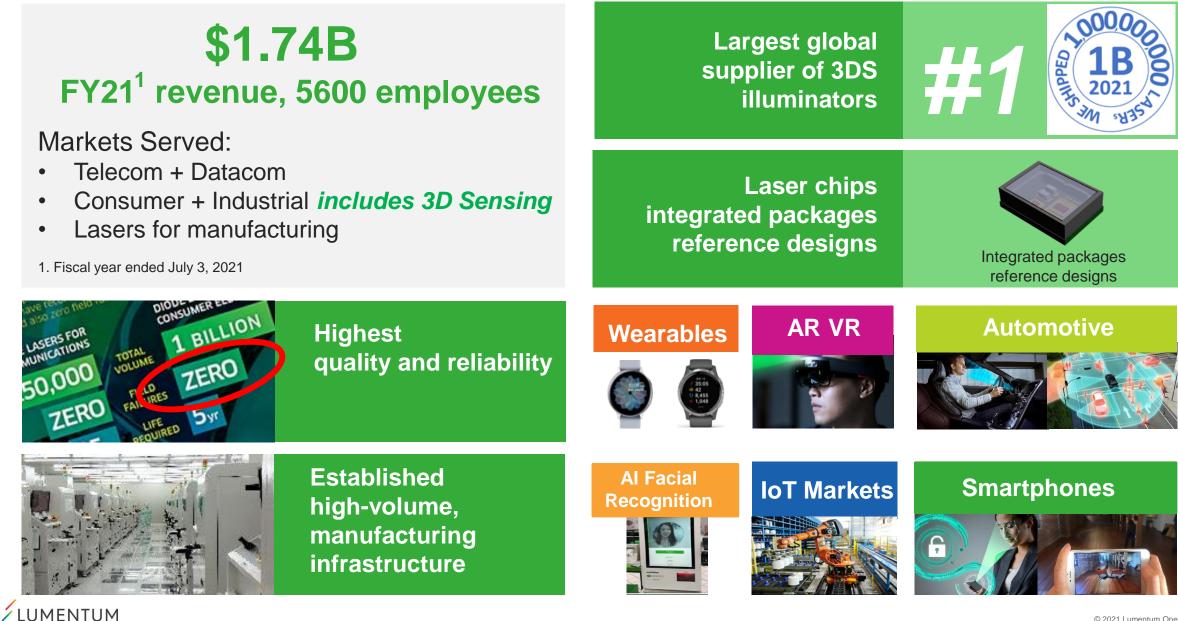
Addressable High-Performance Multi-Junction VCSEL Arrays for Advanced Sensing Applications

David Cheskis Eric Hegblom Matt Everett Ken Huang

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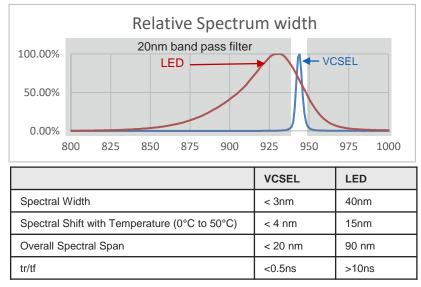
Lumentum – A leading 3D sensing optical solutions provider

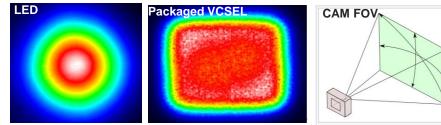


VCSELs enable advanced sensing systems

VCSEL benefits vs. LEDs

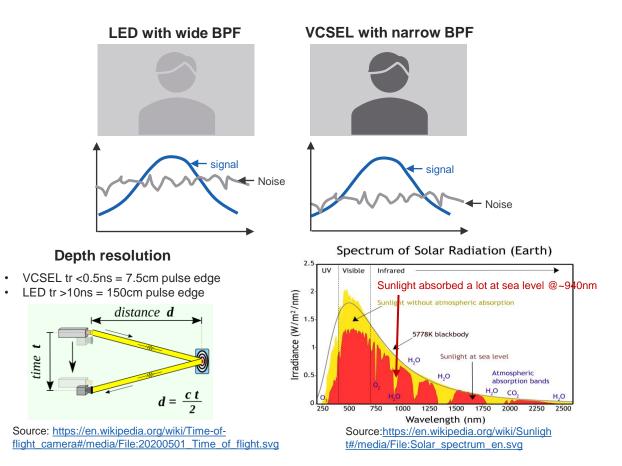
Narrow spectral band width, Spectral stability FOI flexibility & optimized beam shape fit to sensor FOV 20x faster rise and fall times





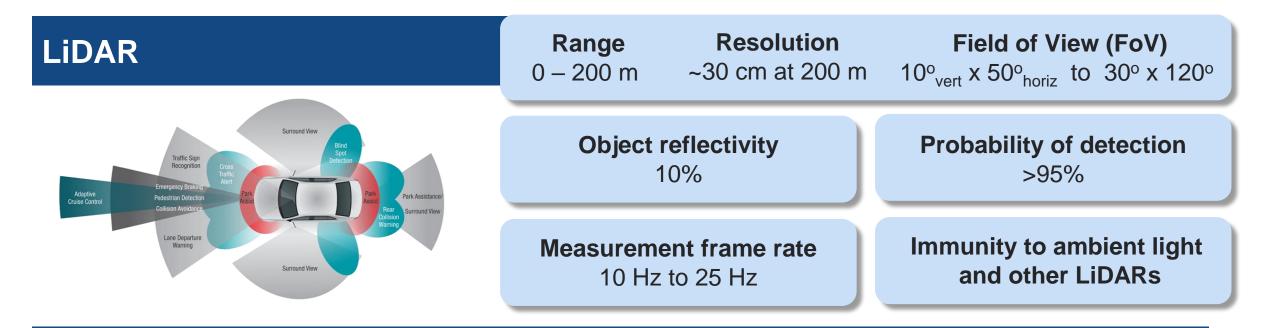
- → Allow narrow Band Pass Filter for depth quality improvement
- → Less light loss outside FOV for higher system efficiency
- → Improve precision of depth resolution

time



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Automotive applications



In-cabin

Occupancy Monitoring System/Driver Monitoring System (OMS/DMS)

Seatbelt fastened/unfastened

Passenger size and positioning

Facial recognition

Alert to check rear seat when exiting or entering the vehicles Detect movement after vehicle is parked or moved Monitor occupant biometrics like heart rate, temperature



Gesture recognition in-cabin controls



3D depth sensing systems brings value to AloT markets







Smart Building



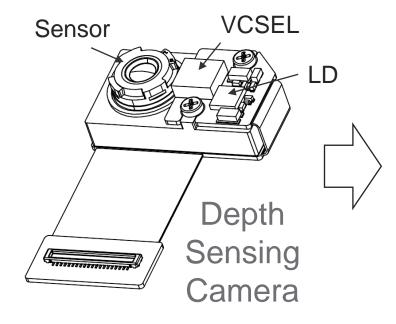
Smart Healthcare



Smart Retail



Smart Manufacturing

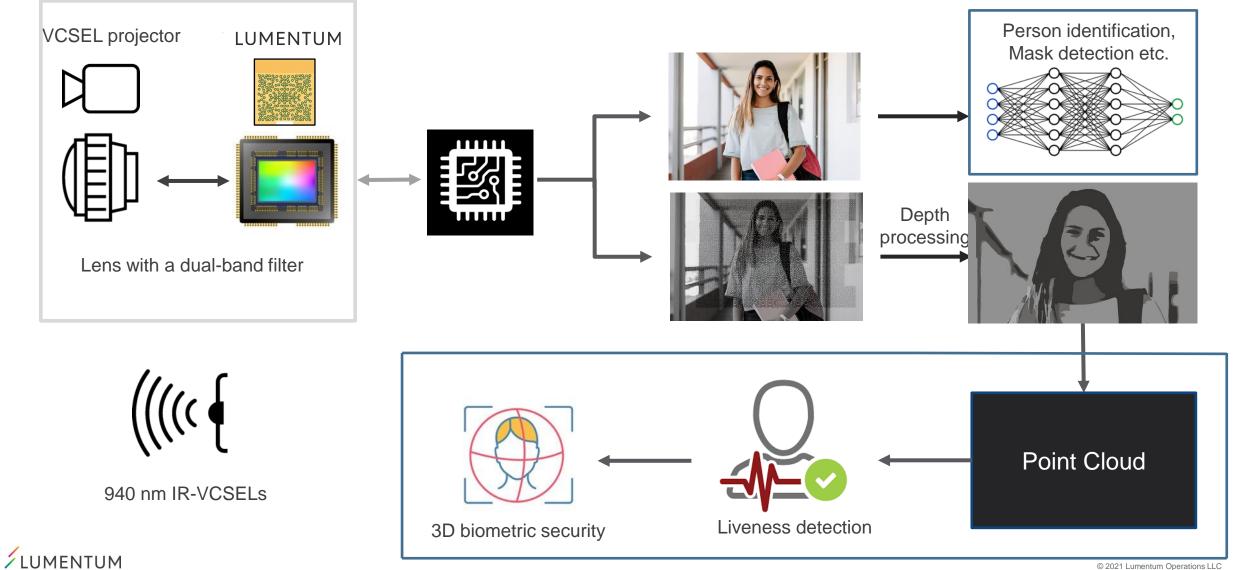


AR/VR Examples:

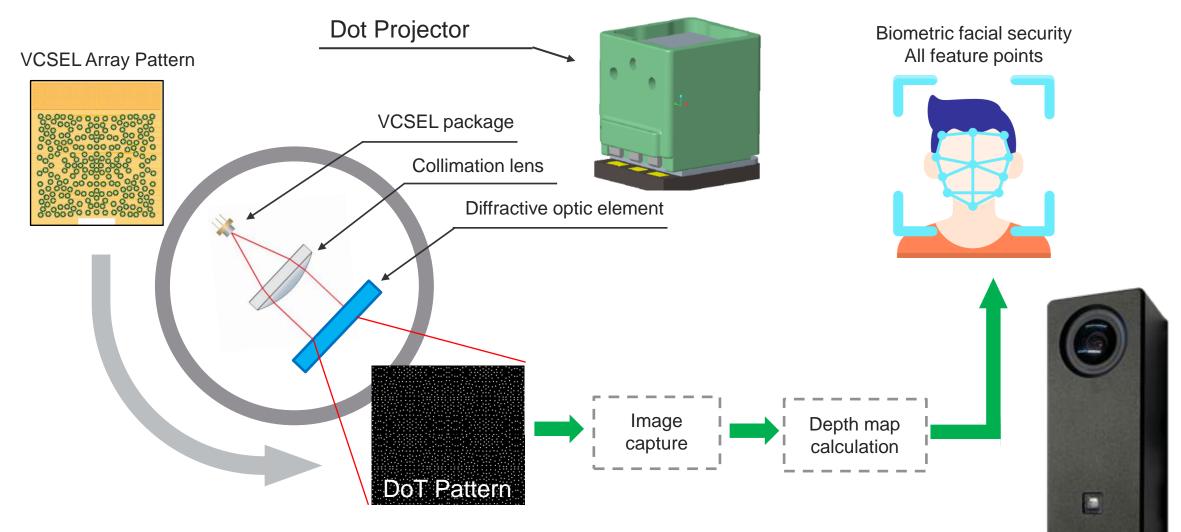
- Burberry launches AR tool with Google Search
- Lancôme use spatial computing for Immersive Shopping Event In Paris
- Museum of Future Experiences (MoFE) announces 'Liminality', curated VR pieces
- VR Experience to open at Perez Art Museum Miami
- 'New Comedy Show' takes place simultaneously in VR and in person
- IMVU teams up with emerging designers for Metaverse Fashion Show
- Inditex launches 'Pacific Game' a new AR game
- London's Design Museum showcases an AI-Designed AR Sneaker

Source from <u>https://www.forbes.com/sites/cathyhackl/2021/06/13/metaverse-weekly-60-minutes-</u> covers-the-metaverse-lancme-uses-spatial-computing-pacsun-on-roblox-and-more/?sh=3c32b72d3af2

Biometric facial security for access control



Illumination and biometric facial security using 3D sensing

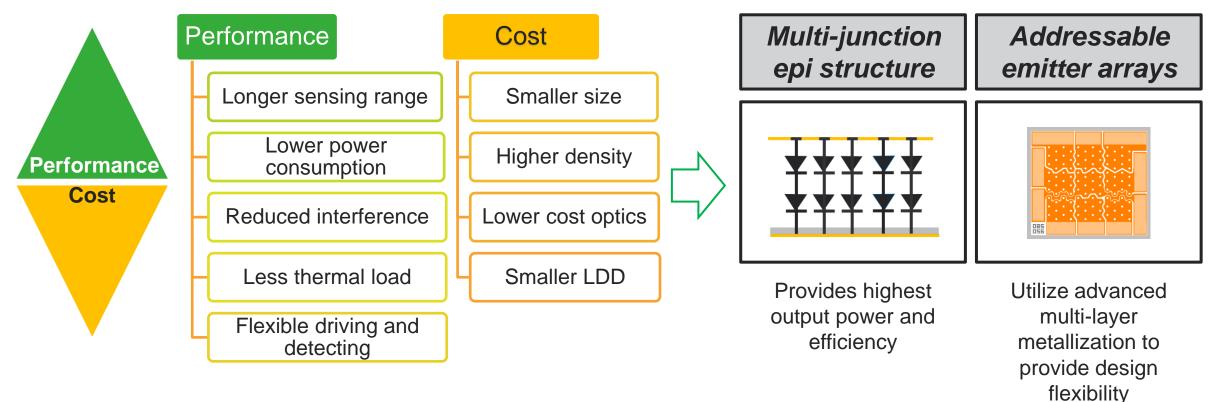




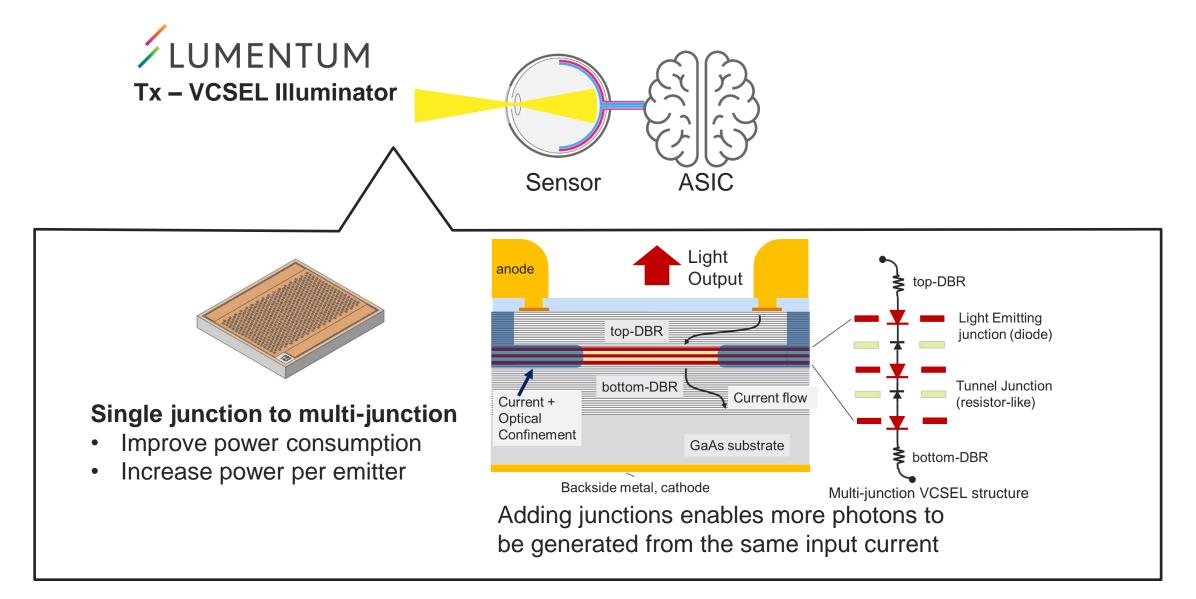
A VCSEL-based illuminator provides all the possibilities

Smart Illumination....

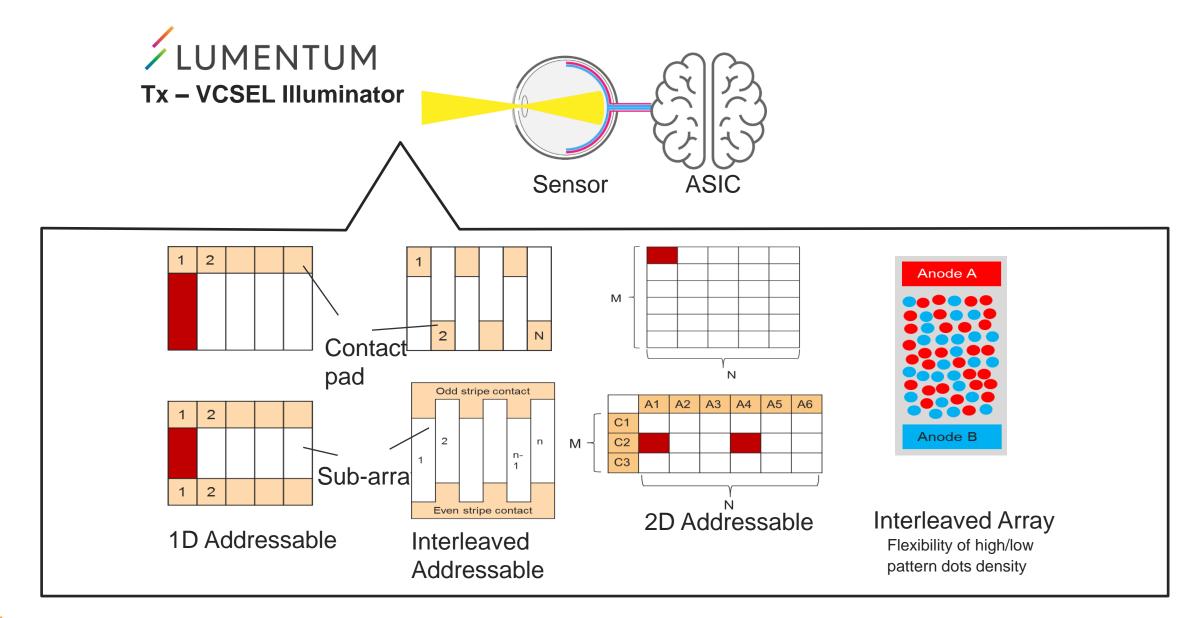
- > New solutions, new features, new applications enabled by laser innovations
 - Multi-junction VCSEL chips realize the same peak power but with small die size
 - Addressable emitters can be lit up more efficiently to reduce power consumption



Smart illumination – Multi-junction stack structure



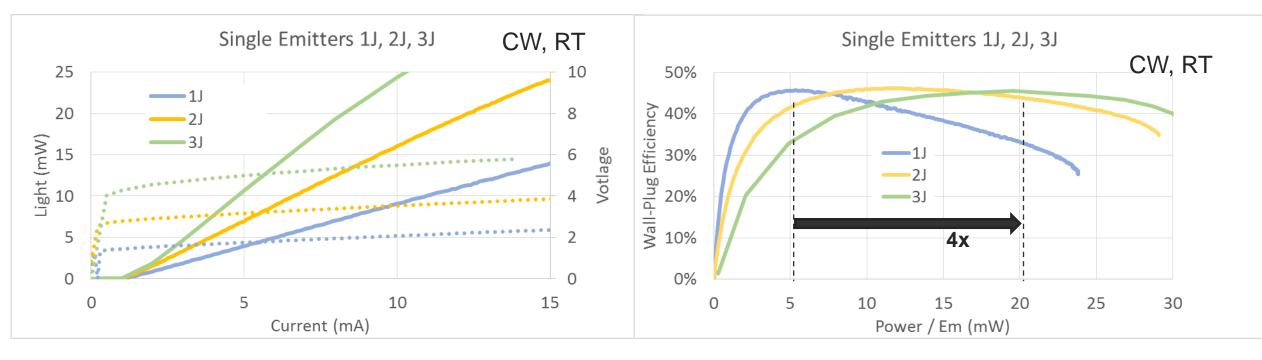
Smart illumination – Addressable emitters



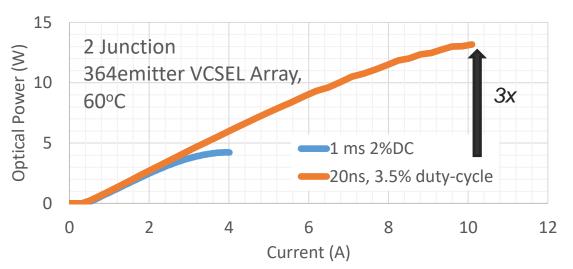


Motivation for multi-junction VCSELs and performance

Increasing peak power with multi-junction VCSEL design and short pulses



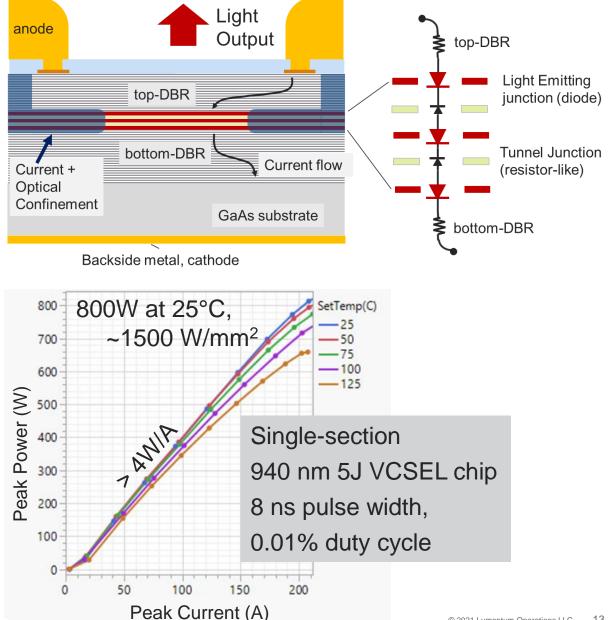
- More junctions increases voltage
- Lower current to reach same power
- Lower current -> Lower I²R losses -> Peak wall-plug efficiency extends to higher power
- Higher power purely from efficiency increase limited ... need to reduce heating with shorter pulses.



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Multi-junction for high power VCSELs

- More junctions -> More photons (higher output power) for the same input current.
- Higher power normally requires Higher current -> Higher I²R losses
- Adding more junctions improves efficiency of laser at higher output power enabling higher output power
- Reducing current improves driver efficiency
- At low (~<0.1% duty-cycle)
 - 200mW to 3W / emitter
 - High power density (100W/ mm^2 to 1.5 kW / mm^2)



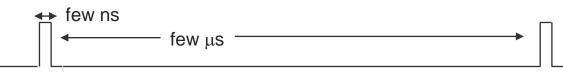
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LiDAR's need for high power density at low-duty cycle

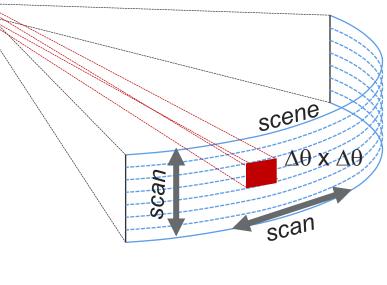
- For longer range (10 200m), Time-of-Flight LiDAR require higher power within narrow angular slices or blocks
- Higher power per angle or angle² means
 - Higher power / mm or
 - higher power / mm² at light source

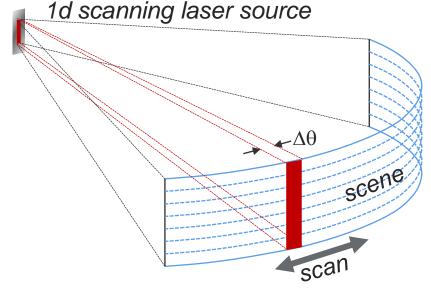
 $\Delta \theta = \Delta x / f$

- Power may be delivered at low ~<0.1% duty-cycle
 - Short, ns, pulses desired for reasonable z-resolution (1 ns round trip ~ $\Delta z = 0.15m$)
 - Need ~us intervals (1 us roundtrip ~ z = 150m away)

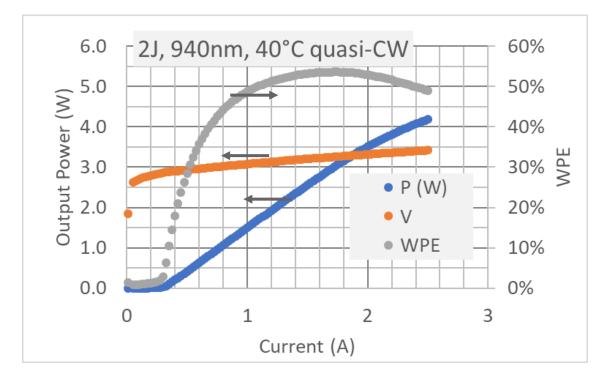


2d scanning laser source





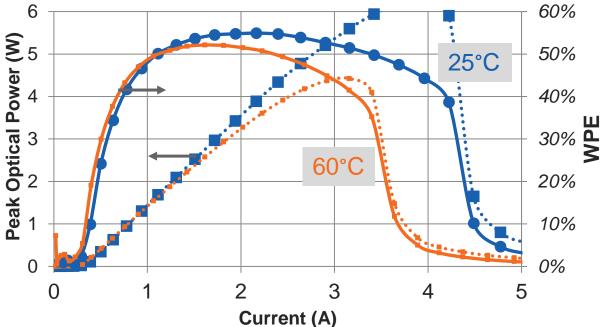
Aside ... 2J can improve efficiency even at high duty-cycle



55% Peak Wall-plug efficiency at room temperature

- Over 50% peak
 Wall-plug efficiency at 40°C qCW
- 2J better than best 1 J even in quasi-CW operation

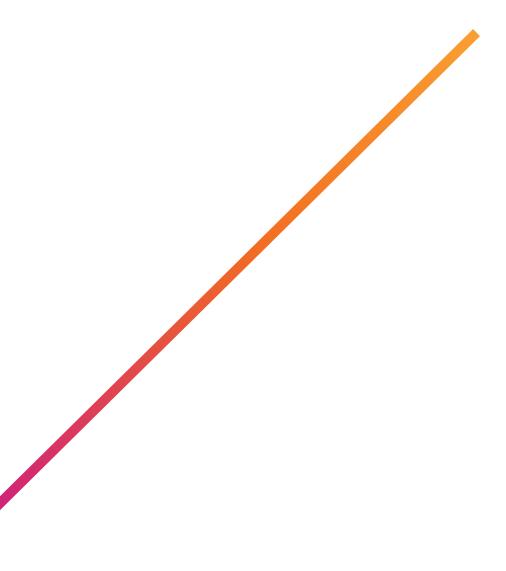




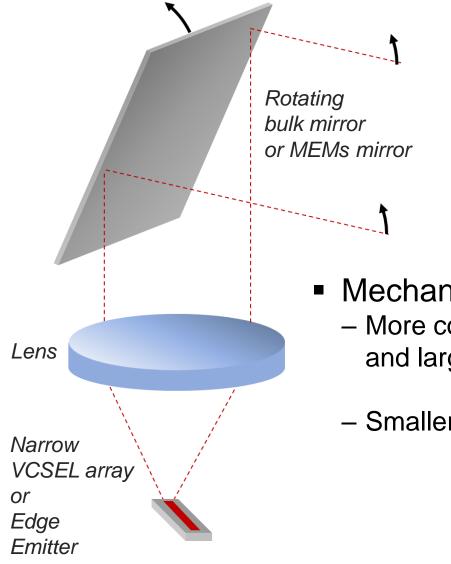




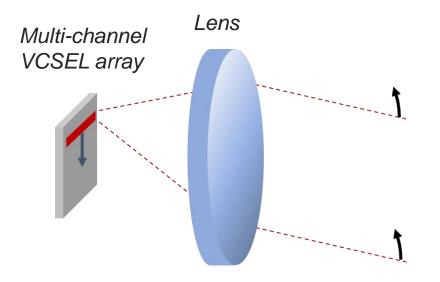
Scanning and chip geometry



Electronic vs. mechanical scanning



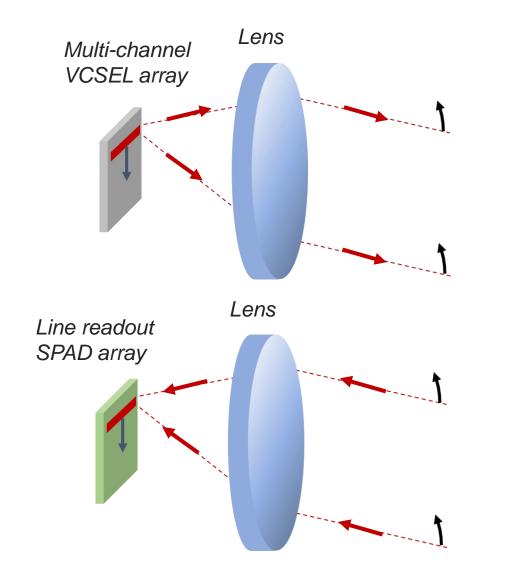
- Mechanical Scan:
 More complex, costly and larger optics
 - Smaller laser diode chip



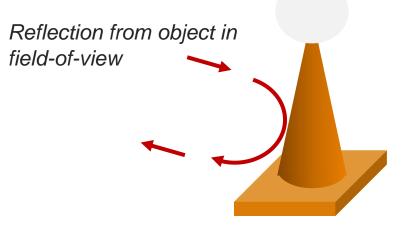
- All electronic scan LiDAR:
 - Simple, compact, robust optics
 - Already employed in consumer electronics for few meter range
 - Larger laser chip required vs.
 mechanical scan approach

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Electronic scan – matching to detector array



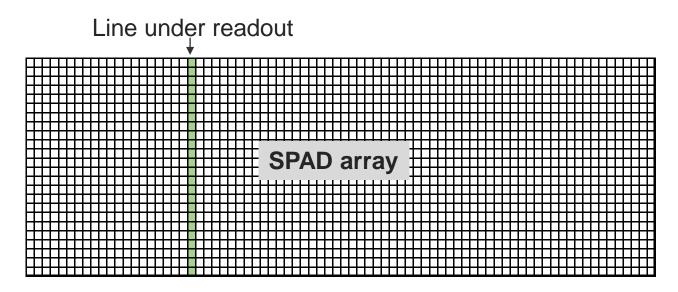
 All electronic scan requires match between VCSEL array and single photon avalanche detector (SPAD) array



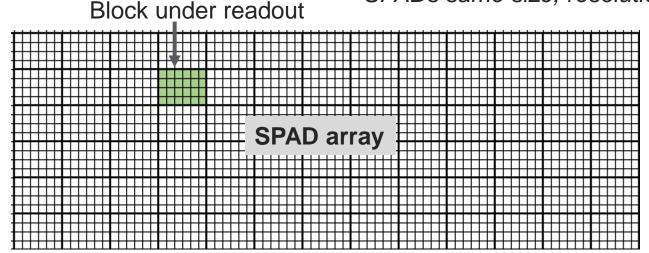


Matching to single-photon avalanche photodiode (SPAD) arrays

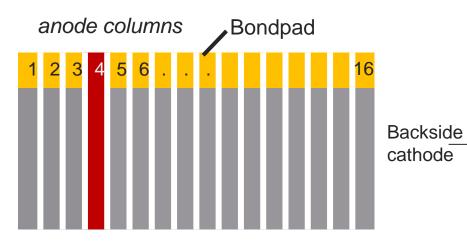
- SPAD arrays may have hundreds of columns
- VCSEL array also requires hundreds of channels
- Newer SPADs can readout in blocks rather than lines
- Block readout different from macro-pixels.
 Pixels not summed, but read-out individually



Both read-out zone same # of pixels. SPADs same size, resolution



Addressable VCSEL array geometries



Column addressable

- Typ. config front side anodes, backside cathode
- Conductive substrate

Pro / Cons:

- Simple fabrication
- Works with commonly available line read-out SPADs
- Large chips or many bond-pads often required
- Trade-off for width / number of channels
 - Narrow -> higher resistance, worse uniformity
 + better overlap with SPAD column
 - Wider -> lower resistance, better uniformity, worse overlap with SPAD column



- Frontside anode and cathodes overlapping
- Isolated substrate

Pro/Cons:

16

VCSEL

sub-array

3 4

• More complex fabrication, but can be smaller than column add. for similar number of channels

Bondpads

cathode

rows

B

С

D

• Requires detector have block read-out

anode columns

3

4

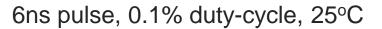
4C

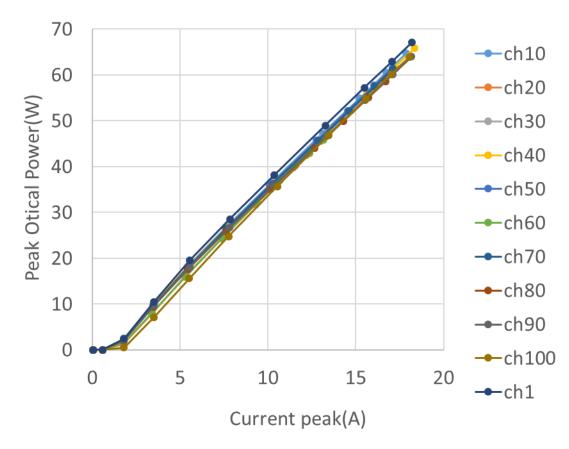
- Current flows over wider path, better uniformity over sub-array
- More efficient with optical power, enables longer distance
- Fewer contact pads (M+N) pads for M x N sub-arrays

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High-power 1D addressable array

- Id addressable chip
- 100 channels
- Chip size 16mm x 4.5mm > 70mm²
- 940nm, 5 Junction design







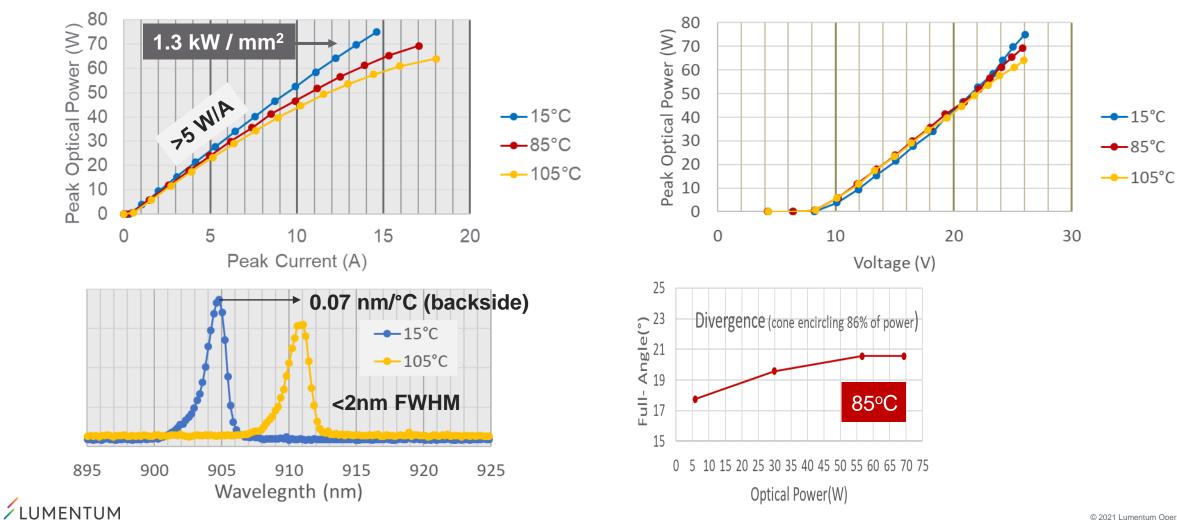


Matrix addressable performance



Results 2D array, individual sub-array (in array of ~200 elements)

- 905nm, 6 Junction design
- ~7ns pulse, 0.1% duty-cycle.
- Sub-array size ~0.05 mm² Overall chip ~12mm²



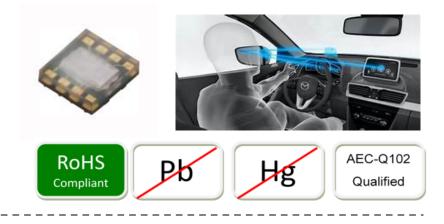
Reliability is a key Lumentum VCSEL difference



- Lumentum's Kaizen quality culture
- Submarine proven fiber optical component history
- >900M units of VCSEL chips shipped, zero field failures!

 VCSEL module for automotive in-cabin applications completed AEC-Q102 qualification

 Qualified in a packaged solution





IATF 16949-2016 certified VCSEL fab



For more information on automotive LiDAR VCSEL technology...





Summary

- The latest generation of VCSEL chips are the light sources of choice for advanced sensing solutions
- Matrix addressability opens additional options for LiDAR architectures
- Higher peak power densities using multi-junction epitaxial materials enable
 > 1kW/mm² (low-duty-cycle, few ns pulse) permit extension to longer distances
- VCSEL development has advanced rapidly as high-volume applications drive innovation and infrastructure
- Lumentum is focused on advancing VCSEL technology, the manufacturing platform, products, and integration
- Ecosystem partners are established for module integrators, detector solutions, and electronic integration



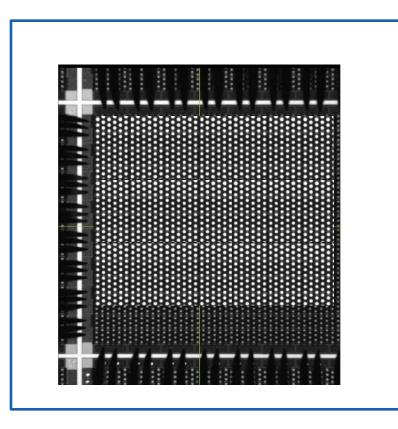
Leading 3D sensing VCSEL innovation

Thank you!

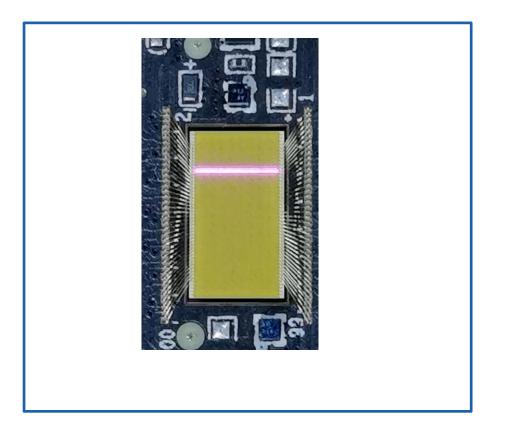
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Addressable VCSEL array examples

Matrix addressable VCSEL array

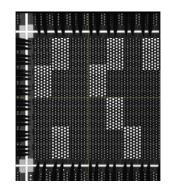


1D VCSEL array



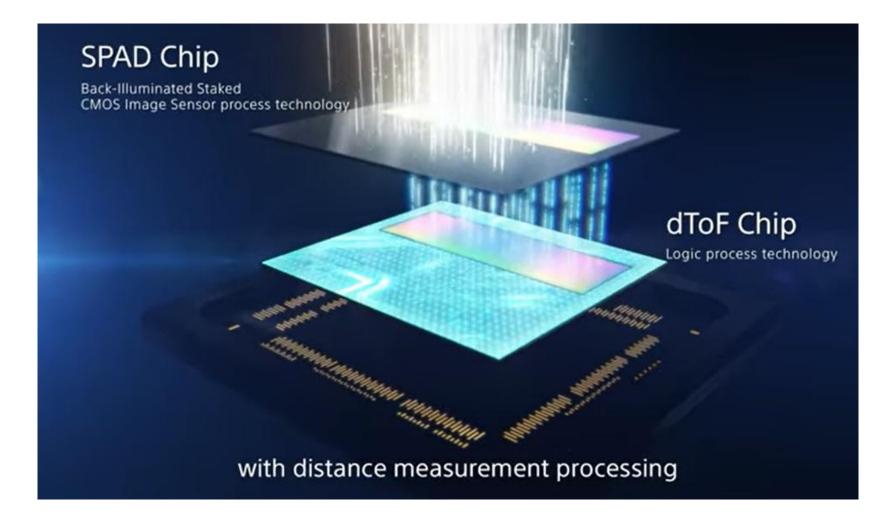
Addressable VCSEL array examples

2-D Matrix addressable VCSEL array





Stacked SPAD image sensor





Summary

- Matrix addressability opens additional options for LiDAR architectures
- Higher peak power densities using multi-junction epitaxial materials enable
 > 1kW/mm² (low-duty-cycle, few ns pulse) permit extension to longer distances
- Simpler optics + no need for TEC
- Proven at manufacturing processes at consumer volumes



Summary

- The latest generation of high-power, multi-junction VCSEL chips are the light sources of choice for automotive ToF LiDAR architectures
- Addressability of individual emitter sections allows a perfect match of the emitter to available and future detector devices
- VCSEL development has advanced rapidly as high-volume applications drive innovation and infrastructure
- Lumentum is focused on expanding its automotive VCSEL portfolio
- Ecosystem partners are established for module integrators, detector solutions, and electronic integration



Automotive in-cabin sensing applications

Occupancy Monitoring System/Driver Monitoring System (OMS/DMS)

Seatbelt fastened/unfastened Passenger size and positioning Facial recognition Alert to check rear seat when exiting or entering the vehicles Detect movement after vehicle is parked or moved Monitor occupant biometrics like heart rate, temperature



Gesture recognition in-cabin controls

