

Design For Scale-GPU Based Infrastructure for Autonomous Vehicles

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AGENDA

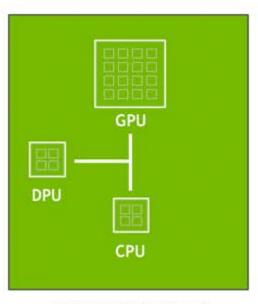
Autonomous Vehicle Development

- GPU Platform Overview
- Fundamentals of AV Stack Architecture
- AV Workflow and Platform: Training and Inference
- Architecture to Scale
- NVIDIA DRIVE End to End Infra for AV development

Conclusion

- Key Pointers
- Developer Engagement Platforms
- NGC NVIDIA Software Hub

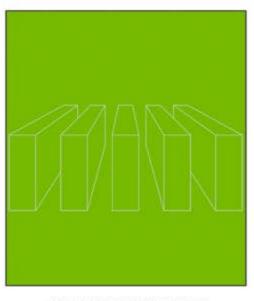
FUNDAMENTALS OF NVIDIA PLATFORM



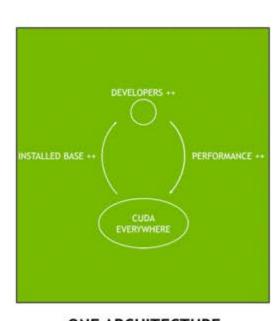
X-FACTOR SPEED UP



FULL STACK



DATA-CENTER SCALE



ONE ARCHITECTURE

AV Development Workflows

Auto AV Training and Inference

NGC FOR PERFORMANCE

Architecture to Scale





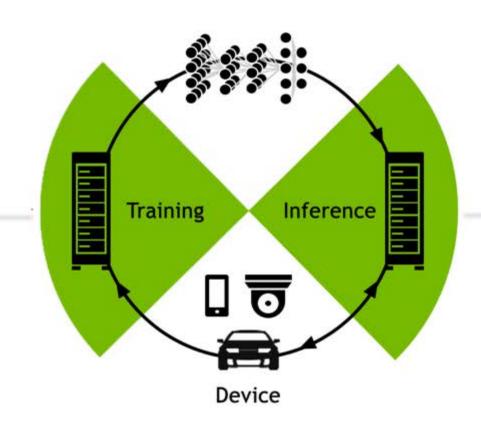


AUTONOMOUS VEHICLES

Training and Inference at Scale

Billions of Trillions of Operations
GPU train larger models, accelerate
time to market

Training

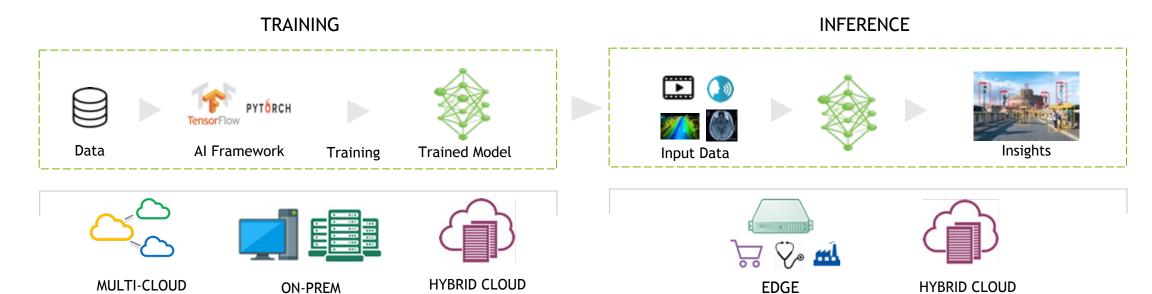


10s of billions of image, voice, video queries per day GPU inference for fast response, maximize data center throughput

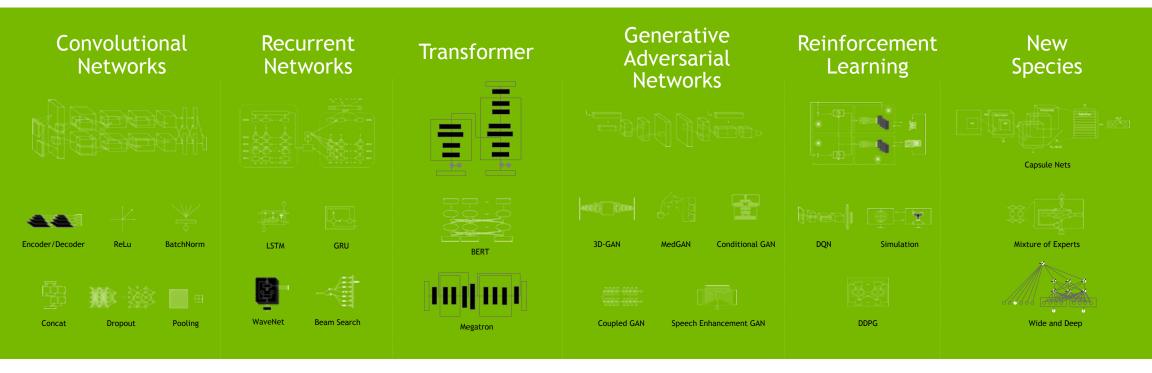
Data center Inference

AI WORKFLOW

Training and Inference

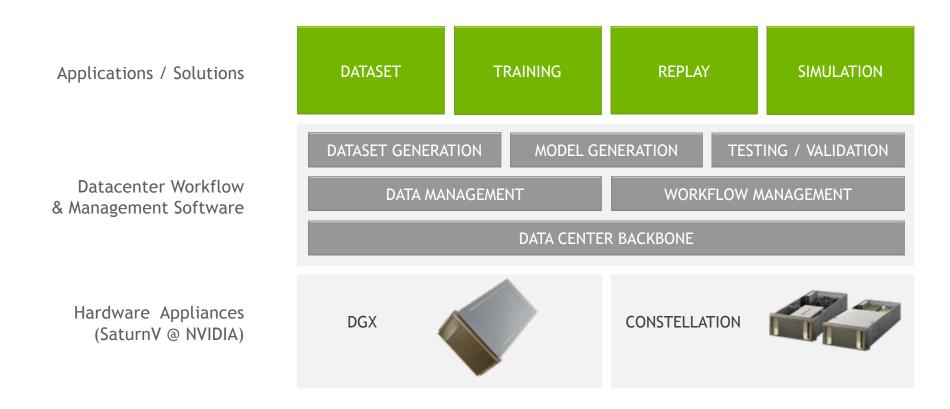


CAMBRIAN EXPLOSIAN OF AI MODELS



NVIDIA DRIVE INFRASTRUCTURE

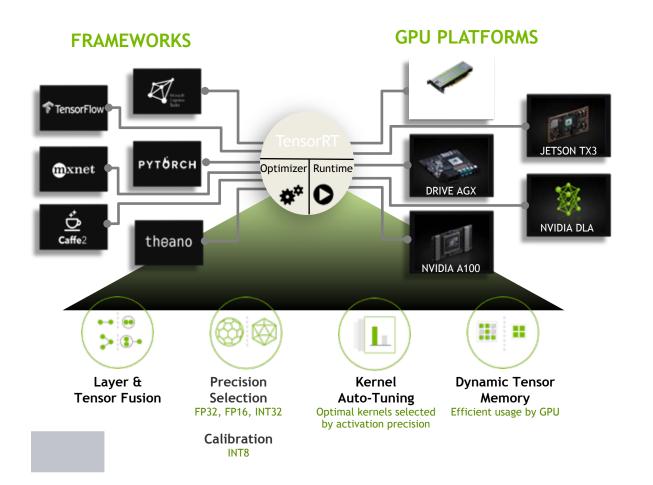
HW & SW Infrastructure | Applications, Tools, Services



OIDIA.

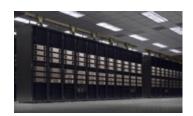
NVIDIA TENSORRT INFERENCE PLATFORM

AV Stack Pillars: Frameworks and Infrastructure











Drive





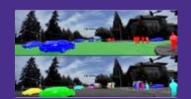
Jetson

Data Center



DRIVE SOFTWARE MODELS

Perception | Mapping | Planning | Driver Monitoring







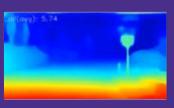
Distance



Time to Collision (RNN)



Free Space



Structure from Motion



Lidar



Paths



Signs



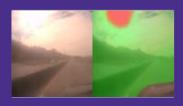
Map



High Beam



Parking



Camera Blindness



Intersection



Traffic Lights



Gestures/Pose



Gaze



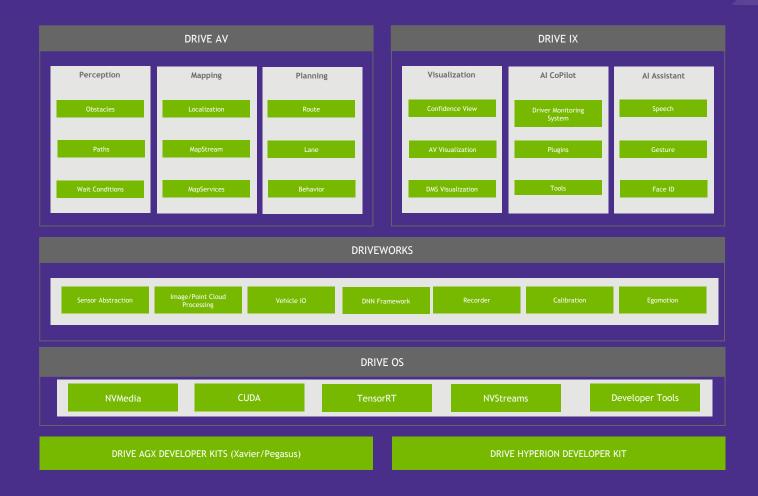
Prediction (RNN)



Radar



NVIDIA DRIVE SOFTWARE





DRIVE OS FOR SAFETY

The First Functional Safety (FuSa) Operating System for Accelerated Computing and Artificial Intelligence



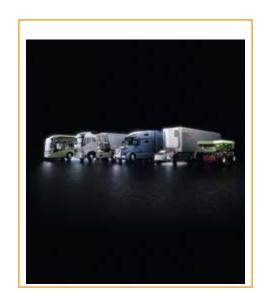
Automotive Industry Safety Standards



Scalable from L2 to L5



Comprehensive Security Model



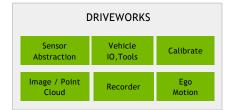
Industry Partners And Experience

NVIDIA DRIVE

E2E AV flow to enable large scale AV development & testing



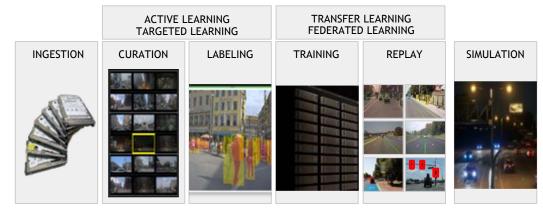
1,000's Engineers 20+ DNNs, 50 Parallel Experiments



1,000's Engineers HW & SW 20 million lines of code





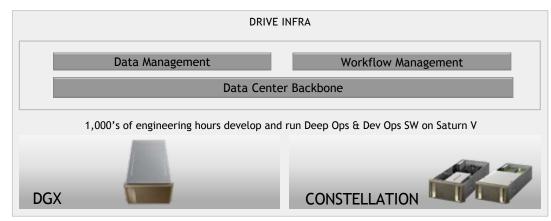


300k hours, 300M frames 10 PB/wk data collected

Sensor Data+Logs

> 1,500 labelers 50M+ labeled images

100's DGX Saturn V 1M+ virtual miles driven 200+ DRIVE Constellations



ENABLING PORTABILITY WITH NGC CONTAINERS

NGC Deep Learning Containers

Extensive

Diverse range of workloads and industry specific use cases

Optimized

- DL containers updated monthly
- Packed with latest features and superior performance

Secure & Reliable

- Scanned for vulnerabilities and crypto
- Tested on workstations, servers, & cloud instances

Scalable

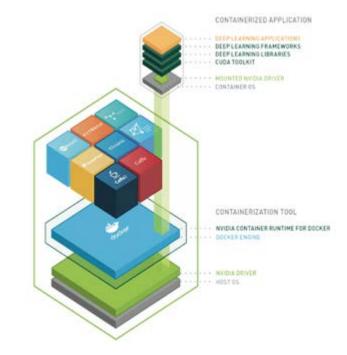
Supports multi-GPU & multi-node systems

Designed for Enterprise & HPC

- Supports Docker, Singularity & other runtimes

Run Anywhere

- Bare metal, VMs, Kubernetes
- x86, ARM, POWER
- Multi-cloud, on-prem, hybrid, edge

















CONCLUSION

- Key Pointers
 - NVIDIA AV Full Stack Scalable solution
 - AV Infrastructure addressing Training, Replay and Inference at Scale
 - AV stack developed on Standardized Frameworks
 - Customizable AV Workflow management tools
 - DRIVE AGX Platform for production deployment
 - HW Platform: DRIVE Xavier / Orin + Auto-grade discrete GPU
 - SW Platform: DRIVE OS, DRIVEWORKS, DRIVE AV, DRIVE IX
 - Safety certified HW and SW platform



DEVELOPER ENGAGEMENT PLATFORMS

Information, downloads, special programs, code samples, and bug submission	<u>developer.nvidia.com</u>
Containers for cloud and workstation environments	ngc.nvidia.com
Insights & help from other developers and NVIDIA technical staff	devtalk.nvidia.com
Technical documentation	docs.nvidia.com
Deep Learning Institute: workshops & self-paced courses	courses.nvidia.com
In depth technical how to blogs	devblogs.nvidia.com
Developer focused news and articles	news.developer.nvidia.com
Webinars	nvidia.com/webinar-portal
GTC on-demand content	gputechconf.com

Create