@qualcomm

Location: Sophia Antipolis



Integrating the SIM

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Path to an integrated SIM



Qualcomm Technologies, Inc. (QTI) mobile processor not to scale

Size benefit of integrating the SIM card



Size benefit of integrating the SIM card

Traditional SIM cards or eSIMs include NVM, constraining the technology node it can use

Typically 30nm

SoC use the latest technology nodes, currently at 10nm and reducing

External NVM is leveraged

Qualcomm SPU

Snapdragon Processor

Snapdragon Processor



Hardware Architecture





Hardware Architecture



The Secure Processor is an isolated core with secure trust boundaries with all other cores

It is not a softSIM or a TEE-based SIM

The Secure Processor is designed to meet the same security criteria as a SIM card: Common Criteria EAL4+

Security



Security



The Secure Processing Unit is equivalent to a discrete smartcard Secure IC except that it does not host Flash memory Code and Data at rest are stored in the device Flash with a high level of security enforced by SPU:

- Confidentiality (AES-256 encryption)
- Authenticity (CMAC-AES-256)
- Anti-Rollback (version counter handle and stored in SPU)

Qualcomm[®] SPU design

is targeting EAL4+ certification according to PP084 (Secure IC Platform)



Certification - example of eSIM

The principles of composite certification can apply as in the case of eSIM



Qualcomm has initiated a certification effort for the Secure Processor with the BSI, targeting EAL4+

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Standardization efforts & Industry activities









Publication of iUICC POC Group Primary Platform Requirements Endorsed new ways to store operator credentials compatible with integrated SIM On-going standardization within ETSI SCP

Beyond the SIM use case - realizing the full potential of a secure processor



Each use case today has his own dedicated solution, with its own ecosystem, standardization and security certification processes.



Enabling multiple use cases VPP Interfaces Standardized in GlobalPlatform





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