

OpenCL

State of the Nation

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Toronto, May 2017



Topics

The Good

The amazing
progress of OpenCL

1

The Bad

Lessons Learned from
the first eight years

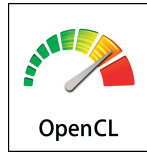
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The Exciting

Where do we go
from here?

3

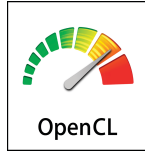
OpenCL 2.2 Finalized Here at IWOCCL!



2011

OpenCL 1.2

Becomes industry baseline

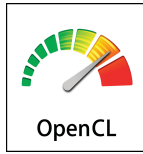


2013

OpenCL 2.0

Enables new class of hardware

SVM
Generic Addresses
On-device dispatch

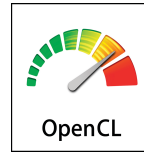


2015

OpenCL 2.1
SPIR-V 1.0

SPIR-V in Core

Kernel Language Flexibility



2017

OpenCL 2.2
SPIR-V 1.2

OpenCL C++ Kernel Language
Static subset of C++14
Templates and Lambdas

SPIR-V 1.2
OpenCL C++ support

Pipes
Efficient device-scope communication between kernels

Code Generation Optimizations

- Specialization constants at SPIR-V compilation time
- Constructors and destructors of program scope global objects
- User callbacks can be set at program release time

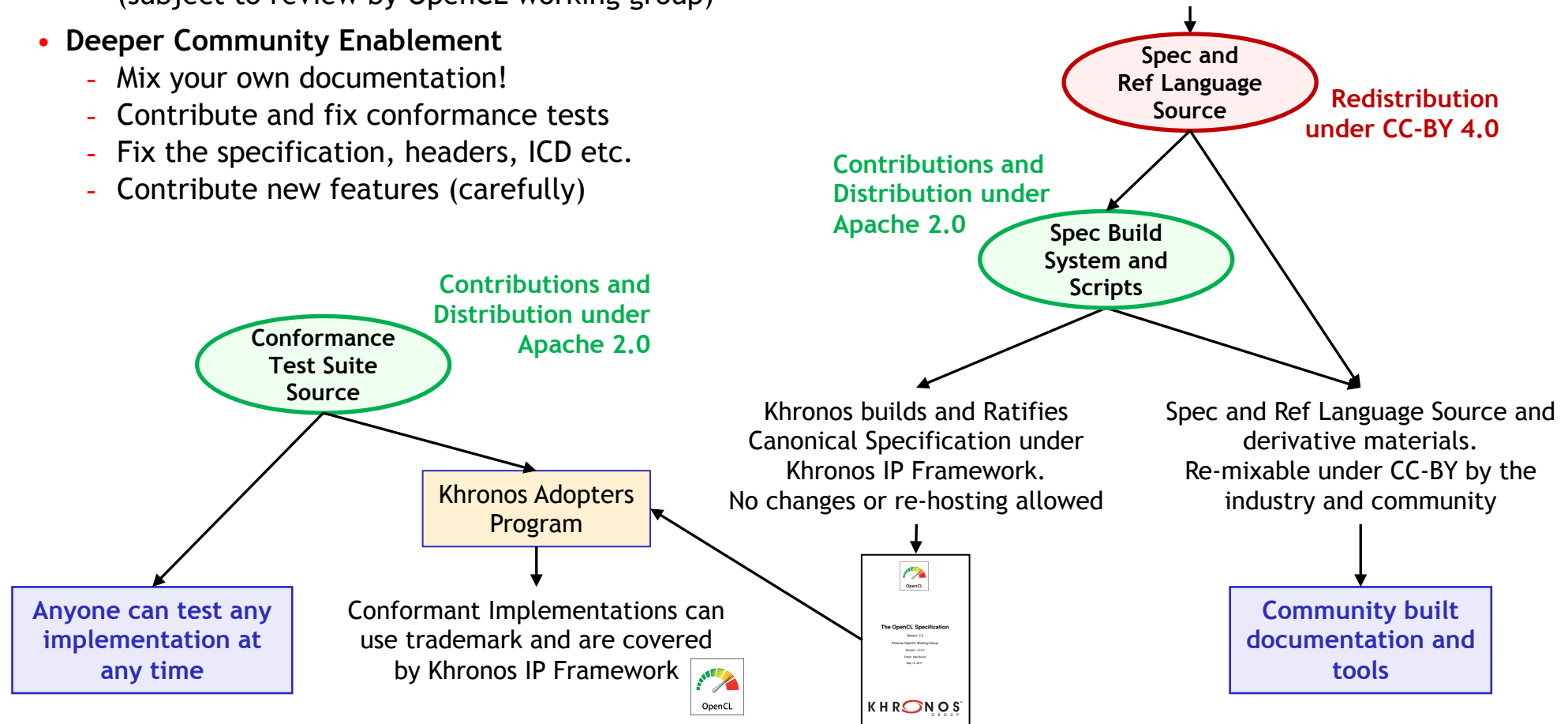
<https://www.khronos.org/opencl/>



New Open Source Engagement Model

- Khronos is open sourcing specification sources, conformance tests, tools
 - Merge requests welcome from the community (subject to review by OpenCL working group)
- Deeper Community Enablement
 - Mix your own documentation!
 - Contribute and fix conformance tests
 - Fix the specification, headers, ICD etc.
 - Contribute new features (carefully)

Source Materials for Specifications and Reference Documentation **CONTRIBUTED Under Khronos IP Framework** (you won't assert patents against conformant implementations, and license copyright for Khronos use)



Shout Out to University of Windsor

The [Windsor Testing Framework](#), also released today, enables developers to quickly install and configure the OpenCL Conformance Test Suite on their own systems.

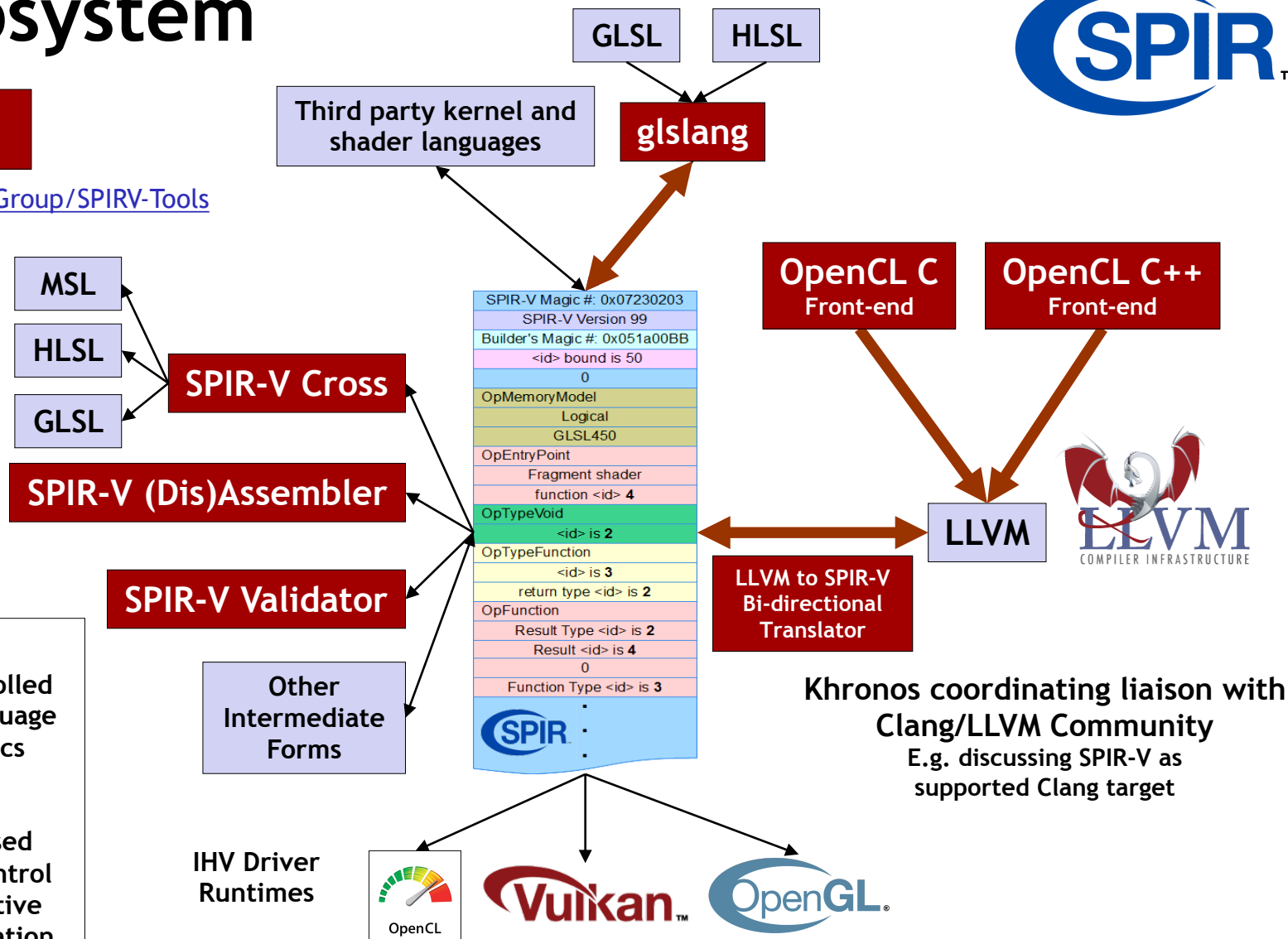


University
of Windsor

SPIR-V Ecosystem

Khronos has open sourced these tools and translators

<https://github.com/KhronosGroup/SPIRV-Tools>



SPIR-V

- Khronos defined and controlled cross-API intermediate language
 - Native support for graphics and parallel constructs
 - 32-bit Word Stream
- Extensible and easily parsed
- Retains data object and control flow information for effective code generation and translation

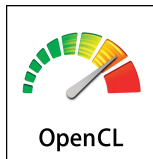
SYCL Ecosystem

- Single-source heterogeneous programming using STANDARD C++
 - Use C++ templates and lambda functions for host & device code
 - Layered over OpenCL
- Fast and powerful path for bring C++ apps and libraries to OpenCL
 - C++ Kernel Fusion - better performance on complex software than hand-coding
 - Halide, Eigen, Boost.Compute, SYCLBLAS, SYCL Eigen, SYCL TensorFlow, SYCL GTX
 - triSYCL, ComputeCpp, VisionCpp, ComputeCpp SDK ...
- More information at <http://sycl.tech>

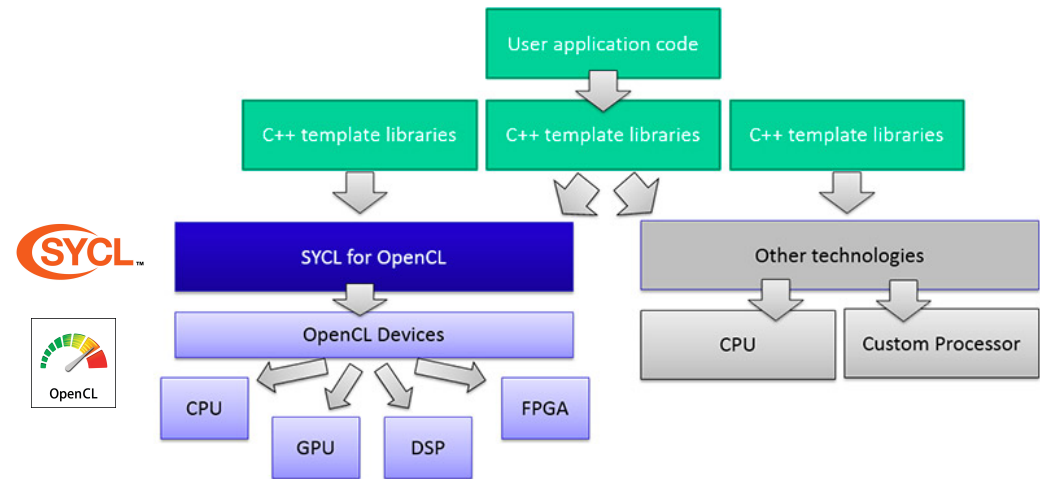
Developer Choice

The development of the two specifications are aligned so code can be easily shared between the two approaches

C++ Kernel Language
Low Level Control
'GPGPU'-style separation of
device-side kernel source
code and host code

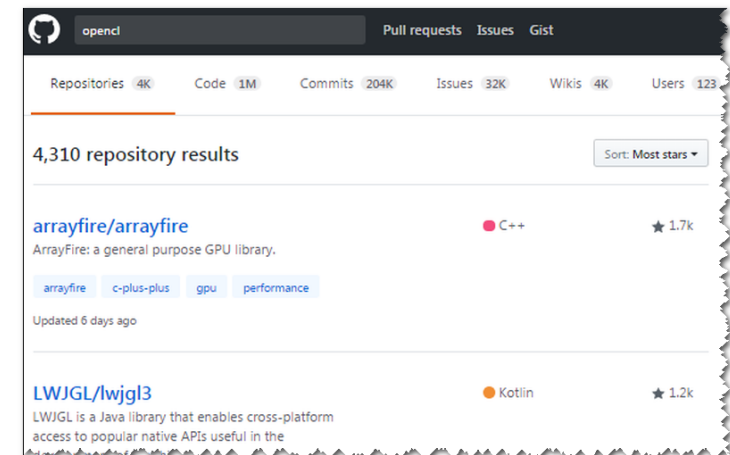


Single-source C++
Programmer Familiarity
Approach also taken by
C++ AMP and OpenMP



OpenCL Adoption

- 100s of applications using OpenCL acceleration
 - Rendering, visualization, video editing, simulation, image processing
- Over 4,000 GitHub repositories using OpenCL
 - Tools, applications, libraries, languages
 - Up from 2,000 two years ago
- Multiple silicon and open source implementations
 - Increasingly used for embedded vision and neural network inferencing
- Khronos Resource Hub
<https://www.khronos.org/opencl/resources/opencl-applications-using-opencl>



OpenCL as Language/Library Backend

Caffe

C++ based
Neural
network
framework

Halide

Language for
image
processing and
computational
photography

C++ AMP
Accelerated Massive Parallelism
with Microsoft Visual C++

MulticoreWare
open source
project on
Bitbucket

SYCL™

Single
Source C++
Programming
for OpenCL

aparapi

Java language
extensions
for
parallelism

OpenCV

Vision
processing
open source
project

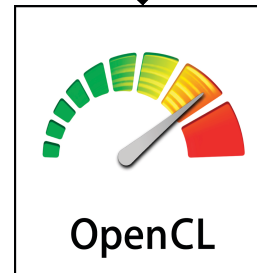
OpenACC
DIRECTIVES FOR ACCELERATORS

Compiler
directives for
Fortran,
C and C++

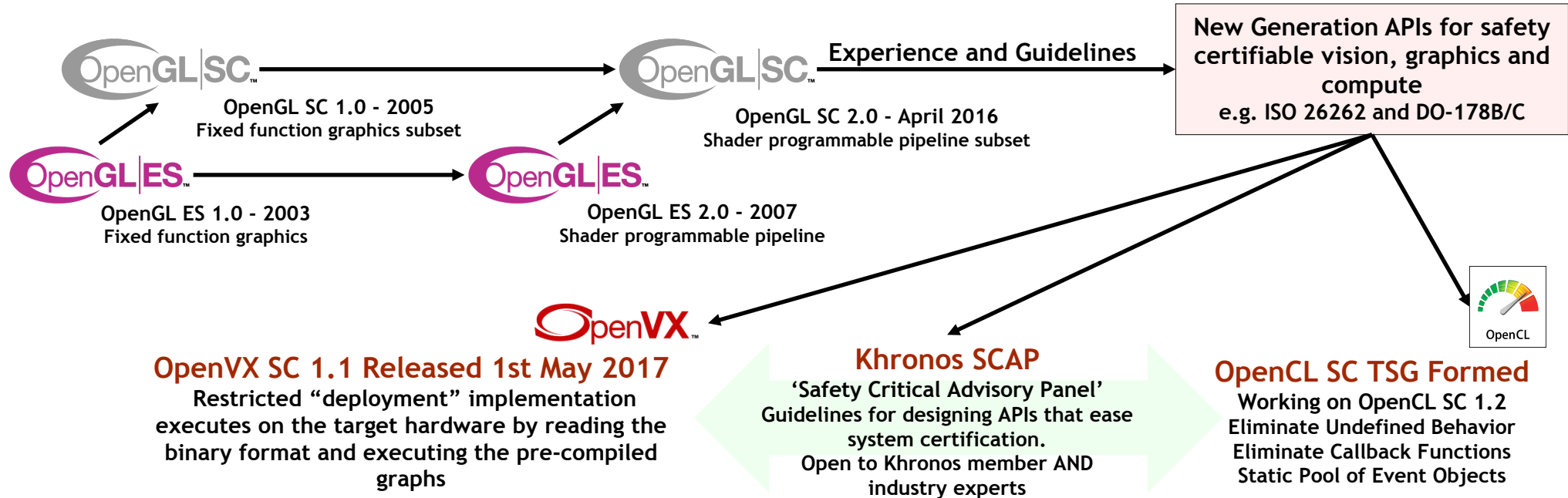
TensorFlow

Open source
software library
for machine
learning

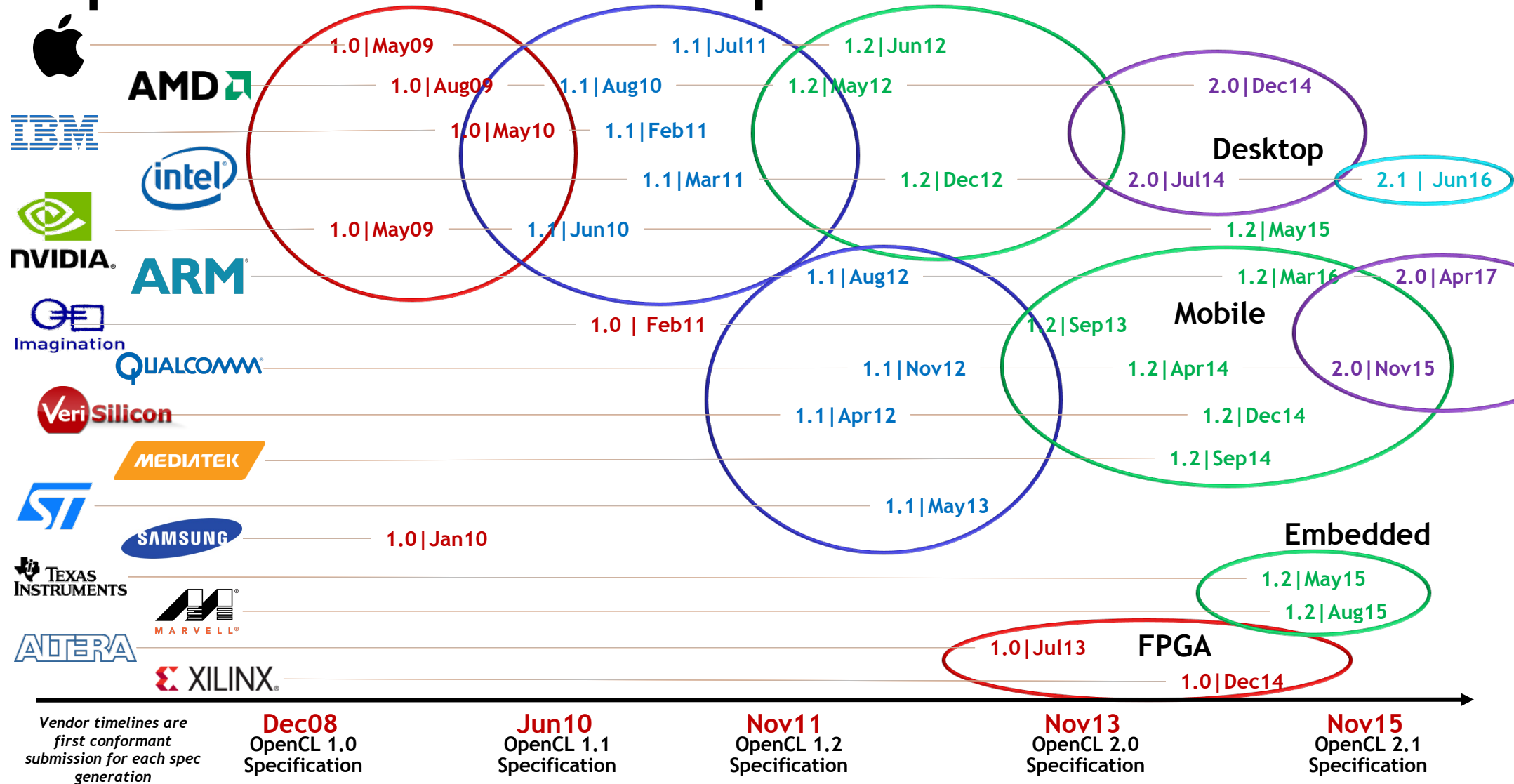
Hundreds of languages, frameworks
and projects using OpenCL to access
vendor-optimized, heterogeneous
compute runtimes



Safety Critical APIs



OpenCL Conformant Implementations

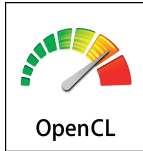


OpenCL - 1000s Man Years Effort



Single Source C++ Programming

Full support for features in C++14-based Kernel Language



API and Language Specs

Brings C++14-based Kernel Language into core specification



Portable Kernel Intermediate Language

Support for C++14-based kernel language e.g. constructors/destructors

OpenCL C++ Kernel Language

Static subset of C++14

Templates and Lambdas

SPIR-V 1.2 with C++ support

SYCL 2.2 single source C++

Pipes

Efficient device-scope communication between kernels

Multiple Code Generation

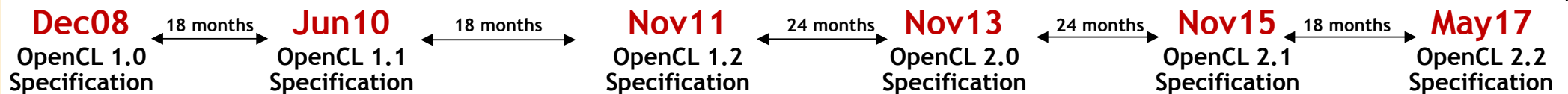
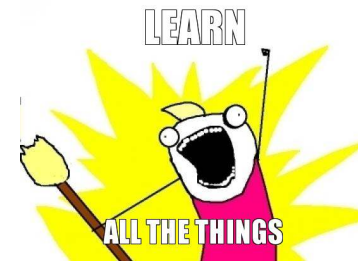
Optimizations

SPIR-V in Core
Subgroups into core
Subgroup query operations
clCloneKernel
Low-latency device timer queries

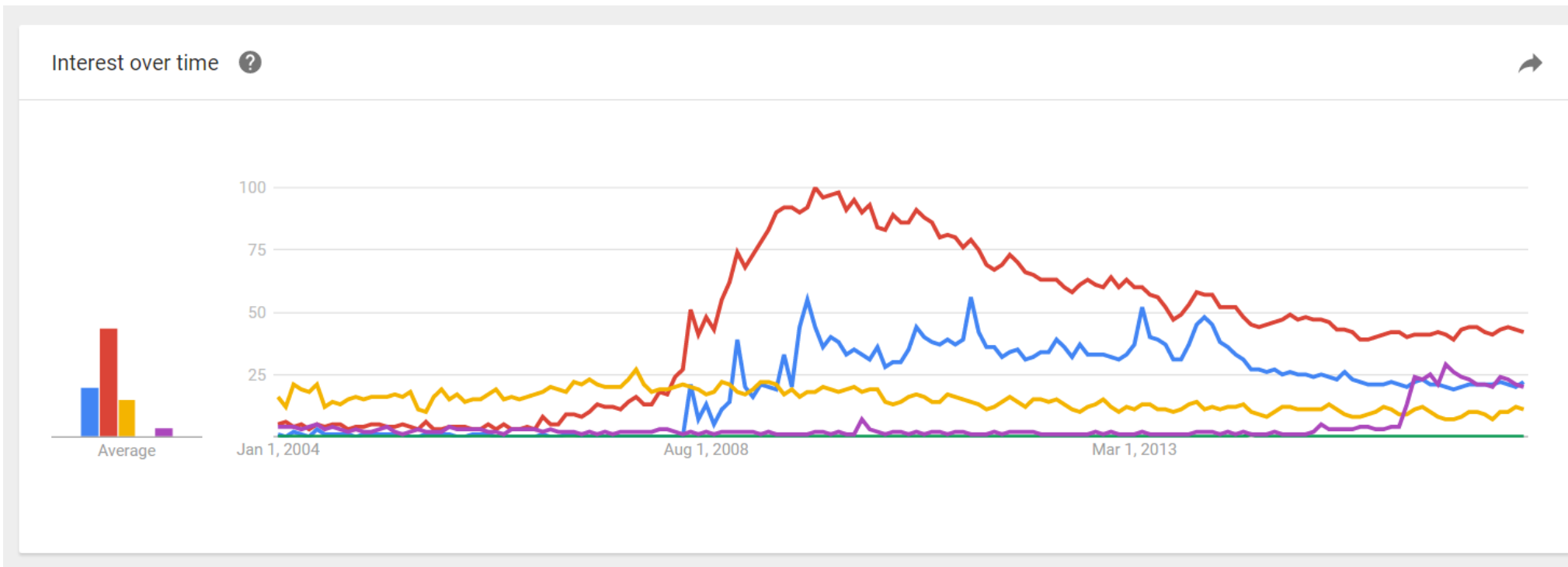
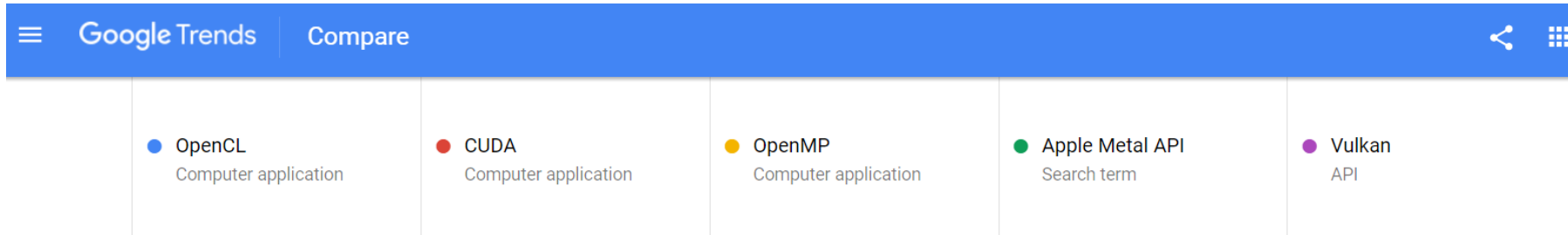
Shared Virtual Memory
On-device dispatch
Generic Address Space
Enhanced Image Support
C11 Atomics
Pipes
Android ICD

Device partitioning
Separate compilation and linking
Enhanced image support
Built-in kernels / custom devices
Enhanced DX and OpenGL Interop

3-component vectors
Additional image formats
Multiple hosts and devices
Buffer region operations
Enhanced event-driven execution
Additional OpenCL C built-ins
Improved OpenGL data/event interop



Google Trends



Embrace the Layered Ecosystem

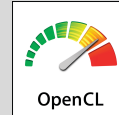
OpenCL mixed providing low-level hardware access with 'ease-of-use'

Didn't make it clear that low-level performance portability is impossible

Did not focus on rapidly porting efficient libraries

Applications

Rich Middleware Ecosystem
Libraries, languages, tools, engines



Hardware

Middleware just needs direct access to hardware. Driver should 'get out of the way'

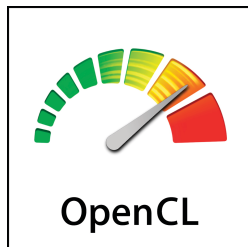
Middleware can provide ease of use

Middleware has the system/domain context to try to provide performance portability

Run-time abstraction hardware is needed:

- Software vendors can't afford to port to every type/generation hardware
- Hardware vendors want to keep innovating under an abstraction

Market Segments Need Deployment Flexibility



OpenCL has been
over-monolithic

E.g. DSP inferencing should not
be forced to ship IEEE FP32

Solution: feature sets - enabling
toggling capabilities within a
coherent framework without
losing conformance

Desktop (actual and cloud)

Use cases: Video Image Processing, Gaming Compute, Rendering, Neural Network Training and Inferencing

Roadmap: Vulkan interop, dialable precision, pre-emption, collective programming and improved execution model, dynamic parallelism, pre-emption

Mobile

Use case: Photo and Vision Processing, Neural Network Inferencing

Roadmap: SVM, dialable precision for inference engine and pixel processing efficiency, pre-emption and QoS scheduling for power efficiency

HPC

Use case: Numerical Simulation, Neural Network Training, Virtualization

Roadmap: enhanced streaming processing, enhanced library support

FPGAs

Use cases: Network and Stream Processing

Roadmap: enhanced execution model, self-synchronized and self-scheduled graphs, fine-grained synchronization between kernels, DSL in C++

Embedded

Use cases: Vision, Signal and Pixel Processing, Neural Network and Inferencing

Roadmap: arbitrary precision for power efficiency, hard real-time scheduling, asynch DMA

Other Lessons

Lessons	How We Learned Them	How We Do Better!
Language flexibility is good! Enable language innovation!	OpenCL WG spent way too long designing OpenCL C and C++	Ingest SPIR-V! BUT Vendors need to support it!
Lack of tools and libraries	Assumption that the Working Group's job is done once the specification is shipped	'Hard launches' i.e. simultaneous availability of spec, libraries, implementations and engines
Needs to be adopted/available on key platforms	Apple are focused on Metal OpenCL/RenderScript Confusion NVIDIA not pushing to 2.0	Add value to key platforms and/or develop viable portability solutions
Middleware and application insights and prototyping are essential during standards design	The OpenCL Working Group has lacked active software developer participation	Encourage ISVs to join Khronos to help steer the industry! AND OpenCL Advisory Panels

Khronos Advisory Panels

The Working Group invites input and shares draft specifications and other WG materials



Members

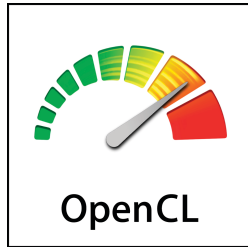
Pay membership Fee
Sign NDA and IP Framework
Directly participate in working groups

Advisors

Pay \$0
Sign Advisors Agreement = NDA and IP Framework
Provide requirements and feedback on specification drafts to the working group

Advisory Panel membership is
'By Invitation' and renewed annually.
No 'minimum workload' commitment - but we love input and feedback!
Please reach out if you wish to participate!

Requirements for 'OpenCL Next'



Working Group Decision!
Converge with and leverage
Vulkan design!
Expand on Vulkan supported
processors types and
compute capabilities

Low-level explicit API as
Foundation of multi-layer ecosystem



Features set for
Market Deployment Flexibility



SPIR-V Ingestion for
Language flexibility



Widely Adopted
No market barriers to deployment



Installable tools architecture for
Development flexibility



Low-latency, multi-threaded dispatch
For fine-grained, high-performance



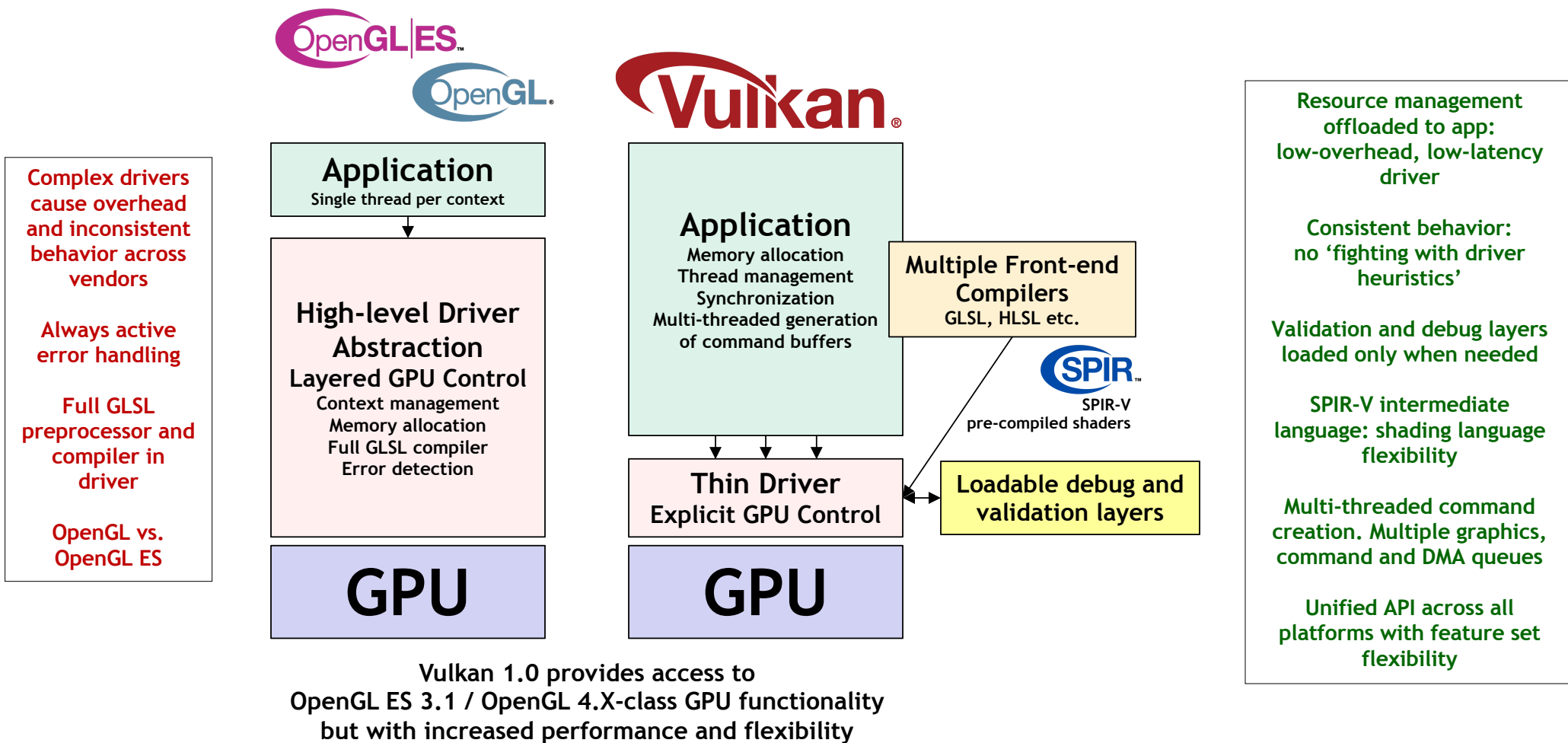
At least OpenCL 2.X-class
compute capabilities



Support for
diverse processor types



Vulkan Explicit GPU Control



Vulkan Adoption

All Major GPU Companies shipping Vulkan Drivers - for Desktop and Mobile Platforms



Mobile, Embedded and Console Platforms Supporting Vulkan



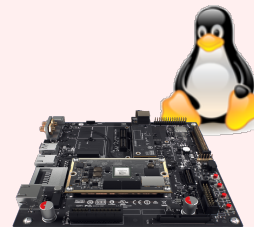
Android 7.0



Nintendo Switch



Android TV



Embedded Linux



SteamOS



ubuntu



redhat



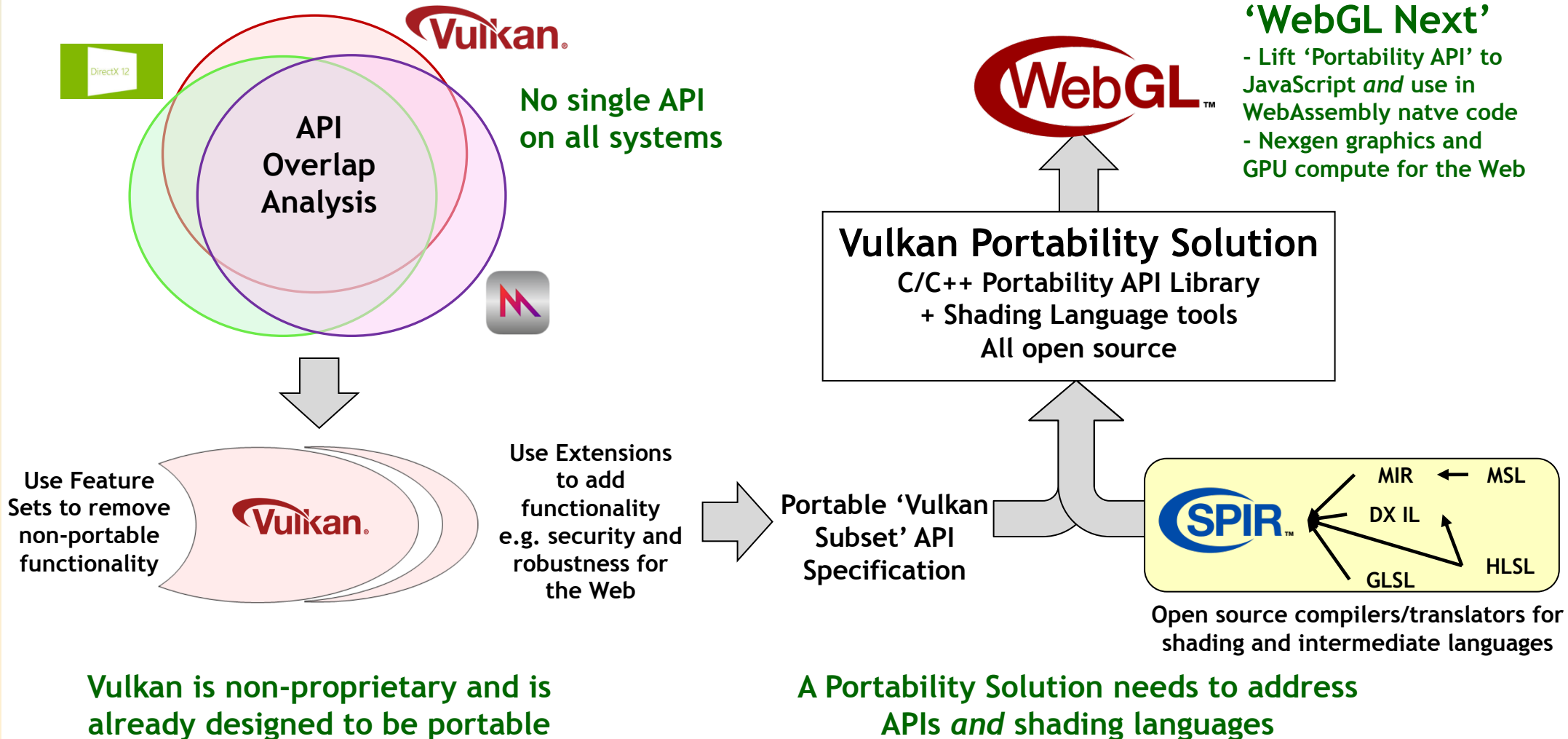
TIZEN



NINTENDO SWITCH

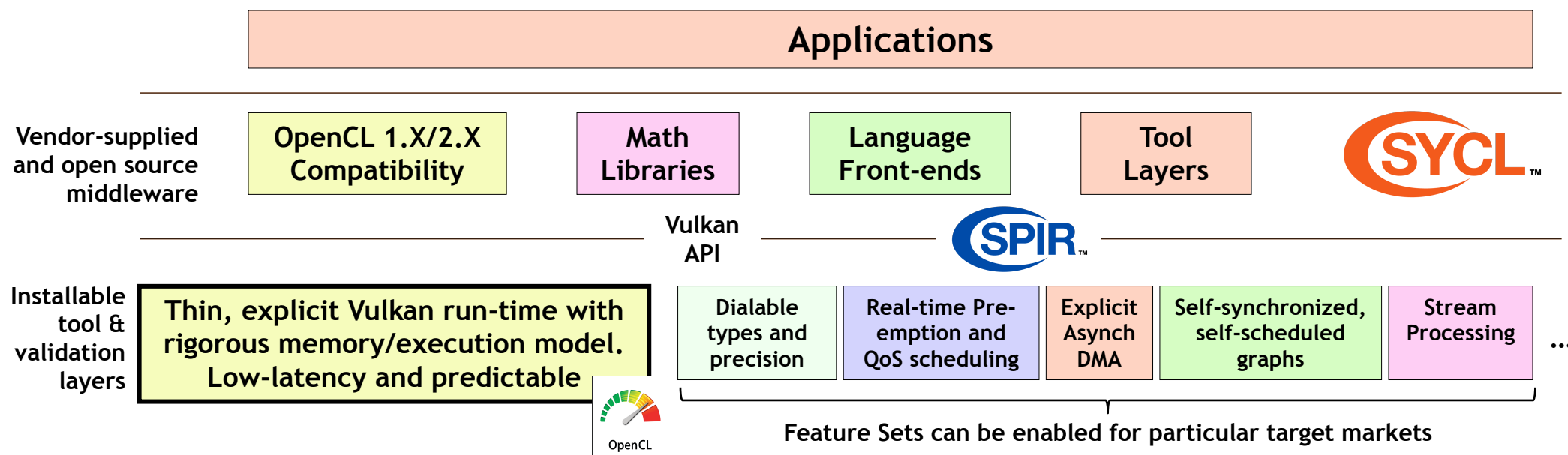


GPU Portability - Call For Participation



‘OpenCL-V’ - OpenCL and Vulkan Convergence

- Converge OpenCL roadmap over time with Vulkan API and run-time
 - Support more processor types, e.g. DSPs and FPGAs (graphics optional)
- Layered ecosystem for backwards-compatibility and market flexibility
 - Feature sets for target market agility
- Single runtime stack for graphics *and* compute
 - Streamline development, adoption and deployment for the entire industry



OpenCL Evolution Discussions

C++ AMP
Accelerated Massive Parallelism
with Microsoft Visual C++

Single source C++ programming.
Great for supporting C++ apps,
libraries and frameworks



Industry working to bring
Heterogeneous compute to
standard ISO C++

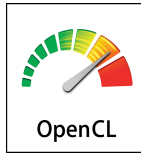
C++17 Parallel STL hosted by Khronos
Executors - for scheduling work
“Managed pointers” or “channels” -
for sharing data
Hoping to target C++ 20 but
timescales are tight



SYCL 1.2
C++11 Single source
programming

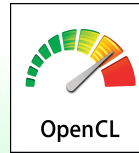


SYCL 2.2
C++14 Single source
programming



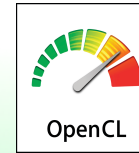
2011

OpenCL 1.2



2015

OpenCL 2.1
SPIR-V in Core



2017

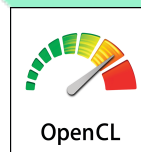
OpenCL 2.2
C++ Kernel Language



‘OpenCL-V’
Converge Vulkan
and OpenCL

Your Input!

OpenCL 1.2++?
Focus on embedded imaging,
vision and inferencing
Make FP32 optional for DSPs and
general power efficiency



Get Involved!

- **OpenCL is driving to new level of community engagement**
 - Learning from the Vulkan experience
 - We need to know what you need from OpenCL
 - IWOCL is the perfect opportunity to find out!
- **Any company or organization is welcome to join Khronos**
 - For a voice and a vote in any of these standards
 - www.khronos.org
- **If joining is not possible - ask about the OpenCL Advisory Panel**
 - Free of charge - enables design reviews, requirements and contributions
- **Neil Trevett**
 - ntrevett@nvidia.com
 - [@neilt3d](https://twitter.com/neilt3d)

