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Kalyan Perumalla

About Me: Experienced Professional, Manager, Researcher, Hands-on Developer, Mentor

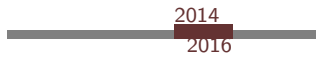
Highlights: US Citizen; Q Clearance (active); 21-year work experience; PhD CS (GT'99), MS CS (UCF'93); National Academy of Science US Army Research Lab Assessment Board Member (2015-17); Durham IAS Fellow (2015); DOE Career Award (2010-2015); Editorial Board Member of 2 Journals (2008-); 5 Best papers; 14 Tutorials; 1 Book; 1 Monograph; 100+ Publications; H-index 31 (Google Scholar); 6 Keynotes/distinguished lectures; 20+ Invited talks; Taught operating systems; Inter-collegiate programming team coach (1995-2000), member (1991-93); Expert, hands-on sequential/parallel programmer (1991-); Developed multiple massively-parallel software packages

Work Experience

- Since 2014 **Distinguished R&D Staff Member**, Oak Ridge National Laboratory.
- Since 2014 **Group Leader**, Discrete Computing Systems, Oak Ridge National Laboratory.
- Since 2018 **Joint Professor**, ISyE, University of Tennessee, Knoxville.
- Since 2006 **Adjunct Professor**, CSE, Georgia Institute of Technology.
- 2009-2013 **Senior R&D Staff Member**, Oak Ridge National Laboratory.
- 2005-2009 **R&D Staff Member**, Oak Ridge National Laboratory.
- 1997-2005 **Research Faculty**, Georgia Institute of Technology.
- 1992-1997 **Intern**, Bellcore, Bell Labs, Schlumberger, IST.

Recently Developed/Co-developed Software Packages

- 2016-2018 **CUPPA**, Graph Generator, of massive scale-free graphs; written in C++ and CUDA; generates billions of edges in a few seconds on modern GPUs, ORNL-sponsored.
- 2016-2018 **DeepEx**, Deep Learning, on large datasets; very light, portable software footprint, scales to large heterogeneous CPU-GPU platforms; better scaling than TensorFlow; tested on hundreds of GPUs; written in C++, MPI, CUDA, CUDNN, NCCL, US DOE-sponsored.
- 2016-2018 **MutEnt**, Image Registration, with novel mutual entropy computation code for highly scalable and efficient computation on large-sized, high-volume images; beats the best-known implementations in OpenCV, US Air Force-sponsored.
- 2014-2017 **CloneX**, Efficient ensembles of decision trees of millions of simulations, scaled to 1000+ GPUs of Titan supercomputer, ORNL-sponsored.



RBLAS, *Reversible basic linear algebra subprograms (BLAS)*, portable across GPUs and CPUs (C, C++, FORTRAN, CUDA), DOE-sponsored.



RCC, Source-to-Source Reversible C Compiler, US DOE and NSF-sponsored.



NetWarp, *High-fidelity network simulation*, modified Xen hypervisor scheduler; 256 VMs time-synchronized on 1 box, US Army-sponsored.



BLOCKTRI, *Most scalable block tridiagonal matrix solver*, multi-node, multi-core and multi-GPU code; FORTRAN, C, CUDA, US DOE-sponsored.



musik, *Scalable discrete event micro-kernel*, with advanced Time Warp plus conservative virtual time-synchronization, written in C++, MPI, tested on 216,000 cores, US DOE, DoD and NSF-sponsored.



μπ, *Most scalable simulator of parallel programs*, scaled to 216,000 cores of Cray XT5; simulating over 227 million virtual tasks, US DOE-sponsored.

Selected Technical Skill Set

PARALLELISM

Hands-on parallel programming experience spanning 20 years, since days of MASP, BBN, SGI to modern supercomputers of today, including multi-cores, GPUs, networks; expert parallel/distributed program coder in MPI, CUDA, Sockets, Shared Memory

MODELING

Agent-based Modeling, Cyber simulations, Epidemiological outbreak models, Electric grid models, Transient Electric Grid Models, Micro/Meso-scopic Transportation models

COMPUTING

Sophisticated sequential/parallel algorithm development, Performance enhancement, Debugging

CODING

Hands-on coder in C/C++, Java, FORTRAN, Python, PHP, MySQL, shell scripting, and others

ACCELERATORS

Expert in GPU computing; coded complex algorithms in CUDA for many applications

Management and Teamwork Training



Scrum, *Scrum Alliance Inc.*, Certified Scrum Product Owner, Certified Scrum Master, Scrum at Scale.



Developing Leadership Potential, *Oak Ridge National Laboratory*.



Management Boot Camp, *Oak Ridge National Laboratory*.



Project Management, *Oak Ridge National Laboratory*.



Unconscious Bias, *Oak Ridge National Laboratory*.

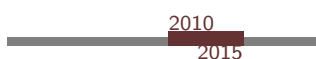
Recognition



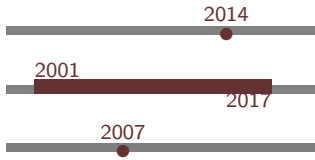
Best Scientific Achievement in Past 5-Years, *CSMD, ORNL*.



IAS Fellow, *Institute of Advanced Study, Durham University, UK*.



DOE Early Career Award, Advanced Scientific Computing Research, Office of Science, research grant of \$2.5mil over 5 years.



Division Annual Award, CSED, ORNL.



Tutorials, (14), at HPCS, WSC, PADS, IITSEC, and others.



Significant Event Award, Oak Ridge National Laboratory.

Leadership and Service



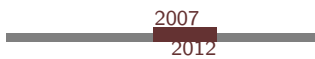
Panel Member, National Academy of Sciences, US ARMY IS TAB.



Creativity and Innovation Team Lead, Oak Ridge National Laboratory.



Editorial Board Member, ACM TOMACS, SCS SIMULATION.



Program Chair, PADS'07, MASCOTS'12, SIMUTOOLS'12,'17.



Lab Seed Fund Committee Member, Oak Ridge National Laboratory.



Committee Member, Multiple international conferences.



Advisor/Mentor, 20+ postdoc, doctoral, undergraduate, high school interns.



Programming Team Coach, ACM Intercollegiate Programming Contests.

Graduate Education



PhD, Computer Science, Georgia Institute of Technology.



MS, Computer Science, University of Central Florida.

Selected Research Highlights

CROSS-CUTTING SIMULATIONS

- **Transportation**: Largest (million-link), 100x realtime models on GPU
- **Epidemiology**: Largest individual-level disease outbreak (billion+ people)
- **Cyber**: Million-node packet-level network simulations (DARPA NMS); honeypot simulations; large worm attacks; mobile ad-hoc networks (US Army)
- **Electromagnetics (EM)**: Parallel discrete event EM model for large terrains
- **Exascale**: Largest (2.5×10^8 -task) MPI program simulation on 2×10^5 cores

SOLVERS

Largest and fastest block tri-diagonal parallel solver BLOCKTRI

DISCRETE EVENT

Supercomputer-based simulations on 216,000 cores with μ sik

CLONING

First cloned simulations scaled to 1,000+ GPUs, 100x efficient ensembles

Publications



Summary, 2 books, 3 book chapters, 4 national agency reports, 30 journal articles, 14 invited articles, 10 abstracts with presentations, 6 abstracts with posters, 68 refereed conference papers, 19 technical reports.

H-index 31 (Google Scholar, Nov 28, 2018)



Best Papers, 5 winners, 2 finalists.

SCS Summer Simulation 2014, ICST SimuTools 2010 (finalist), IEEE/ACM PADS 2009 (finalist), SCS Spring Simulation 2008, IEEE/ACM PADS 1999, 2002, 2005

Curriculum Vitae

Kalyan Perumalla

Distinguished R&D Staff Member and
Group Leader, Discrete Computing Systems Group
Computer Science and Mathematics Division
Oak Ridge National Laboratory
(Managed by UT-Battelle for the US Department of Energy)
Oak Ridge, Tennessee, USA

Homepage: <https://www.ornl.gov/staff-profile/kalyan-r-s-perumalla>

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I. BRIEF BIOGRAPHY



Kalyan Perumalla is a Distinguished Research and Development Staff Member and Manager at the [Oak Ridge National Laboratory](#) (ORNL, a [US Department of Energy](#) laboratory). Dr. Perumalla founded and currently leads the Discrete Computing Systems Group in the [Computer Science and Mathematics Division](#) at the [Oak Ridge National Laboratory](#). He also serves as an Adjunct Professor in the [School of Computational Sciences and Engineering](#) at the [Georgia Institute of Technology](#) and as an ORNL Joint Professor in the [Department of Industrial and Systems Engineering](#) at the [University of Tennessee, Knoxville](#).

In 2015, he was selected as a Fellow of the [Institute of Advanced Study](#) at [Durham University](#), UK. He has served on the [National Academy of Sciences](#)' Technical Advisory Boards on Computational Sciences and Information Science at the [U.S. Army Research Laboratory](#), 2015-2017.

Dr. Perumalla is among the first recipients of the [U.S. Department of Energy](#) Early Career Award in Advanced Scientific Computing Research (\$2.5 million over 5 years). Over the past 20 years, he has served as a principal investigator or co-principal investigator on several research projects sponsored by the Department of Energy, Department of Homeland Security, Air Force, DARPA, Army Research Laboratory, National Science Foundation, and industry.

Dr. Perumalla earned his Ph.D. in computer science from the Georgia Institute of Technology in 1999. His areas of interest include high performance computing, reversible computing, parallel discrete event simulation, parallel combinatorial optimization, scalable machine learning and artificial intelligence implementations, and computer-based modeling and simulation of large-scale man-made systems such as transportation networks, energy grids and cyberphysical systems. His notable research contributions are in the application of reversible computation to high performance computing and in advancing the vision of a new class of supercomputing applications using real-time, parallel discrete event simulations. High performance simulations spanning over **200,000 processor cores** and **hundreds of GPUs** have been achieved by his algorithms and research prototypes on large supercomputing systems.

He has published his research and delivered invited lectures and tutorials on topics spanning high performance computing and simulation. His recent book [Introduction to Reversible Computing](#) ([Amazon](#), [CRC Press](#)) is among the first few in its area. He co-authored another book, three book chapters, and over 100 articles in peer-reviewed conferences and journals. Five of his co-authored papers received the best paper awards, in 1999, 2002, 2005, 2008, and 2014. Some of his research prototype tools in parallel and distributed computing have been disseminated to research institutions worldwide. Dr. Perumalla serves as program committee member and reviewer for international conferences and journals. He is a member of the editorial boards of the [ACM Transactions on Modeling and Computer Simulation](#) (TOMACS) and the [Transactions of the Society for Modeling and Simulation International](#) (SIMULATION).

Dr. Perumalla is trained in Agile Development technologies, and is a [Certified Scrum Master](#), [Certified Scrum Product Owner](#), and a [Certified Scrum at Scale Practitioner](#).

II. RESEARCH INTERESTS

Exascale Computing; Scalable Implementations of Machine Learning Systems and Artificial Intelligence (AI); Reversible Computing; Computer Modeling and Simulation of Man-made Systems including Electric Grids and Intelligent Transportation; Virtual Machines and Cloud Computing; Cyberphysical Systems and Digital Twins; Cyber Security/Network Simulation; Discrete Event Simulation; Parallel Algorithms; Parallel Integer Programming and Combinatorial Optimization; Image Processing; Domain Specific Languages

III. EMPLOYMENT HISTORY

- **Oak Ridge National Laboratory**, Oak Ridge, Tennessee, USA 2014-*Present*
Distinguished R&D Staff Member, Computing & Computational Sciences Directorate.
 - Responsibilities include initiation, planning and managing major research programs or projects; broad responsibility guided by unit goals, policies, procedures and strategy; exercise of considerable latitude in determining objectives and approach to work assignments; influences top management in determining unit goals and objectives; performance of work managed by results; development of new client relationships; management of full range of research services in most complex problems; competence with all standard and advanced equipment, systems, policies and work methods; maintenance of international reputation for knowledge in speciality; establishment of track record in pushing the edge of knowledge; advanced knowledge of wide range of principles, theories, professional competencies and research methods; advanced knowledge of other disciplines and functions; handling most complex, diverse and impactful problems with original solutions and integration/coordination of disciplines, functions and staff; application of considerable judgement and professional competency; and achievement and maintenance of reputation as key contributor to major programs; externally recognized expertise in one or more fields.
- **Oak Ridge National Laboratory**, Oak Ridge, Tennessee, USA 2014-*Present*
Group Leader, Discrete Computing Systems Group, Computer Science and Mathematics Division, Computing and Computational Sciences Directorate.
 - Responsibilities include personnel performance evaluations/appraisals and development plans; promotion processes, salary recommendations; time entry and accounting; budget preparation and tracking; absence policies; oversight of administrative assistants; interaction with human resources personnel; incentivizing with avenues such as significant awards; sensitivity to legal, programmatic, personal, and technical considerations; procurement and purchasing; travel and project approvals; ergonomics and safety compliance; hosting visitors; recruitment, interviewing, and external representation; and several others.
- **University of Tennessee**, Knoxville, Tennessee, USA 2018-*Present*
Joint (Full) Professor, Department of Industrial and Systems Engineering.

- **Georgia Institute of Technology**, Atlanta, Georgia, USA *2006-Present*
Adjunct Professor, College of Computing (School of Computational Sciences and Engineering).
- **Durham University**, Durham, UK *2015*
IAS Fellow, Fellow at the Institute of Advanced Study (IAS): Performing interdisciplinary research in the Epiphany term under the theme Emergence.
- **Oak Ridge National Laboratory**, Oak Ridge, Tennessee, USA *2009-2014*
Senior R&D Staff Member, Computational Sciences and Engineering Division, Computing & Computational Sciences Directorate.
- **Oak Ridge National Laboratory**, Oak Ridge, Tennessee, USA *2010-2014*
R&D Manager and Team Lead, Computational Sciences and Engineering Division, Computing & Computational Sciences Directorate.
- **Oak Ridge National Laboratory**, Oak Ridge, Tennessee, USA *2005-2008*
Research Staff Member, Computational Sciences and Engineering Division, Computing & Computational Sciences Directorate.
- **Georgia Institute of Technology**, Atlanta, Georgia, USA *2000-2005*
Research Faculty Member, College of Computing: Advised graduate students, supervised research assistants, taught senior undergraduate course, obtained externally sponsored research funding, served as co-PI on multiple projects, performed collaborative research within as well as outside Georgia Tech.
- **Georgia Institute of Technology**, Atlanta, Georgia, USA *1997-2000*
Research Scientist. Performed research in large-scale network modeling, high-performance simulation, compilers and DoD HLA federations.
- **Georgia Institute of Technology**, Atlanta, Georgia, USA *1993-1997*
Graduate Research Assistant. Researched ATM network simulation and parallel simulation.
- **Bellcore**, Morristown, New Jersey, USA *Summer 1996*
Technical Staff. Researched formal specification, modeling & simulation systems for wired & wireless networks; modeled large ATM PNNI networks.
- **Bellcore**, Red Bank, New Jersey, USA *Summer 1995*
Intern. Researched performance evaluation techniques for ATM networks.
- **Schlumberger**, Tulsa, Oklahoma, USA *Summer 1994*
Intern. Designed and developed network communication and visualization software for real-time data from oil exploration devices.

- **Institute for Simulation and Training**, Orlando, Florida, USA *1992-1993*
Developer. Team member for DoD Computer Generated Forces in distributed interactive (JSAF) battlefield simulation. Developed intelligent behavior for simulated tanks & soldiers. Investigated aggregation & disaggregation of entities.
- **University of Central Florida**, Orlando, Florida, USA *1991-1992*
Graduate Teaching Assistant. Taught sophomore-level computer programming class: teaching, preparing exams, grading exams and lab work.

IV. EDUCATION

A. Academic

- **Ph.D., Computer Science** *December 1999*
Georgia Institute of Technology, Atlanta, Georgia, USA
Thesis Title: “Techniques for Efficient Parallel Simulation and their Application to Large-Scale Telecommunication Network Models”
Advisor: Dr. Richard Fujimoto
- **M.S., Computer Science** *June 1993*
University of Central Florida, Orlando, Florida, USA
Thesis Title: “A Debugging Environment for the Parallel Virtual Machine (PVM)”
Advisor: Dr. Udaya Vemulapati
- **B.E., Mechanical Engineering** *May 1991*
Osmania University, Hyderabad, AP, India
Thesis Title: “Interactive Computation of the Critical Path Method”

B. Management Training

- **Completed the “Developing Leadership Potential (DLP)” Program** *March-July 2016*
Oak Ridge National Laboratory, Oak Ridge, Tennessee, USA
Overview: Curriculum designed to address advanced leadership needs in career development, personal strengths & challenges, leadership communication strategies, networking, senior leader expectations. Uses a variety of methods and venues to enhance ongoing learning and application. Provides support through networking, interaction with senior leaders, individual coaching and personal assessments.
Designed for: Individuals who are currently in a leadership role, have completed Management Boot Camp, are seeking to grow his/her leadership abilities, and identified as a high potential individual and as a potential successor for a higher level role.

- **Completed the “Management Boot Camp: Skills for Managers”** *February 2011*
American Management Association, Oak Ridge, Tennessee, USA
Coverage: Effective Communication, The Art of Influencing Others, Coaching for Performance, Motivation, Delegating for Growth and Development, Institutional Planning and Integrated Performance Management, Facilities and Space, Employee Recruitment, Strategic Planning and Execution, Time Keeping, Finance Fundamentals, Employee Discipline, Employee Performance Management
- **Completed the “Project Management Training”** *March 2010*
Oak Ridge National Laboratory, Oak Ridge, Tennessee, USA
- **Other training: “Unconscious Bias” and “Sexual Harrasment in Workplace”** *2010-present*
Oak Ridge National Laboratory, Oak Ridge, Tennessee, USA

C. Teamwork Training

- **Certified Scrum Master** *March 2018*
The Scrum Alliance for Scrum and Agile (www.scrumalliance.org)
- **Certified Scrum Product Owner** *May 2018*
The Scrum Alliance for Scrum and Agile (www.scrumalliance.org)
- **Certified Scrum at Scale Practitioner** *Nov 2018*
The Scrum Alliance for Scrum and Agile (www.scrumalliance.org)

V. AWARDS & RECOGNITION

A. Fellowships

- **Durham IAS Fellow**, awarded a research fellowship following a worldwide selection process, at the *Institute of Advanced Study (IAS), Durham University, United Kingdom*; fellowship period Jan-March 2015 (all expenses paid by fellowship).
www.dur.ac.uk/ias/fellows/furtherparticulars/

Completed a 3 month collaborative engagement on Durham campus with 7 fellows, under the Epiphany term theme *Emergence*. Interdisciplinary cohort of fellows spanned philosophy, art, anthropology, history, and economics.

B. Appointments

- **Member of National Academy of Sciences’ Panel** for Assessment of Computational Science at the Army Research Laboratory, July 2017.
Served on the July 2017 panel on Aberdeen Proving Grounds campus to review the computational science research programs and projects at the ARL.
- **Member of National Academy of Sciences’ Panel**, appointed for a two-year term as panel member on Information Science at the Army Research Laboratory, April 2015 – April 2017.
Served on the June 2015 panel on Aberdeen Proving Grounds and Adelphi campuses to advise on information science research directions of the ARL.

C. Awards

- **Division's Significant Technical Contribution Award**, “in recognition of the most Significant Technical Contribution by Advancing the Field of Reversible Computing,” Computer Science and Mathematics Division, Oak Ridge National Laboratory, December 2017.
- **Division's Best Publication Award**, Computational Sciences and Engineering Division, Oak Ridge National Laboratory, July 2014.
- **Best Paper Award Winner**, co-author, paper on *Simulating Billion-Task Parallel Programs* in the SCS Summer Simulation Conference, International Symposium on Performance Evaluation of Computer and Telecommunication Systems, July 2014.
- **Best Paper Award Finalist**, co-author, paper on *Efficiently Scheduling Multi-core Guest Virtual Machines on Multi-core Hosts in Network Simulation* in the ACM/IEEE/SCS Workshop on Principles of Advanced and Distributed Simulation, July 2011.
- **Best Paper Award Finalist**, co-author, paper on *Reversible Parallel Discrete Event Execution of Large-scale Epidemic Outbreak Models* in the ACM/IEEE/SCS Workshop on Principles of Advanced and Distributed Simulation, June 2010.
- **Best Paper Award Finalist**, co-author, paper on *Agent Simulations on Multi-GPU and Multi-Core Clusters* in the ICST International Conference on Simulation Techniques and Tools, March 2010.
- **DOE Career Award Winner, FY 2010-2015**, *ReveR-SES: Reversible Software Systems*, announced January 2010
 - one of the 69 selected from a national pool of over 2200 pre-proposal and over 1500 proposals
 - one of only two awarded in FY10 to national laboratories for research in advanced scientific computing research.
- **Best Paper Award Winner**, co-author, paper on *Agent Simulations on GPUs* in the SCS Spring Simulation Multi-Conference, April 2008.
- **Significant Event Award**, “Largest and Fastest PDES Executions on the Blue Gene,” Oak Ridge National Laboratory, September 2007.
- **Best Project Poster Winner**, member of team led by John Stovall in the “Electric Grid Real-time Monitoring & Visualization” project, among Oak Ridge National Laboratory-Directed R&D projects of 2003-2006, November 2006.
- **Best Paper Award Winner**, co-author, paper on *Plasma Simulations* in the ACM/IEEE/SCS Workshop on Parallel & Distributed Simulation, June 2005.
- **Best Paper Award Winner**, co-author, paper on *Updateable Simulation* in the ACM/IEEE/SCS Workshop on Parallel & Distributed Simulation, May 2002.
- **Best Paper Award Winner**, co-author, paper on *Reverse Computation* in the ACM/IEEE/SCS Workshop on Parallel & Distributed Simulation, May 1999.
- **Contest Winning Teams and Honorable Mentions**, Programming team coach, Georgia Tech: Coached ACM programming teams 1993-2000. Teams placed 1st among 60-70 teams

in ACM Southeast Regional Programming Contests in 1994, 1998 and 1999. Team placed 10th in 1995 World Finals. Teams placed 1st in IBM Java Challenge 1999 and 2000. www.acm.org/contest

- **Programming team member**, University of Central Florida: Team placed 2nd in the ACM Southeast Regional Programming Contest, 1992.

D. Award Nominations

- **UT-Battelle Awards Night**, nominated four consecutive years (2010-2013) as the best division-level candidate from the Computational Sciences and Engineering Division (of ~150 research staff members), Oak Ridge National Laboratory, in the category of scientific research.

VI. SPONSORED RESEARCH AND DEVELOPMENT

A. Funded Research Projects

- **Co-PI (ORNL), Oak Ridge National Laboratory Directed Research and Development**
“Deep CYBERIA: Application of Digital Twin Techniques to Operational Assets”, 2018-2020.
- **PI (ORNL), US Department of Energy**
“Development of Integrated Transmission, Distribution, and Communication (TDC) Models [Grid Modernization Laboratory Consortium (GMLC)]”, with Z. Huang (Lead PI, PNNL) and LLBL, NREL, ANL, SNL and INL. 2016–2019.
- **Co-PI (ORNL), Oak Ridge National Laboratory Directed Research and Development**
“Rapid Digital-Twin Development Framework for Quantitative Assessment of Grid Cyber Resilience”, 2018.
- **Co-PI (ORNL), A sponsor from the Intelligence Community**
“Fast Detection Algorithms for Parallel Execution using DLIB on GPUs”, 2017-2018.
- **Co-PI (ORNL), A sponsor from the Intelligence Community**
“High Performance Algorithms and Optimization of Image Registration Workloads on Multi-GPU, Multi-Core, and Mobile Platforms”, 2016-2018.
- **PI (ORNL), US Department of Energy**
“ReveR-SES: Reversible Software Execution Systems”, 2010–2015, DOE (ASCR) Early Career Award.
- **PI (ORNL), Oak Ridge National Laboratory Directed Research and Development**
“CloneX: Discrete Event Cloning at Exascale”, 2014–2016.
- **PI (ORNL), Department of Defense Army Research Laboratory**
“NetWarp-Frontline: VM/Cloud-based Discrete Event Simulations for military MANETs”, 2012–2013.

- **PI (ORNL), Caterpillar Inc.**
“Rocks3D-HPC”, 2011–2012.
- **PI (ORNL), Department of Defense Army Research Laboratory**
“Design of Discrete-Event Testbed for Systems Effects in Defense Communications”, 2011–2012.
- **PI (ORNL), Oak Ridge National Laboratory Directed Research and Development**
“Runtime Infrastructures for Discrete Event Execution at Peta-scale and Beyond”, 2009–2011.
- **PI (ORNL), Oak Ridge National Laboratory Program Development**
“High-Performance Discrete Computing Systems”, 2008–2009.
- **PI (ORNL), US Department of Homeland Security/Southeastern Regional Research Initiative**
“RealSim: HPC and Data-Enabled Solutions to Critical Applications,” 2007–2008, with B. Worley (Project Director, ORNL).
- **Co-PI (ORNL), Oak Ridge National Laboratory Program Development**
“Large-scale Simulation and Bio-Medical Technologies”, 2007–2008, with D. Hetrick (PI, ORNL), Barbara Beckerman (Co-PI, ORNL).
- **PI (ORNL), Oak Ridge National Laboratory Program Development**
“High-Performance Parallel Discrete Event Technologies”, 2006–2007.
- **Co-PI (ORNL), National Geo-Spatial Intelligence Agency**
“An Agile and Error-Resilient GIS Database Architecture for Uncertainty Management,” 2006–2008, with B. Bhaduri (PI), A. Ganguly (Co-PI), M. Shankar (Co-PI).
- **PI (ORNL), Oak Ridge National Laboratory Directed Research and Development**
“An Evolutionary Approach to Porting Applications to Peta-Scale Platforms”, 2006–2009, LDRD 07-024 with J. Vetter (Co-PI, ORNL).
- **PI (ORNL), Oak Ridge National Laboratory Program Development**
“Infrastructure Testbeds for High Performance Platforms”, 2006–2007.
- **Co-PI (ORNL) (*joined subsequent to funding*), Oak Ridge National Laboratory Directed Research and Development**
“Real-Time, Interconnection-Wide Power System Analysis & Visualization”, 2005–2006, LDRD 05-015, with J. Stovall (PI, ORNL), et al.
- **Co-PI (Georgia Tech), US National Science Foundation Information Technology Research**
“Collaborative Research: ITR: Global Multi-scale Kinetic Simulations of Earth’s Magnetosphere using Parallel Discrete Event Simulation”, 2003–2006, ATM-0326431 (Georgia Tech), with R. Fujimoto (PI, Georgia Tech), S. Pande (Co-PI, Georgia Tech), H. Karimabadi (PI, SciberNet Inc).
- **Co-PI (Georgia Tech), US Defense Advanced Research Projects Agency**
“Backplane-based Network Simulation & Emulation”, 2004–2005, with R. Fujimoto (PI, Georgia Tech), G. Riley (Co-PI, Georgia Tech), R. Sundaram (PI, Northeastern).

- **Co-PI (Georgia Tech), US Defense Advanced Research Projects Agency** Network Modeling & Simulation (NMS) Program
“A Backplane Approach to Flexible, Efficient Network Emulation”, 2000–2003, N66001-00-1-8934, with R. Fujimoto (PI, Georgia Tech).
- **Co-PI (Georgia Tech) (joined subsequent to funding), US Defense Advanced Research Projects Agency** Next Generation Internet (NGI) Program
“Scalable Self-Organizing Simulations”, 1996–1999, N66001-96-C-8530, with R. Fujimoto (PI, Georgia Tech), S. Bhatt (PI, Bellcore), M. Chen (PI, Boston U.), J. Cowie (Co-PI, Boston U.), D. Leskiw (PI, Ultra Corp.), D. Nicol (PI, Dartmouth), A. Ogielski (PI, Bellcore), E. Zegura (Co-PI).
- **Co-PI (Georgia Tech) (joined subsequent to funding), US National Science Foundation** “Simulation of Integrated Communication Systems”, 1995–1998, NCR-9527163, with R. Fujimoto (PI, Georgia Tech), S. Bhatt (PI, Bellcore), J. Kurose (PI, U. Mass.), D. Nicol (PI, Dartmouth), A. Ogielski (PI, Bellcore), D. Suvaine (PI, Rutgers), D. Towsley (PI, U. Mass.).

B. Unfunded Large-Size Proposals

- **Co-PI (ORNL), Intelligence Community** “[A Proposal on Image Processing],” with David Bolme (PI, ORNL).
- **PI (ORNL), National Institutes of Health** “Parallel Discrete Event Simulation of HIV Propagation”, 2015–2017, with Ume Abbas (PI, Cleveland Clinic Foundation).
- **PI (ORNL), US Department of Energy** Advanced Scientific Computing Research, FOA-1059, “Resiliency in Extreme Scale Computing Platforms: Anomaly Detection and Fault Avoidance Via Real-time Learning”, 2015–2018, with U. Ramachandran, A. Chatterjee and S. Pande (Georgia Institute of Technology), and V. Sarkar (Rice University).
- **PI (ORNL), US Department of Energy** Advanced Scientific Computing Research, LAB14-1088, “RX – Tools for Reconciling Reality-vs-Expectation in Extreme-scale Computing”, 2014–2016.
- **PI (ORNL), US Department of Energy** Advanced Scientific Computing Research, “NetWarp: High-fidelity Experimentation Environments for Analysis of Rapidly Evolving Software,” 2009–2012, with D. Jefferson (PI, Lawrence Livermore Laboratory), J. Liu (Florida International University).
- **PI (ORNL), US Defense Advanced Research Projects Agency** National Cyber Range (NCR) Program, “ORANGE-Net: A Real-Time Resilient and Scalable System for National Cyber Range,” 2008–2009, with Microsoft Inc., and NASA Jet Propulsion Laboratory.
- **PI (ORNL), US National Science Foundation** PetaScale Applications (PetaApps) Program, “Petascale Simulation of Large-Scale Transportation Cyberinfrastructure,” 2008–2012, with R. Fujimoto (PI, Georgia Tech), R. Bagrodia (PI, SNT Inc.).
- **Co-PI (ORNL), Oak Ridge Associated Universities** HPC Grant Program, “Solving Hard Cases of the Graph Isomorphism Problem in Parallel”, 2009–2011, with N. Deo (PI, UCF).

- **PI (Georgia Tech), US National Science Foundation** Networking Technologies and Systems (NeTS) Program, “Design of Sensor Networks using Constraints-based Specification and Simulation,” 2005–2008, with S. Das (PI, SUNY), C. Carothers (PI, RPI).
- **PI (Georgia Tech), US National Science Foundation** Formal and Mathematical Foundations (FMF) Program, “Real-time and Best-effort Solutions of Mixed Integer Programs,” 2004–2007, with M. Savelsbergh (Co-PI, Georgia Tech), J. Linderoth (PI, Lehigh), M. Ferris (PI, U.Wisconsin), A. Miller (Co-PI, U. Wisconsin).

VII. TEACHING

- **Faculty**, Georgia Institute of Technology, Summer 2003: Taught *Advanced Operating Systems* (CS4210) to senior-undergraduate and graduate computer science students.
 - Georgia Tech end-of-course student evaluations for this course:

Question	Rating (0-5)
1. COURSE SEEMED WELL PLANNED AND ORGANIZED	4.0
2. GOOD JOB COVERING COURSE OBJECTIVES/CONTENT	4.0
3. EXPLAINED COMPLEX MATERIAL CLEARLY	4.7
4. WAS APPROACHABLE AND WILLING TO ASSIST	4.9
5. ENCOURAGED STUDENTS TO CONSULT WITH HIM/HER	4.8
6. CLASS ATTENDANCE IMPORTANT IN PROMOTING LEARNING	4.7
7. NUMBER OF ASSIGNMENTS WAS REASONABLE	4.1
8. EXAMS COVERED COURSE CONTENT/OBJECTIVES	4.2
9. EXAMS WERE OF APPROPRIATE DIFFICULTY	4.0
10. THE INSTRUCTOR WAS AN EFFECTIVE TEACHER	4.7

- **Graduate Teaching Assistant**, Georgia Institute of Technology, Fall 1993–Fall 1994: Taught special section of an introductory course on computing; handled laboratory classes.
- **Instructor**, University of Central Florida, Spring 1992: Taught data structures and algorithms to junior computer science students.
- **Graduate Teaching Assistant**, University of Central Florida, Fall 1991: Handled laboratory classes for business majors on software tools such as databases, spreadsheets and word processors.

VIII. MENTORING

A. Post-doc and Post-Masters

- Varisara Tansakul, (M.S., Industrial and Systems Engineering), Oak Ridge National Laboratory, May’18-present:
 - Supervisor to Ms. Tansakul on multiple projects
- Maksudul Alam, (Ph.D., Computer Science), Oak Ridge National Laboratory, Dec’16-present:
 - Supervisor to Dr. Alam on multiple projects
- Alfred Park, (Ph.D., Computer Science), Oak Ridge National Laboratory, Nov’10-Oct’12:

- Supervisor to Dr. Park on multiple projects
- Sudip Seal, (Ph.D., Computer Science and Ph.D., Physics), Oak Ridge National Laboratory, Nov'07-Nov'09:
 - Supervisor to Dr. Seal on multiple projects
- Srikanth Yoginath (M.S., Computer Science), Georgia Institute of Technology and Oak Ridge National Laboratory, Jan'07-Jul'14
 - Supervisor to Dr. Yoginath as Group Leader and Ph.D. Advisor

B. Students

1. Doctoral Thesis Advisor

- Georgia Institute of Technology, Atlanta, GA – Advised as on-record thesis committee co-chair:
 - Srikanth Yoginath, 2007-2014 (currently at Discrete Computing Systems, ORNL)
Thesis Title: “[Virtual Time-Aware Virtual Machine Systems](#)”
Thesis Committee: K. Perumalla (advisor), R. Fujimoto (co-advisor), U. Ramachandran, D. Bader, G. Riley

2. Doctoral Thesis Committee Member

- University of Rome La-Sapienza, Rome, Italy:
 - Davide Cingolani, 2018
 Thesis title “Speculative Parallel Discrete Event Simulation” (Advisor: Prof. Francesco Quaglia)
- Virginia Polytechnic and State University (Virginia Tech):
 - Maksudul Alam, 2016
 Thesis title “Parallel Generation Techniques for Large Random Graphs” (Advisors: Profs. Maleq Khan and Madhav Marathe)
- Karlsruhe Institute of Technology, Karlsruhe, Germany:
 - Philipp Andelfinger, 2016
 Thesis title “Identifying and Harnessing Concurrency for Parallel and Distributed Network Simulation” (Advisor: Prof. Hannes Hartenstein)
- Georgia Institute of Technology, Atlanta, GA:
 - Dushmanta Mohapatra, 2015
 Thesis title “Coordinated Memory Management in Virtualized Environments” (Advisor: Prof. Kishore Ramachandran)
- EURECOM, Nice, France:

- Bilel Rodhamme, 2013
Thesis title “Simulation des Réseaux à grande Échelle sur les architectures de calculs hétérogènes”
- Georgia Institute of Technology, Atlanta, GA:
 - Alfred J. Park, 2009
Thesis title “Master-Worker Parallel Discrete Event Simulation” (Advisor: Prof. Richard Fujimoto)

3. Interns

- Oak Ridge National Laboratory – direct supervisor/mentor for student interns:
 - James Fox (Georgia Institute of Technology), Summer 2018, “Community Detection-based Generation of Electric Distribution Networks”
 - Amin Nikakhtar (Texas Tech University), Summer 2018-Fall 2018, on doctoral thesis research proposal titled “Polynomial Optimization for Optimal Power Flow and Assured Generation”, competitively selected under the U.S. DOE Office of Science Graduate Student Research Program
 - Till Koester (University of Rostock, Germany), Fall 2016-Spring 2017, “High-Performance Simulations of Cell Biology Models on Modern GPUs”
 - Maksudul Alam (Virginia Tech), Spring 2016, “Computational Support for Cloning Simulations”
 - Zhengchun Liu (U. Barcelona, Spain), Spring 2016, “High Performance Agent-based and Discrete Event Simulations”
 - Maksudul Alam (Virginia Tech), Summer 2015, “Efficient Index Searching on Modern GPUs”
 - Charles Elliott (CJ) Johnson (L&N STEM Academy), Summer 2015, “Message Passing Interface in JavaScript”
 - Masatoshi Hanai (Tokyo Institute of Technology), 2015, “Exact-Differential Simulations” (mentored while at Durham University, UK)
 - Melissa Yu (Farragut High School), Summer 2013, “Memory-Efficient Reversible Algorithms for Integer Square Root Computation”
 - Matthew Street (Middle Tennessee State University), Summer 2010, “Visualizing Rollbacks in Parallel Discrete Event Simulations”
 - Brandon Aaby (Maryville College), Summer 2008, “High Performance Hybrid Computing using Networks of Processors and GPGPUs”
 - Clayton Thurmer (Oberlin University), Spring 2008, “Discrete Event Simulator Benchmarking”
 - Brandon Aaby (Maryville College), Summer 2007, Fall 2007, “Social Science Simulations on GPGPUs”

- Patrick Wilkerson (Austin Peay State University), Summer 2007, “Parallel FDTD as Asynchronous Speculative Execution”
- John Wright (Mercer University), Summer 2007, “Reversibility of Computational Methods”
- Kathleen Abercrombie (Georgia Tech), Summer 2006, “Cut Selection in Sensor Network Optimization”
- Eric Beier (Texas A&M), Summer 2006, “Federated Optimization of Sensor Networks”
- Alfred Park (Georgia Tech), Summer 2006, “Integrated Analysis of Sensor Networks”
- Jason Roop (N. Carolina Central), Summer 2006, “Social Network Simulation using GPGPUs”

4. Other

- Georgia Institute of Technology – Special Topics (CS 8903) students:
 - Sivagowri Swaminathan, Summer 2005, “Incremental Mixed Integer Programming”
 - Ashok Babu Amara, Fall 2004, “Scalable Sensor Network Simulation”
 - Srikanth Sundargopalan, Fall 2003, “High-Fidelity Modeling of Computer Worms”
- Georgia Institute of Technology – Master’s thesis committee member:
 - Jagrut Dave, Fall 2004 – Spring 2005
- Georgia Institute of Technology – Supervised or advised six other students and graduate research assistants, jointly with Dr. Richard Fujimoto

C. Mentee Staff

- Oak Ridge National Laboratory
 - Ethan Coffey, Jan-Oct 2014 (Mentor Program) – mentored Ethan about career growth and research directions
 - Rajasekhar Karthik, Apr-Jun 2015 – mentored Rajasekhar about big data research

D. Contest Coaching

- **Programming Team Coach**, Georgia Institute of Technology *1994–2000*
Coached the programming teams to top places in regional and international **ACM programming contests**. Under my coaching:
 - Coached Georgia Tech to place in **top 5** in southeast region **4 times**.
 - Coached Georgia Tech to participate in **World Finals 3 times**.
- **Programming Team Member**, University of Central Florida *1991-1992*
 - Placed **8th** in 1991 in ACM Southeast Regional Contest.
 - Placed **2nd** in 1992 in ACM Southeast Regional Contest.

IX. PUBLICATIONS

A. H-Index and G-Index

- **H-index:** 31 (Google Scholar, as of October 22, 2018)
- **I10-index:** 58 (Google Scholar, as of October 22, 2018)
- **Citations:** 3013 (Google Scholar, as of October 22, 2018)

B. Books

1. Kalyan Perumalla, “Introduction to Reversible Computing,” *Computational Science Series*, Chapman & Hall/CRC Press, ISBN 978-143-9873-40-3, 325 pages, 2013.
www.crcpress.com/product/isbn/9781439873403
2. (*Monograph*) Richard Fujimoto, Kalyan Perumalla and George Riley, “Network Simulation,” *Synthesis Lectures on Communication Networks* (Jean Walrand, Editor), *Morgan & Claypool Publishers*, ISBN 1598291106, 2006.
www.morganclaypool.com/page/lectures/lectureList.jsp

C. Book Chapters

1. Kalyan Perumalla, “Computational Spectrum of Agent Model Simulation,” in *Modelling, Simulation and Optimization*, *IN TECH Publishers*, ISBN 978-953-7619-36-7, published April 2010.
2. Kalyan Perumalla, “Model Execution,” in *CRC Handbook of Dynamic System Modeling* (Paul Fishwick, Editor), ISBN 1584885653, 2007.
3. Homa Karimabadi, Yuri Omelchenko, Jonathan Driscoll, Richard Fujimoto and Kalyan Perumalla, “A New Methodology for Multi-Scale Simulation of Plasmas,” in *Lecture Book on Advanced Methods for Space Engineering*, January 2007.

D. Agency Reports

1. National Academies of Science, Engineering and Medicine Panel Report, “The 2017-2018 Assessment of the Army Research Laboratory,” May 2018, ISBN 978-0-309-47161-9,
www.nap.edu/25011 (contributor to Computational Sciences).
2. National Academies of Science, Engineering and Medicine Panel Report, “The 2015-2016 Assessment of the Army Research Laboratory,” April 2017, ISBN 978-0-309-45436-0,
www.nap.edu/24653 (contributor to Information Sciences).
3. I. Foster, T. Lehman, N. Rao, B. Lyles, I. Monga, P. Balaprakash, K. Perumalla, S. Prowell, R. Vatsavai, “Smart High-Performance Networks: Towards New Generation Intelligent Networking Infrastructure for Distributed Science Environments,” U.S. Department of Energy Workshop Report, December 2016 <https://www.ornl.gov/smartbp2016/Final-report.pdf>.
4. “Computational Modeling of Big Networks (COMBINE),” US DOE ASCR Workshop Report (contributor and co-organizer), December 2012
<https://www.nist.gov/sites/default/files/documents/itl/antd/DoE-Combine-Report.pdf>.

E. Journal Articles

1. Maksudul Alam, Maleq Khan, Kalyan Perumalla and Madhav Marathe, “Generating Massive Scale-Free Networks: Novel Parallel Algorithms using the Preferential Attachment,” in Transactions on Parallel Computing, June 2018 (*in review*)
2. Maksudul Alam and Kalyan Perumalla, “Novel Parallel Algorithms for Fast Multi-GPU-based Generation of Massive Scale-free Networks,” in Journal of Data Science and Engineering, March 2018 (*in review*)
3. Masatoshi Hanai, Toyotaro Suzumura, Elvis Liu, Georgios Theodoropoulos and Kalyan Perumalla, “Exact Differential Simulation: Differential Processing of Large-Scale Discrete Event Simulations”, ACM Transactions on Modeling and Computer Simulation, 2018 (*accepted*).
4. Srikanth Yoginath and Kalyan Perumalla, “Scalable Cloning on GPUs with Application to Time Stepped Simulation on Grids”, ACM Transactions on Modeling and Computer Simulation, January 2018.
5. Hunter Vallejos, James Nutaro and Kalyan Perumalla, “An Agent-Based Model of the Exponential-Pareto Distribution,” Journal of Economic Interaction and Coordination, Vol. 13, No. 3, pages 641-656, 2018.
6. S. Dinesh Kumar, Himanshu Thapliyal, Azar Mohammad and Kalyan Perumalla, “Design Exploration of a Symmetric Pass Gate Adiabatic Logic for Energy-Efficient and Secure Hardware”, Integration, the VLSI Journal, Vol. 58, pages 369-377, June 2017.
7. Kalyan Perumalla, Olama Mohammed and Srikanth Yoginath, “Model-Based Dynamic Control of Reversible Speculative Forays”, Elsevier Electronic Notes in Theoretical Computer Science, Vol. 327, pages 93-107, October 2016.
8. Srikanth Yoginath and Kalyan Perumalla, “Efficient Parallel Discrete Event Simulation on Cloud/Virtual Machine Platforms”, ACM Transactions on Modeling and Computer Simulation, Vol. 26, No. 1, pages 5:1-5:26, 2015.
9. Srikanth Yoginath and Kalyan Perumalla, “Virtual Machine-Based Simulation Platform For MANET-Based Cyber Infrastructure”, SCS Journal of Defense Modeling and Simulation, Vol. 12, No. 4, pages 439-456, 2015 [**A featured article of the journal**].
10. Kalyan Perumalla and Srikanth Yoginath, “Towards Reversible Linear Algebra Subprograms: A Performance Study”, Springer Transactions on Computational Science Special Issue on Reversible Computing, Vol. 24, pages 56-73, 2014.
11. Kalyan Perumalla, Alfred Park and Vinod Tipparaju, “Discrete Event Execution with One-Sided and Two-Sided GVT Algorithms on 216,000 Processor Cores”, ACM Transactions on Modeling and Computer Simulation, Vol. 24, No. 3, pages 16:1-16:25, 2014.
12. Kalyan Perumalla and Alfred Park, “Reverse Computation for Rollback-based Fault Tolerance in Large Parallel Systems”, Cluster Computing Journal, Vol. 17, No. 2, pages 303-313, 2014.
13. Alfred Park and Kalyan Perumalla, “Efficient Heterogeneous Execution on Large Multicore and Accelerator Platforms: Case Study Using a Block Tridiagonal Solver”, Journal of Parallel and Distributed Computing, Vol. 73, No. 12, pages 1578-1591, 2013.

14. Kalyan Perumalla and Vladimir Protopopescu, "Reversible Simulations of Elastic Collisions", ACM Transactions on Modeling and Computer Simulation, Vol. 23, No. 2, pages 12:1-12:25, 2013.
15. Sudip Seal, Kalyan Perumalla and Steven Hirshman, "Revisiting Cyclic Reduction and Parallel Prefix-Based Algorithms for Tri-diagonal Systems of Equations", Journal of Parallel and Distributed Computing, Vol. 73, No. 2, pages 273-280, 2013.
16. Sudip Seal, Kalyan Perumalla and Stephen P. Hirshman, "Scaling the SIESTA Magnetohydrodynamics Equilibrium Code," Concurrency and Computation: Practice and Experience, Vol. 25, No. 15, pages 2207-2223, 2013.
17. Kalyan Perumalla and Sudip Seal, "Discrete Event Modeling and Massively Parallel Execution of Epidemic Outbreak Phenomena," SIMULATION: Special Issue on Parallel and Distributed Simulation, Vol. 88, Issue 7, pages 768-783, 2012.
18. Kalyan Perumalla, Brandon Aaby, Srikanth Yoganath and Sudip Seal, "Interactive, Graphical Processing Unit-based Evaluation of Evacuation Scenarios at State-Scale," SIMULATION: Transactions of the Society for Modeling and Simulation International, Vol. 88, Issue 6, pages 746-761, 2012.
19. Sudip Seal and Kalyan Perumalla, "Reversible Parallel Discrete Event Formulation of a TLM-based Radio Signal Propagation Model", ACM Transactions on Modeling and Computer Simulation (TOMACS), Vol. 22, No. 1, pages 4:1-4:25, 2011.
20. Steven Hirshman, Kalyan Perumalla, Vickie Lynch and Raul Sanchez, "Bcyclic: A Parallel Block Tri-diagonal Matrix Cyclic Solver," Journal of Computational Physics, Volume 229, Issue 18, pages 6392-6404, 2010.
21. Kalyan Perumalla and Srikanth Yoganath, "Reversible Discrete Event Formulation and Optimistic Parallel Execution of Vehicular Traffic Models," International Journal of Simulation and Process Modeling, 2009.
22. Kalyan Perumalla, Richard Fujimoto, Homa Karimabadi, "Efficient Parallel Execution of Event-Driven Electromagnetic Hybrid Models," International Journal for Multi-scale Computational Engineering, Vol. 5(1), 2007.
23. Homa Karimabadi, Yuri Omelchenko, Jonathan Driscoll, Richard Fujimoto and Kalyan Perumalla, "A New Methodology for Multi-scale Simulation of Plasmas," in ISS-7 Lecture Notes on Advanced Methods for Space Simulations, Kyoto, Japan, 2007.
24. Yarong Tang, Kalyan Perumalla, Richard Fujimoto, Homa Karimabadi, Jonathan Driscoll and Yuri Omelchenko, "Optimistic Simulation of Physical Systems using Reverse Computation," SIMULATION: Transactions of the Society for Modeling and Simulation International, Vol. 82, No. 1, pages 61-73, 2006.
25. Homa Karimabadi, Jonathan Driscoll, Jagrut Dave, Yuri Omelchenko, Kalyan Perumalla, Richard Fujimoto and N. Omid, "Parallel Discrete Event Simulations of Grid-based Models – Asynchronous Electromagnetic Hybrid Code," in Springer Lecture Notes in Computer Science, 2005.
26. George Riley, Mostafa Ammar, Richard Fujimoto, Alfred Park, Kalyan Perumalla and Donghua Xu, "A Federated Approach to Distributed Network Simulation", ACM Transactions on Modeling and Computer Simulation (TOMACS), Vol. 14, No. 2, 2004.

27. Kalyan Perumalla and Richard Fujimoto, "Interactive Parallel Simulations with the JANE Framework", Future Generation Computer Systems, Vol. 17, No. 5, Elsevier Science, 2001.
28. Christopher Carothers, Kalyan Perumalla and Richard Fujimoto, "Efficient Optimistic Parallel Simulations using Reverse Computation," ACM Transactions on Modeling and Computer Simulation (TOMACS), Vol. 9, No. 3, 1999.
29. Sandeep Bhatt, Richard Fujimoto, Andrew Ogielski and Kalyan Perumalla, "Parallel Simulation Techniques for Large-scale Networks," IEEE Communications, Vol. 36, No. 8, 1998.
30. Kalyan Perumalla and Narsingh Deo, "Parallel Algorithms for Maximum Sub-sequence and Sub-array," Parallel Processing Letters, Vol. 5, No. 3, 1995.

F. Invited Articles

1. Kalyan Perumalla, "Magic, Miracle and Emergence on a Reversibility Spectrum," Insights Journal of the Durham University (*accepted*), 2018.
2. Kalyan Perumalla, "Concurrent Conversation Modeling and Parallel Simulation of the Naming Game in Social Networks," in Proceedings of Winter Simulation Conference (WSC), Las Vegas, NV, December 2017.
3. Kalyan Perumalla, "Relating the Limits of Computational Reversibility to Emergence," in Proceedings of the 9th International Conference on Reversible Computation (RC), July 2017.
4. Masatoshi Hanai, Toyotaro Suzumura, Georgios Theodoropoulos, and Kalyan Perumalla, "Towards Large-Scale What-If Traffic Simulation with Exact-Differential Simulation," in Proceedings of Winter Simulation Conference (WSC), Huntington Beach, CA, December 2015.
5. Srikanth Yoginath and Kalyan Perumalla, "Design of a High-Fidelity Testing Framework for Electric Grid Control Software," in Proceedings of Winter Simulation Conference (WSC), Savannah, GA, December 2014.
6. Kalyan Perumalla, "Tutorial: Parallel Simulation on Supercomputers," in Proceedings of Winter Simulation Conference (WSC), Berlin, Germany, December 2012.
7. Srikanth Yoginath, Kalyan Perumalla and Brian Henz, "Runtime Performance And Virtual Network Control Alternatives In Vm-Based High-Fidelity Network Simulations," in Proceedings of Winter Simulation Conference (WSC), Berlin, Germany, December 2012.
8. Christopher Carothers and Kalyan Perumalla, "On Deciding between Conservative and Optimistic Approaches on Massively Parallel Platforms," in Proceedings of Winter Simulation Conference (WSC), Washington, D.C., December 2010.
9. Kalyan Perumalla, "Switching to High Gear: Opportunities for Grand-scale Real-Time Simulations," in Proceedings of Distributed Simulations and Real-Time Applications (DS-RT), Singapore, October 2009.
10. Kalyan Perumalla, "Parallel and Distributed Simulation: Traditional Techniques and Recent Advances," in Proceedings of Winter Simulation Conference (WSC), Monterey, California, December 2006.

11. Alfred Park, Kalyan S. Perumalla, Vladimir Protopopescu, Mallikarjun Shankar, Frank DeNap and Bryan Gorman, "On Evaluation Needs of Real-life Sensor Network Deployments," in Proceedings of European Modeling and Simulation Symposium (EMSS), Barcelona, Spain, October 2006.
12. Kalyan Perumalla, "Parallel and Distributed Systems and the High-Level Architecture," Interservice/Industry Training, Simulation and Education Conference (IITSEC), Orlando, Florida, December 2005.
13. Kalyan Perumalla, Matthew Andrews and Sandeep Bhatt, "TeD Models for ATM Internetworks," ACM Performance Evaluation Review, Vol. 25, No. 4, March 1998.
14. Kalyan Perumalla, Andrew Ogielski, Richard Fujimoto, "TeD - A Language for Modeling Telecommunication Networks," ACM Performance Evaluation Review, Vol. 25, No. 4, March 1998.

G. Abstracts with Presentations

1. Kalyan Perumalla and Alan Burwell, "GPU-Accelerated Optimization of Flight Path Planning for Unmanned Aerial Systems," in INFORMS Computing Society Conference, Knoxville, Tennessee, January 2019.
2. Kalyan Perumalla, Maksudul Alam and Srikanth Yoginath, "EpiClone: Efficient GPU-based Cloning System for Massive What-If Decision Analyses of Epidemic Models," in INFORMS Computing Society Conference, Knoxville, Tennessee, January 2019.
3. Maksudul Alam and Kalyan Perumalla, "Scale-Free Networks: Fast GPU-based Generation and Applications," in INFORMS Computing Society Conference, Knoxville, Tennessee, January 2019.
4. Kalyan Perumalla and Jack Wells, "Computational Directions in Energy System Modeling, Simulation and Optimization," in Workshop on Macroeconomic Energy Systems Modeling and Optimization, Prague, Czechoslovakia, September 2016.
5. Srikanth Yoginath and Kalyan Perumalla, "Unique Simulation Test-bed Requirements to Realize Internet of Things (IoT)," in Future of Instrumentation and Internet Workshop, Washington, DC, May 2015.
6. Kalyan Perumalla, "Discrete Event Execution and Reversibility: Challenges in the Path to Asynchrony for Massively Parallel Computing," in SIAM Joint Mathematics Meeting, Boston, Massachusetts, January 2012.
7. Kalyan Perumalla and Christopher Carothers, "Compiler-based Automation Approaches to Reverse Computation," in Workshop on Reverse Computation, Atlanta, Georgia, June 2010.
8. Kalyan Perumalla, "High-Performance Simulations for Capturing Feedback and Fidelity in Complex Networked Systems," in SIAM Conference on Parallel Processing for Scientific Computing (PP10), Seattle, Washington, February 2010.
9. Kalyan Perumalla, Brandon Aaby, Srikanth Yoginath and Sudip Seal, "Towards Highly Interactive, GPU-based Evaluation of Evacuation Transport Scenarios at State-Scale," National Evacuation Conference, February 2010.
10. Kalyan Perumalla, "Cyber Security Experimentation: Gory Detail or None at All?" in SIAM Annual Meeting (AN), Denver, Colorado, July 2009.

11. Kalyan Perumalla, Richard Fujimoto, Santosh Pande, Homa Karimabadi, Jonathan Driscoll and Yuri Omelchenko, "Virtual Simulator: An Infrastructure for Design and Performance-Prediction of Massively Parallel Codes," in American Geophysical Union Fall Meeting (GU), San Francisco, California, December 2005.

H. Abstracts with Posters

1. Sudip Seal, Steven Hirshman and Kalyan Perumalla, "Scaling Optimization of the SIESTA MHD Code," abstract and poster at the 55th Annual Meeting of the American Physical Society, Division of Plasma Physics, Denver, CO, November 2013.
2. Kalyan Perumalla, "ReveR-SES: Reversible Software for Exascale," abstract and poster at Department of Energy Exascale PI Meeting, Arlington, VA, October 2012.
3. Sudip Seal, Kalyan Perumalla and Steven Hirshman, "Improved Parallelization of the SIESTA Magneto-hydrodynamic Equilibrium Code Using Cyclic Reduction," abstract and poster at the 53rd Annual Meeting of the American Physical Society, Division of Plasma Physics, Salt Lake City, Utah, November 2011.
4. Brandon Aaby and Kalyan Perumalla, "A Case Study of Efficient Social Network Simulation through General Processing on Graphics Processing Units (GPGPUs)," Journal of Undergraduate Research of the Department of Energy, October 2008.
5. Patrick Wilkerson and Kalyan Perumalla, "A Case Study of the Performance of Speculative Asynchronous Simulation on Parallel Computers," Journal of Undergraduate Research of the Department of Energy, October 2008.
6. John Wright and Kalyan Perumalla, "Two Case Studies in Reversibility of Computational Methods," Journal of Undergraduate Research of the Department of Energy, October 2008.

I. Refereed Conference/Workshop Papers

1. Srikanth Yoganath, Varisara Tansakul, Supriya Chintavali, Curtis Taylor and Kalyan Perumalla, "Effective Digital-Twin for Resilient Cyber-Physical Systems," in International Conference on Cyber-Physical Systems (in review)
2. Juan Lopez, Ryan Kerekes, Kalyan Perumalla, Mark Buckner, "Digital-Twin Development Framework for Quantitative Assessment of Power Grid Cyber-Resilience," in Proceedings of the IEEE SoutheastCon, Orlando, FL, April 2018.
3. Srikanth Yoganath and Kalyan Perumalla, "Efficient Reversible Uniform and Non-Uniform Random Number Generation in UNU.RAN," in Proceedings of the Spring Simulation Multi-Conference, April 2018.
4. Maksudul Alam and Kalyan Perumalla, "GPU-based Parallel Algorithm for Generating Massive Scale-free Networks using the Preferential Attachment Model," in Proceedings of the BigGraphs Workshop at the IEEE BigData Conference (IEEE BigGraphs), December 2017.
5. Till Köster, Kalyan Perumalla, Adelinde M. Uhrmacher, "Efficient Simulation of Nested Hollow Sphere Intersections: for Dynamically Nested Compartmental Models in Cell Biology," in Proceedings of the ACM International Conference on Principles of Advanced Discrete Simulation (SIGSIM-PADS), May 2017.

6. Maksudul Alam, Srikanth Yoginath and Kalyan Perumalla, “Performance of Point and Range Queries for In-Memory Databases using Radix Trees on GPUs,” in Proceedings of the International Conference on High Performance Computing and Communication, December 2016.
7. Kalyan Perumalla, Mohammed Olama and Srikanth Yoginath, “Model-based Dynamic Control of Speculative Forays in Parallel Computation,” in Proceedings of the International Workshop on Practical Applications of Stochastic Modeling, April 2016.
8. S. Dinesh Kumar, Himanshu Thapliyal, Azhar Mohammad, Vijay Singh, Kalyan Perumalla, “Energy-Efficient and Secure S-Box Circuit Using Symmetric Pass Gate Adiabatic Logic,” in Proceedings of the International Symposium on Very Large Scale Integrated Circuits (ISVLSI), 2016.
9. Masatoshi Hanai, Toyotaro Suzumura, Georgios Theodoropoulos, Kalyan Perumalla, “Exact-Differential Large-Scale Traffic Simulation,” in Proceedings of the ACM Conference on Principles of Advanced Discrete Simulation (SIGSIM-PADS), June 2015.
10. Kalyan Perumalla and Alfred Park, “Simulating Billion-Task Parallel Programs,” in Proceedings of International Symposium on Performance Evaluation of Computer and Telecommunication Systems (SPECTS), Monterey, CA, July 2014 [**Best Paper Award**].
11. Srikanth Yoginath and Kalyan Perumalla, “Empirical Evaluation of PDES Execution on Cloud and Virtual Machine Platforms,” in Proceedings of ACM SIGSIM Conference on Principles of Advanced Discrete Simulation (PADS), Montreal, Canada, May 2013.
12. Srikanth Yoginath and Kalyan Perumalla, “Optimized Hypervisor Scheduler for Parallel Discrete Event Simulations on Virtual Machine Platforms,” in Proceedings of ICST International Conference on Simulation Tools and Techniques (SimuTools), Cannes, France, March 2013.
13. Srikanth Yoginath, Kalyan Perumalla and Brian Henz, “Taming Wild Horses: The Need for Virtual Time-based Scheduling of VMs in Network Simulations,” in Proceedings of International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (MASCOTS), Washington, DC, August 2012.
14. Kalyan Perumalla, Alfred Park and Vinod Tipparaju, “GVT Algorithms and Discrete Event Dynamics on 129K+ Processor Cores,” in Proceedings of International Conference on High Performance Computing (HiPC), Bangalore, India, December 2011.
15. Kalyan Perumalla and Alfred Park, “Improving Multi-Million Virtual Rank MPI Execution in $\mu\pi$,” in Proceedings of International Symposium on Modeling and Simulation of Computing and Telecommunication Systems (MASCOTS), Singapore, July 2011.
16. Kalyan Perumalla, James Nutaro and Srikanth Yoginath, “Towards High Performance Discrete Event Simulations of Smart Electric Grids,” in Proceedings of International Workshop on High Performance Computing, Networking and Analytics for the Power Grid (HiPCNAG), Seattle, Washington, November 2011.
17. Srikanth Yoginath and Kalyan Perumalla, “Efficiently Scheduling Multi-core Guest Virtual Machines on Multi-core Hosts in Network Simulation,” in Proceedings of International Workshop on Principles of Advanced and Distributed Simulation (PADS), Nice, France, July 2011. [**Best Paper Finalist**]

18. Christopher Carothers and Kalyan Perumalla, "On Deciding between Conservative and Optimistic Approaches on Massively Parallel Platforms," in Proceedings of Winter Simulation Conference (WSC), Washington, DC, December 2010.
19. Kalyan Perumalla and Sudip Seal, "Reversible Parallel Discrete-Event Execution of Large-scale Epidemic Outbreak Models," in Proceedings of International Workshop on Principles of Advanced and Distributed Simulation (PADS), Atlanta, Georgia, May 2010. [**Best Paper Finalist**]
20. Kalyan Perumalla, " $\mu\pi$: A Scalable and Transparent System for Simulating MPI Programs," in Proceedings of ICST International Conference on Simulation Tools and Techniques (SimuTools), Malaga, Spain, March 2010.
21. Brandon Aaby, Kalyan Perumalla and Sudip Seal, "Efficient Simulations of Agent-Based Models on Multi-GPU and Multi-Core Clusters," in Proceedings of ICST International Conference on Simulation Tools and Techniques (SimuTools), Malaga, Spain, March 2010. [**Best Paper Finalist**]
22. Sudip Seal and Kalyan Perumalla, "Scalable Parallel Execution of an Event-based Radio Signal Propagation Model for Cluttered 3D Terrains," in Proceedings of International Conference on Parallel Processing, Vienna, Austria, September 2009.
23. Kalyan Perumalla and Jack Schryver, "A Connectionist Modeling Approach to Rapid Analysis of Emergent Social Cognition Properties in Large-Populations," in Proceedings of Human Behavior-Computational Modeling and Interoperability Conference, Oak Ridge, Tennessee, June 2009.
24. Kalyan Perumalla, "Efficient Execution on GPUs of Field-based Vehicular Mobility Models," in Proceedings of International Workshop on Principles of Advanced and Distributed Simulation, Lake Placid, New York, June 2009.
25. Richard Mills, Forrest Hoffman, Patrick Worley, Kalyan Perumalla, Art Mirin, Glenn Hammond and Barry Smith, "Coping at the User-Level with Resource Limitations in the Cray Message Passing Toolkit MPI at Scale: How Not to Spend Your Summer Vacation," in Proceedings of Cray User Group Meeting, Atlanta, Georgia, May 2009.
26. Srikanth Yognath and Kalyan Perumalla, "Parallel Vehicular Traffic Simulations using Reverse Computation-based Optimistic Execution," in Proceedings of International Workshop on Principles of Advanced and Distributed Simulation, Rome, Italy, June 2008.
27. Kalyan Perumalla and Brandon Aaby, "Data Parallel Execution Challenges and Runtime Performance of Agent Simulations on GPUs," in Proceedings of Spring Computer Simulation Conference, Ottawa, Canada, April 2008. [**Best Paper Award**]
28. Kalyan Perumalla and Martin Beckerman, "An Analysis Approach to Large-Scale Vehicular Network Simulations," in Proceedings of Summer Computer Simulation Conference, San Diego, California, June 2007.
29. Kalyan Perumalla, "Scaling Time Warp-based Discrete Event Execution to 10^4 Processors on a Blue Gene Supercomputer," in Proceedings of ACM Computing Frontiers, Ischia, Italy, May 2007.
30. Kalyan Perumalla, "A Systems Approach to Scalable Transportation Network Modeling," in Proceedings of Winter Simulation Conference (WSC), Monterey, California, December 2006.

31. Kalyan S. Perumalla, "On Accounting for the Interplay of Kinetic and Non-Kinetic Aspects of Population Mobility Models," in Proceedings of European Modeling and Simulation Symposium (EMSS), Barcelona, Spain, October 2006.
32. Kalyan S. Perumalla, "Parallel Execution of Region-Scale Evacuation Traffic Models," in Proceedings of International Workshop on Principles of Advanced and Distributed Simulation, Singapore, May 2006.
33. Kalyan S. Perumalla, "Discrete-Event Execution Alternatives on GPGPUs," in Proceedings of International Workshop on Principles of Advanced and Distributed Simulation, Singapore, May 2006.
34. Kalyan S. Perumalla, Richard M. Fujimoto, Homa Karimabadi, "Scalable Simulation of Electromagnetic Hybrid Codes," in Proceedings of International Conference on Computational Science, Reading, United Kingdom, May 2006.
35. Kalyan S. Perumalla, Richard M. Fujimoto, Prashant Thakare, Santosh Pande, Homa Karimabadi, Yuri Omelchenko, Jonathan Driscoll, "Performance Prediction of Large-scale Parallel Discrete Event Models of Physical Systems," in Proceedings of Winter Simulation Conference, Orlando, Florida, December 2005.
36. Kalyan S. Perumalla, Richard M. Fujimoto, Santosh Pande, Homa Karimabadi, Jonathan Driscoll, Yuri Omelchenko, "Virtual Simulator: An Infrastructure for Design and Performance-Prediction of Massively Parallel Codes," in Proceedings of Eos Transactions, American Geophysical Union, Fall Meeting Supplement, San Francisco, California, December 2005.
37. Homa Karimabadi, Yuri Omelchenko, Jonathan Driscoll, Richard Fujimoto and Kalyan Perumalla, "A New Simulation Technique for Study of Collision-less Shocks: Self Adaptive Simulations," in Proceedings of 4th Annual International Astrophysics Conference (IGPP), Palm Springs, CA, June 2005.
38. Homa Karimabadi, Yuri Omelchenko, Jonathan Driscoll, Richard Fujimoto and Kalyan Perumalla, "A New Methodology for Multi-scale Simulation of Plasmas," in Proceedings of 7th International Symposium for Space Simulations (ISSS), Kyoto, Japan, March 2005.
39. Yarong Tang, Kalyan Perumalla, Richard Fujimoto, Homa Karimabadi, Jonathan Driscoll and Yuri Omelchenko, "Optimistic Parallel Discrete Event Simulations of Physical Systems using Reverse Computation," in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Monterey, CA, May 2005. **[Best Paper Award]**
40. Kalyan Perumalla, "μsik – A Micro-kernel for Parallel/Distributed Simulation Systems," in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Monterey, CA, May 2005.
41. Kalyan Perumalla and Srikanth Sundaragopalan, "High Fidelity Modeling of Computer Network Worms," in Proceedings of Annual Computer Security Applications Conference (ACSAC), Tucson, AZ, December 2004.
42. Homa Karimabadi, Yuri Omelchenko, Jonathan Driscoll, N. Omid, Richard Fujimoto and Kalyan Perumalla, "A New Approach to Modeling Physical Systems: Discrete Event Simulations of Grid-based Models," in Proceedings of Workshop on State-Of-The-Art in Scientific Computing (PARA), June 2004.

43. Alfred Park, Richard Fujimoto and Kalyan Perumalla, “Conservative Synchronization of Large-scale Network Simulations,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Austria, June 2004.
44. Richard Fujimoto, Kalyan Perumalla, Alfred Park, Hao Wu, Mostafa Ammar, and George Riley, “Large-Scale Network Simulation – How Big? How Fast?” in Proceedings of IEEE/ACM International Symposium on Modeling, Analysis and Simulation of Computer Telecommunication Systems (MASCOTS), October 2003.
45. Weidong Shi, Kalyan Perumalla and Richard Fujimoto, “Power-aware State Dissemination in Mobile Distributed Virtual Environments,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), San Diego, California, June 2003.
46. Kalyan Perumalla, Alfred Park, Richard Fujimoto and George Riley, “Scalable RTI-based Parallel Simulation of Networks,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), San Diego, California, June 2003.
47. Kalyan Perumalla, and Richard Fujimoto, “Using Reverse Circuit Execution for Efficient Parallel Simulation of Logic Circuits,” in Proceedings of The International Society for Optical Engineering (SPIE) Annual Meeting, Seattle, Washington, July 2002.
48. Kalyan Perumalla, Richard Fujimoto, Thom McLean and George Riley, “Experiences Applying Parallel and Interoperable Network Simulation Techniques in On-line Simulations of Military Networks,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Washington, D.C., May 2002.
49. Steve Ferenci, Richard Fujimoto, Mostafa Ammar, Kalyan Perumalla and George Riley, “Updateable Simulations,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Washington, D.C., May 2002. **[Best Paper Award]**
50. Kalyan Perumalla, “Web Services for Extensible Modeling and Simulation,” in Proceedings of Workshop on Extensible Modeling and Simulation Framework (XMSF), Monterey, California, August 2002.
51. Kalyan Perumalla and Richard Fujimoto, “Virtual Time Synchronization over Unreliable Network Transport,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Lake Arrowhead, California, May 2001.
52. George Riley, Mostafa Ammar, Richard Fujimoto, Donghua Xu and Kalyan Perumalla, “Distributed Network Simulations using the Dynamic Simulation Backplane,” in Proceedings of the International Conference on Distributed Computing Systems, April 2001 (ICDCS), April 2001.
53. Richard Fujimoto, Thom McLean, Kalyan Perumalla and Ivan Tadic, “Design of High-performance RTI software,” in Proceedings of Distributed Simulations and Real-time Applications (DS-RT), August 2000.
54. Steve Ferenci, Kalyan Perumalla and Richard Fujimoto, “An Approach to Federating Parallel Simulators,” in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Bologna, Italy, May 2000.
55. Christopher Carothers, Kalyan Perumalla and Richard Fujimoto, “The Effect of State Saving in Optimistic Simulation on a Cache-coherent Non-uniform Memory Access (CC-NUMA) Architecture,” in Proceedings of the Winter Simulation Conference, December 1999.

56. Christopher Carothers, Kalyan Perumalla and Richard Fujimoto, "Efficient Optimistic Parallel Simulation using Reverse Computation," in Proceedings of ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation (PADS), Atlanta, Georgia, May 1999. **[Best Paper Award]**
57. Jeff Linderth, Kalyan Perumalla and Martin Savelsbergh, "PARINO: A Parallel Branch and Cut Code," in Proceedings of INFORMS National Meeting, Cincinnati, Ohio, May 1999.
58. Kalyan Perumalla and Richard Fujimoto, "Efficient Large-scale Process-oriented Parallel Simulations", in Proceedings of the Winter Simulation Conference, December 1998.
59. Ioannis Nikolaidis, Anthony Cooper, Kalyan Perumalla and Richard Fujimoto, "Time Parallel Generation of Self-similar ATM Traffic," in Proceedings of the Winter Simulation Conference, December 1997.
60. Kalyan Perumalla, Matthew Andrews and Sandeep Bhatt, "A Virtual PNNI Network Testbed," in Proceedings of the Winter Simulation Conference, December 1997.
61. Martin Savelsbergh, Kalyan Perumalla, Jeff Linderth, Umakishore Ramachandran, "PARINO, A Parallel Integer Optimizer", in Proceedings of International Symposium on Mathematical Programming, August 1997.
62. Kalyan Perumalla, Anthony Cooper, Richard Fujimoto, "An Efficiency Prediction Method for ATM Multiplexer," in Proceedings of Broadband Communications, April 1996.
63. Ivan Yanasak, Gautam Shah, Kalyan Perumalla, et al, "Parallelizing Sequential Algorithms for the Generalized Assignment Problem," DIMACS Challenge of Parallel Computing, Rutgers University, October 1994.
64. Kalyan Perumalla and Narsingh Deo, "Parallel Algorithms for Maximum Sub-sequence and Sub-array," in Proceedings of International Conference on Combinatorics, Graph Theory and Computing, West Palm Beach, April 1994.
65. Sridhar Hannenhalli, Kalyan Perumalla and Narayan Chandrasekharan, "A Distributed Algorithm for Ear Decomposition," in Proceedings of International Conference on Computing and Information (ICCI), 1993.
66. Uday Vemulapati and Kalyan Perumalla, "A Debugging Environment for PVM," Distributed Computing for Aerospace Applications, October 1993.
67. Clark Karr, Robert Francescini and Kalyan Perumalla, "Integrating Aggregate and Vehicle Level Simulations," in Proceedings of the 3rd Conference on Computer Generated Forces and Behavioral Representation, March 1993.
68. Clark Karr, Robert Francescini and Kalyan Perumalla, "Integrating Battlefield Simulations of Different Granularity," in Proceedings of the Southeastern Simulation Conference, 1992.

J. Technical Reports

1. Maksudul Alam, and Kalyan Perumalla, "Generating Billion-Edge Scale-Free Networks in Seconds: Performance Study of a Novel GPU-based Preferential Attachment Model," Technical Report ORNL/TM-2017/486, Oak Ridge National Laboratory, October 2017.
2. Kalyan Perumalla, "Computing a Non-Trivial Lower Bound on the Joint Entropy of Two Images," Technical Report ORNL/TM-2017/85, Oak Ridge National Laboratory, March 2017.

3. Kalyan Perumalla, Maksudul Alam, and Devin White, "Computational Speed and Matching Quality using an Upper Bound on the Normalized Mutual Information (NMI) between Two Images," Technical Report ORNL/TM-2017/87, Oak Ridge National Laboratory, May 2017.
4. Maksudul Alam, and Kalyan Perumalla, "GPU-based Parallel Algorithms for Generating Massive Scale-Free Networks using Preferential Attachment Model," Technical Report ORNL/TM-2017/100, Oak Ridge National Laboratory, September 2017 (in review).
5. Srikanth Yoginath and Kalyan Perumalla, "Reversible Non-Uniform Random Number Generator," Technical Report ORNL/TM-2015/335, Oak Ridge National Laboratory, September 2015.
6. Kalyan Perumalla and Vladimir Protopopescu, "Reversible Simulations of Elastic Collisions," Cornell University Library [arXiv:1302.1126 \[physics.comp-ph\]](https://arxiv.org/abs/1302.1126), arxiv.org/abs/1302.1126, February 2013.
7. Srikanth Yoginath, Kalyan Perumalla, Paul Williams and Richard Bass, "An Incremental Parallelization Approach Applied to the ORNL/NRC FAVOR Code," Technical Report ORNL/TM-2010/176, Oak Ridge National Laboratory, September 2010.
8. Sudip K. Seal and Kalyan Perumalla, "Scalable Parallel Execution of an Event-based Radio Signal Propagation Model for Cluttered 3D Terrains," Technical Report ORNL-2009/165, Oak Ridge National Laboratory, September 2009.
9. Kalyan Perumalla, John Wright and Phani Kuruganti, "On the Reversibility of Newton-Raphson Root-Finding Method," Technical Report ORNL-2007/152, Oak Ridge National Laboratory, July 2008.
10. Alfred Park and Kalyan Perumalla, "Integrated Analysis of Environment-Driven Operational Effects in Sensor Networks," Technical Report, Oak Ridge National Laboratory, August 2006.
11. Kalyan Perumalla, "Generating Perfect Reversals of Simple Linear Codes," Technical Report GIT-CERCS-TR-03-04, Center for Experimental Research in Computing Systems, Georgia Institute of Technology, May 2003.
12. Kalyan Perumalla, "Techniques for Improving Accuracy and Usability in Large-scale Network Emulation," Technical Report GIT-CC-03-04, College of Computing, Georgia Institute of Technology, January 2003.
13. Richard Fujimoto, Kalyan Perumalla and Liang Xiao, Giorgio Casinovi, Madhavan Swaminathan, Siddharth Dalmia, J. Mao, "Parallel Simulation Backplanes for Mixed Signal Circuit Design," Yamacraw Research Report, Georgia Institute of Technology, IAB-10-2000, October 2000.
14. Kalyan Perumalla and Richard Fujimoto, "Source Code Transformations for Efficient Reversibility," Technical Report GIT-CC-99-21, College of Computing, Georgia Institute of Technology, September 1999.
15. Kalyan Perumalla, Martin Savelsbergh and Umakishore Ramachandran, "PARINO: An Extensible Framework for Solving Mixed Integer Programs in Parallel," Technical Report GIT-CC-97-07, College of Computing, Georgia Institute of Technology, March 1997.
16. Kalyan Perumalla and Richard Fujimoto, "GTW++ -- An Object Oriented Interface in C++ to the Georgia Tech Time Warp System," Technical Report GIT-CC-96-09, College of Computing, Georgia Institute of Technology, March 1996.

17. Kalyan Perumalla, Richard Fujimoto and Andrew Ogielski, "Meta TeD – A Meta Language for Modeling Telecommunication Networks," Technical Report GIT-CC-96-32, College of Computing, Georgia Institute of Technology, December 1996.
18. Kalyan Perumalla and Richard Fujimoto, "A C++ Instance of TeD," Technical Report GIT-CC-96-33, College of Computing, Georgia Institute of Technology, December 1996.
19. C. Anthony Cooper and Kalyan Perumalla, "A Performance Prediction Method for ATM Multiplexers," Technical Memorandum TM-25152, Bell Communications Research (Bellcore), August 1995.

K. Drafts in Preparation

1. With Borges Hink et al, "A Digital Twin Framework for Testing, Evaluation and Deployment of Resilient Cyberphysical Ssystems," in IEEE Access Journal, 2018.
2. Srikanth Yoginath, Maksudul Alam, Aravind Ramanathan and Kalyan Perumalla, "DeepEx: Deep-Learning at Exascale," in Journal of Parallel and Distributed Computing Special Issue, 2018.
3. Srikanth Yoginath, Maksudul Alam, Aravind Ramanathan and Kalyan Perumalla, "DeepEx: Scalable Variational Autoencoders Performance Studies," Technical Memorandum, Oak Ridge National Laboratory, 2018
4. Mark Buckner, Juan Lopez, Ryan Kerekes, et al, "Experiences and Insights in Applying Scrum for Research," in Software Engineering, 2018.
5. With Doug Kothe et al, "Exascale Applications: Opportunities, Challenges, Requirements," 2018.
6. Kalyan Perumalla and Christopher Johnson, "Message Passing Interface over JavaScript" 2018.

X. SERVICE & ACTIVITIES

A. Tutorials

1. "Reversible Programming Paradigms and Models," Tutorial, International Conference on Reversible Computation (RC), Kolkata, India, July 2017.
2. "Parallel Discrete Event Simulation," Tutorial, International Conference on High Performance Computing and Simulation (HPCS), Bologna, Italy, July 2014.
3. "Introduction to Reversible Computing," Tutorial, International Conference on High Performance Computing and Simulation (HPCS), Bologna, Italy, July 2014.
4. "Parallel Discrete Event Simulation using Supercomputers, Cloud/VMs, and Accelerators," Tutorial, ACM SIGSIM Principles of Advanced Discrete Simulation (PADS), Denver, CO, May 2014.
5. "Parallel Simulation on Supercomputers," Tutorial, Winter Simulation Conference (WSC), Berlin, Germany, December 2012.

6. "Introduction to Simulation on GPUs," Tutorial, IEEE/ACM International Symposium on Distributed Simulations and Real-time Applications (DS-RT), Singapore, October 2009.
7. "Agent-based Simulations using GPUs," Tutorial, IEEE/ACM/SCS Principles of Advanced and Distributed Simulation (PADS), Lake Placid, NY, June 2009.
8. "Handling Time Management under the HLA," Tutorial, Interservice/Industry Training, Simulation and Education Conference (I/ITSEC), Orlando, Florida, December 2007.
9. "Parallel and Distributed Simulation: Traditional Techniques and Recent Advances," Tutorial, IEEE/ACM/SCS Principles of Advanced and Distributed Simulation (PADS), San Diego, California, June 2007.
10. "Parallel and Distributed Simulation: Traditional Techniques and Recent Advances," Tutorial, Winter Simulation Conference (WSC), Monterey, California, December 2006.
11. "Handling Time Management under the HLA," Tutorial, Interservice/Industry Training, Simulation and Education Conference (I/ITSEC), Orlando, Florida, December 2006.
12. "Distributed Simulation Systems & the High Level Architecture (HLA)," Tutorial, Interservice/Industry Training, Simulation and Education Conference (I/ITSEC), Orlando, Florida, December 2005.
13. "Distributed Simulation Systems & the High Level Architecture (HLA)," Tutorial, Interservice/Industry Training, Simulation and Education Conference (I/ITSEC), Orlando, Florida, December 2004. [*Presented to over 115 attendees from industry, government & military*].
14. "The High Level Architecture for Simulation," Tutorial, ACM/IEEE/SCS Workshop on Parallel and Distributed Simulation, Atlanta, Georgia, May 1999 (co-presented with R. Fujimoto, K. Morse and R. Weatherly).

B. Distinguished/Public Lectures and Keynotes

- "Relating the Limits of Computational Reversibility to Emergence," Invited Keynote Talk, 9th International Conference on Reversible Computation (RC), www.reversible-computation.org, Kolkata, India, July 2017.
- "Effective Exascale Computing using Computational Cloning," Distinguished Lecture Series Talk, Michigan Technological University, MI, April 2017.
- "Theoretical and Practical Relations between Low Energy Computation and Reversible Computing Software," Invited Keynote Talk, IEEE International Symposium on Nanoelectronic Information Systems (IEEE-iNIS), www.ieee-inis.org, India, December 2016.
- "From Desktops to Supercomputers: The Wide Computational Spectrum in Simulating Emergence," Invited Public Lecture, Durham University, Durham, UK, March 2015.
- "Reversible Computing Software for Large-scale Parallel Execution," Distinguished Lecture, Florida International University, Miami, FL, November 2013.
- "Switching to High Gear: Opportunities for Grand-scale Real-time Parallel Simulations," Invited Keynote at the IEEE/ACM Distributed Simulations and Real-time Applications (DS-RT), Singapore, October 2009.

C. Invited Talks and Seminars

- “Towards Enabling GPU-Accelerated Massively Parallel Mixed Integer Programming,” Department of Industrial and Systems Engineering, University of Tennessee, Knoxville, TN, November 2018.
- “On Enabling and Exploiting Reversibility in Large-scale High-Performance Computing,” Innovative Computing Laboratory, University of Tennessee, Knoxville, TN, December 2015.
- “Reversible Software Execution Systems,” DOE Advanced Scientific Computing Research Advisory Board, Washington, DC, December 2015.
- “Intersections of Emergence with Reversibility and Simulation,” Institute of Advanced Study, Durham University, Durham, UK, February 2015.
- “Computational Challenges in the Design of Secure Smart Electric Grids of the Future,” Durham University, Durham, UK, February 2015.
- “Reversible Computing: Revisiting and Revising Certain Critical Assumptions in Serial and Parallel Computing,” University of Kentucky, Lexington, Kentucky, December 2014.
- “ReveR-SES: Reversible Software Execution Systems,” (plenary) DOE Exascale Computing PI Meeting, Washington, DC, October 2012.
- “ReveR-SES: Reversible Software Execution Systems,” DOE Advanced Scientific Computing Research Advisory Board, Washington, DC, November 2011.
- “Supercomputing Applications of the other Kind: Real-time Parallel Discrete Event Simulations of Large, Smart Infrastructures,” IBM T.J.Watson Research Center, York Town, New York, July 2010.
- “High Performance Computing-based Experimentation for Cyber Infrastructure and Security,” Lawrence Livermore National Laboratory, Livermore, California, July 2008.
- “Feasibility, Efficiency and Limits of Compiler-based Automation for Reversibility of Codes,” Lawrence Livermore National Laboratory, Livermore, California, July 2008.
- “Application-Level Asynchronous Speculative Execution,” IBM T.J.Watson Research Center, York Town, New York, September 2007.
- “Computational Tools for Efficient Large-scale Discrete-Event Models,” Oak Ridge National Laboratory, Oak Ridge, Tennessee, May 2005.
- “Computational Methods for Efficient Large-scale System Models,” Indiana University Purdue University, Indianapolis, Indiana, March 2005.
- “Achieving Interoperability and Scalability in Simulation of Networks,” University of Louisville, Louisville, Kentucky, February 2003.
- “Using Reverse Computation towards Efficient Parallel/Distributed Computation,” Bell Labs, Lucent Technologies, Murray Hill, New Jersey, May 2000.
- “Using Reverse Computation towards Efficient Parallel/Distributed Computation,” IBM T. J. Watson Research Center, York Town, New York, April 2000.

- “Using Reverse Computation towards Efficient Parallel/Distributed Computation,” Purdue University, Lafayette, Indiana, April 2000.
- “Using Reverse Computation towards Efficient Parallel/Distributed Computation,” University of Arizona, Tucson, Arizona, April 2000.
- “Using Reverse Computation towards Efficient Parallel/Distributed Computation,” University of Central Florida, Orlando, Florida, April 2000.
- “Towards Reusable Modeling and Parallel Simulation of Telecommunication Networks,” Bell Labs, Lucent Technologies, Murray Hill, New Jersey, February 1998.
- “Reusable Modeling and Parallel Simulation of Networks using the TeD Language,” WINLAB, Rutgers University, Piscataway, New Jersey, January 1997.

D. Professional Board Membership, Chairing, and Committees

- **Editorial Board Member** (Associate Editor), ACM Transactions on Modeling and Computer Simulation (**TOMACS journal**) 2008-present.
- **Editorial Board Member** (Associate Editor), SCS Transactions of the Society for Modeling and Simulation International (**SIMULATION journal**) 2008-present.
- **Program Committee Member** (partial list – not up-to-date)
 - International Conference on Systems Simulation (**AsiaSim**) 2013, 2014.
 - ACM SIGSIM Conference on Principles of Advanced and Discrete Simulation (**PADS**) 2013.
 - IEEE/SCS/ACM International Workshop on Principles of Advanced and Distributed Simulation (**PADS**) continuously from 2003 to 2012.
 - International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (**MASCOTS**) 2001, 2005, 2010-13.
 - International Conference on Algorithms and Architectures for Parallel Processing (**ICA3PP**) 2012.
 - International Conference on Distributed Simulation and Real-time Applications (**DS-RT**) 2010-12.
 - International Conference on High Performance Computing and Communication (**HPCC**) 2012.
 - International Conference on Simulation Tools and Techniques (**SimuTools**) 2010-14.
 - International Conference on High Performance Computing, Storage and Networking (**Supercomputing**) 2008.
- **Steering Committee Member**, International Workshop on Principles of Advanced and Distributed Simulation (**PADS**) 2007-2011.

E. Professional Leadership

- **Judge**, multiple venues over the years; recently on judging panel of doctoral consortium and poster judging teams at the **Tapia Conference** on Diversity in Computing 2018.

- **Track Chair**, Scientific Computing track, Winter Simulation Conference (**WSC**), Savannah, GA, 2014.
- **Program Chair**, International Conference on Simulation Tools and Techniques (**SimuTools**), Lison, Portugal, 2014.
- **Program Chair**, International Symposium on Modeling, Analysis and Simulation of Computer and Telecommunication Systems (**MASCOTS**), Washington, DC, 2012.
- **Guest Editor**, Special Issue on Best of PADS'07, SCS Transactions of the Society for Modeling and Simulation International (**SIMULATION journal**), 85(4), 2009.
- **Program Chair**, Symposium on Asynchronous Methods in Scientific and Mathematical Computing (**ASYM**), San Diego, CA, 2007.
- **Program Chair**, International Workshop on Principles of Advanced and Distributed Simulation (**PADS**), San Diego, CA 2007.

F. Organizational Leadership and Service

- **ACM SIGSIM Chair Nomination**, nominated to the election slate for the chair position of the Association for Computing Machinery Special Interest Group in Simulation 2018 (<https://www.acm.org/elections/sigs/elections>).
- **Team Lead, Creativity and Innovation Team**, Oak Ridge National Laboratory, Division-level team to investigate development of creativity and innovation in research and development, August-September 2015.

Led nine-member team of research staff assigned to analyze and make recommendations to top management on increasing scientific creativity and innovation in the Computational Sciences and Engineering division consisting of 150 research staff (report of findings and recommendation available upon request).

- **Seed Money Review Committee Voting Member**, Oak Ridge National Laboratory Director's Research and Development Funds for Strategic Development, 2006-2007.

Served on the institutional committee consisting of top scientists appointed by top management to evaluate and make recommendations on proposals from scientists across the entire laboratory for "seed projects" with futuristic scientific potential.

G. Reviewing

Served as reviewer for the several journals and conferences over many years.

The following is a **partial list** to illustrate the technical areas:

- Transactions of the Society for Modeling & Simulation Intl. (**SIMULATION**).
- Journal of Parallel and Distributed Computing (**JPDC**)
- Information Processing Letters (**IPL**)
- Interscience Wiley Journal on Software Practice & Experience (**SP&E**)
- IEEE Journal on Computer Communications (**CC**)
- IEEE Conference on Distributed Simulation & Real-time Applications (**DS-RT**)
- ACM Transactions on Modeling and Computer Simulation (**TOMACS**)

- ACM/IEEE Workshop on Parallel and Distributed Simulation (**PADS**)
- INFORMS Winter Simulation Conference (**WSC**)
- IEEE Transactions on Parallel and Distributed Systems (**TPDS**)
- IEEE Conference on Computer Communications (**INFOCOM**).

H. Research Software (Partial List)

<i>Package</i>	<i>Description</i>	<i>Impact</i>
MutEnt	Novel mutual entropy computation code for highly scalable and efficient computation of image registration operations for large sized, high volume images.	<ul style="list-style-type: none"> ■ Runs on CPU and GPU platform (C++, CUDA) ■ Beats the best-known open source implementations available in OpenCV.
DeepEx	Manager for novel ORNL code for Deep Learning that is designed for very light software footprint, scaling to large heterogeneous platforms (GPU and multicore CPU), highly portable compiled implementation for high performance.	<ul style="list-style-type: none"> ■ Runs on supercomputing platforms with GPUs and CPUs (C++, MPI, CUDA – CUDNN, NCCL) ■ Tested on many deep learning networks (VGGNet, etc.), and image data sets.
CUPPA	Co-developed the first GPU-based and multi-GPU-based scale-free graph generator. Demonstrated to generate graphs with billions of edges in seconds.	<ul style="list-style-type: none"> ■ Runs on NVIDIA GPUs (code written in C++, CUDA).
RBLAS	Reversible version of basic linear algebra subprograms (BLAS) interface and implementation that works over traditional (irreversible) BLAS.	<ul style="list-style-type: none"> ■ The only available reversible linear algebra library. Portable across GPUs and CPUs (C, C++, FORTRAN, CUDA)
$\mu\pi$ (MUIP)	The world's most scalable simulator of Message Passing Interface (MPI) programs.	<ul style="list-style-type: none"> ■ Tested on up to 216,000 processor cores of Cray XT5; supports over 227 million virtual tasks
libSynk	Library for high performance time-synchronized communication on distributed memory platforms; written in C, over sockets, MPI & shared memory.	<ul style="list-style-type: none"> ■ Employed by most leading distributed network simulators including <i>pdns</i>, <i>DaSSF</i> & <i>GTNetS</i>
μsik	Novel PDES “micro-kernel”, unifying most existing virtual time-synchronization techniques; written in C++.	<ul style="list-style-type: none"> ■ Designed for scalable Time Warp as well as conservative synchronization on 216,000 processor core execution ■ Being applied to large-scale space physics DES models, neurological simulations and others
TeD	Domain Specific Language and compiler for automated Time Warp-based execution of network models. www.cc.gatech.edu/computing/pads/teddoc.html	<ul style="list-style-type: none"> ■ Precursor to currently leading parallel/distributed network simulators ■ Widely disseminated world-wide and well cited in the literature

<p>FDK <i>Co-author</i></p>	<p>High-performance realization of the Department of Defense High Level Architecture (HLA) Runtime Infrastructure (RTI)</p> <p>www.cc.gatech.edu/computing/pads/fdk.html</p>	<ul style="list-style-type: none"> ■ Among the very few source-available HLA RTI implementations ■ Well recognized in HLA community
<p>PARINO</p>	<p>Parallel/distributed branch-and-cut solver for mixed integer programming (MIP)</p>	<ul style="list-style-type: none"> ■ Incorporated novel cut sharing and distributed management mechanisms

I. Research in the News

- U.S. Department of Energy Advanced Scientific Computing Research Advisory Committee Meeting (chair Daniel Reed) *Minutes of Record* (public), December 2015, “Reversible Software Execution Systems” Pages 23-25
science.energy.gov/~media/ascr/ascac/pdf/meetings/201512/ASCAC_Minutes_Dec_2015.pdf
- Knox News, and others, “Computer science that's spooky cool,”
blogs.knoxnews.com/munger/2010/01/computer_science_thats_spooky.html
- Maryville College News, ORISE News, and others, “Oak Ridge Internship Helps MC Senior Focus on Computer Science Goals,”
maryvillecollege.edu/news/news.asp?id=1192&pgID=1216
- Texas A&M News, ORISE News, and others, “Fostering Safe Sharing of Confidential Information Proves Challenging,”
engineeringnews.tamu.edu/news/1369 and orise.orau.gov/sep/profiles/06beier.htm
- Wired.com, “Net Analysis Gets Turbo Boost,”
www.wired.com/news/infostructure/0,1377,60077,00.html
- PSC News Center (Pittsburgh Supercomputing Center), “Better Networks,”
www.psc.edu/publicinfo/2003/inprogress/
- Georgia Tech News, “Georgia Tech Researchers Create the World’s Fastest Detailed Computer Simulations of the Internet,”
www.gatech.edu/news-room/release.php?id=173.

J. Supercomputing Allocations

- **Titan**, 2012: “ReveR-SES: Reversible Software Execution System for Ultra-scale Computing,” 2,000,000 hours (CSC088) on the Titan supercomputer at the Oak Ridge Leadership Computing Facility, National Center for Computational Sciences, Oak Ridge National Laboratory
- **Jaguar**, 2010: “Data and Runtime Infrastructure for Parallel Discrete Event at Petascale,” 500,000 hours (CSC034 #2) on the Jaguar supercomputer at the Oak Ridge Leadership Computing Facility, National Center for Computational Sciences, Oak Ridge National Laboratory
- **Jaguar**, 2007: “An Evolutionary Framework for Porting Applications to Petascale Platforms,” 150,000 hours (CSC034 #1) on the Jaguar supercomputer at the Oak Ridge Leadership

XI. PERSONAL DATA

A. Family

- Wife Vijaya (married for 24 years), artist
- Two sons (20 and 18 years old).

B. Citizenship and Security Clearance Status

- **Citizenship:** USA
- **Clearance:** DOE Q Clearance (active)

C. Miscellaneous

- **Erdős Number:** 3 (three)
Paul Erdős » (Frank Harrary » Narsingh Deo) | (Fan Chung » Sandeep Bhatti) » Kalyan Perumalla
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