

# Model-Based Design for Safety Critical Automotive Applications

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# Model-Based Design for Safety-Critical Applications Success Stories

 MathWorks tools applied successfully to safety-critical applications in different domains

•	High quality code
	<ul> <li>Over 1 million lines of code have been certified just in the last year</li> <li>One code generator option error was found (and corrected), although the generated code actually performed correctly and passed testing with 100% MCDC coverage.</li> </ul>
	<ul> <li>No compiler errors have been found when using an unqualified COTS compiler with a limited subset of model based C code</li> </ul>
•	High quality design <ul> <li>Defect leakage rates at integration are reduced by at least one order of magnitude</li> </ul>
	<ul> <li>Designs are proven prior to code generation</li> </ul>
	<ul> <li>Model based testing provides more thorough and rigorous method of validating and verifying system design and software requirements</li> </ul>

Honeywell generated flight control code certified to DO178-B Level A

www.mathworks.com/industries/aerospace/miadc05/presentations/potter.pdf faculty.erau.edu/korn/ToolForum/Potter\_files/frame.htm

Alstom generated code for safety-critical power converter control systems

www.mathworks.com/products/rtwembedded/userstories.html?file=10591





Institute for Radiological Protection and Nuclear Safety verified nuclear safety software with PolySpace products https://tagteamdbserver.mathworks.com/ttserverroot/Download/42572\_IRSN\_final.pdf



# Model-Based Design for Safety-Critical Automotive Applications

Customer's "list of presents"

- Leverage advantages of Model-Based Design and state-of-the-art code generation
- 2. Eased compliance demonstration
- 3. Validated / certified tools



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# IEC 61508 in the Automotive Industry

Generic Safety Standard

- Defines safety life cycle
- Constrains software development, verification, and validation processes
- Increasingly relevant for automotive companies
  - Voluntary adherence across Europe (state-of-the-art)
  - Also applicable to noncritical applications (best practices)



## **Model-Based Design for IEC 61508**

- Standard was established in late 1990s: No notion of Model-Based Design, code generation, etc.
- Origin in the process and automation industries: Industry-specific adaptations; subject to interpretation
- Processes, measures, and techniques (especially in the verification and validation area) need to be mapped onto Model-Based Design processes and tools



	Technique/Measure*	Ref	SIL1	SIL2	SIL3	SIL4
1	Suitable programming language	C.4.6	HR	HR	HR	HR
2	Strongly typed programming language	C.4.1	HR	HR	HR	HR
3	Language subset	C.4.2			HR	HR

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# IEC 61508 Compliant Verification and Validation

TÜV approved reference workflow



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# IEC 61508 Compliant Verification and Validation

TÜV approved reference workflow



## MATLAB® SIMULINK®

# IEC 61508 Compliant Verification and Validation

TÜV approved reference workflow



# IEC 61508 Compliant Verification and Validation with MathWorks Products



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# IEC 61508 Compliant Verification and Validation with MathWorks Products





# **Model Advisor**

#### Static analysis of models against a set of checks

- Simulation (Simulink)
- Code generation (RTW-EC)
- Requirements consistency (Simulink V&V)
- Modeling standards (Simulink V&V)
  - MAAB
  - IEC 61508
  - DO178B

- 101 × Task Hierarchy: slvnvdemo\_fuelsys\_docreq Check for blocks that are not discrete Analysis 🗄 🚞 Model Advisor Task Manage 🗄 🦏 By Product jm\_0001: Prohibited Simulink standard blocks inside controllers . ⊕-⊖ Simulink 🗄 🛅 Real-Time Workshop Embedded Coder Run This Check 🗄 🦏 Simulink Verification and Validation Result: 🔥 Warning 🗄 词 Modeling Standards + DO-178B Checks MathWorks Automotive Advisory Board Style Guideline: im 0001: Prohibited Simulink standard blocks inside controller E-C 61508 Checks The following blocks are not permitted in discrete controllers slvnvdemo fuelsvs docrea/throttle.command/Clock Check for blocks that do not use one-based indexing 🗹 📃 Check for invalid filenames rottle & Manifold/Intake Manifold/p0 = 0.589 bar Check for invalid model directory names slvnvdemu 🔽 🛝 Check for blocks that are not discrete Check for prohibited Sink blocks lvnvdemo fi. 🗹 💷 Check for invalid port positioning and configuration slvnvdemo fuelsvs Check for mismatches between names of ports and their cor vdemo\_fuelsys\_docreg/throttle command - 🗆 × - 🗹 💷 Check if block names do not appear below blocks slvnvdemo fuelsys door Check for systems with a mixture of primitive blocks and sub it View Simulation Format Tools Help slvnvdemo fuelsys docreg/ti - 🗹 📃 Check if model has unconnected block inputs and outputs 🖇 🖬 🚭 👗 🖻 💼 💼 (수 수 🔶 으 으 🕨 ) ☑ □Check for improperly positioned Trigger and Enable blocks <u>slvnvdemo fuelsys docreq/throt</u> - 🗹 📃 Check if annotations have drop shadows <u>lvnvdemo fuelsys docreq/engine ga</u> Check if tunable parameters specify expressions, data type c 🔽 💷 Check if Stateflow events are not defined at the chart level o 🖬 🗆 Chack if Stataflow data objects with local econo are not defin f(u) Fcn1 Look In Table F 100% ode3

API to create custom checks and groups (Simulink V&V)

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# **Modeling Standards Checking IEC 61508**

Quickly identify issues

 at the model level that impede
 deployment in IEC 61508
 applications or limit traceability

#### **Benefits**

'he MathWorks

- Automated design reviews
- Enhanced traceability
- Seamless use of Real-Time Workshop Embedded Coder and V&V products

![](_page_11_Figure_8.jpeg)

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# **Modeling Standards Checking IEC 61508**

File Edit Run View Help	
File       Edit       Run       View       Help         Task       Hierarchy: rtwdemo_iec615083         Image: Standards       Image: Standards         Image: Simulink       Image: Simulink       Image: Simulink         Image: Simulink       Image: Simulink	Check for proper usage of Simulink         Analysis (^Triggers Update Diagram)         Identify usage of Simulink blocks that can impact safety         Run This Check         Result:  Warning         a) Check for proper usage of Abs blocks         Identify Absolute Value blocks that can have unreachable code or produce overflows         References to standards and guidelines:
Check for fully defined interfa Check for questionable blocks Check for questionable blocks Check for questionable blocks Check for proper usage of S Check	<ul> <li>ie</li> <li>iEC 61508-3, Table A.3 (2) 'Strongly typed programming language'</li> <li>iEC 61508-3, Table A.3 (3) 'Language subset'</li> <li>iEC 61508-3, Table A.4 (3) 'Defensive programming'</li> <li>iEC 61508-3, Table B.8 (3) 'Control Flow Analysis'</li> <li>MISRA-C: 2004, Rule 14.1</li> <li>MISRA-C: 2004, Rule 21.1</li> <li>The following Absolute Value block is operating on an unsigned value which will result in unreachable code:</li> <li>rtwdemo_iec615083/Abs</li> </ul>
This Absolute Value block is operative value which may result in unreachable	This relational operator block is not outputting a boolean data t which can lead to unpredictable results in the generated code.
	uint 16 uint 16 uint 16 uint 16 Add Abs
These root-level Inport blocks have	undefined attributes. This Absolute Value block is operating on a signed integral value but saturate on integer overflow is not set, which lead to incorrect results in the generated code.

![](_page_13_Picture_0.jpeg)

Model Advisor - rtwdemo\_IEC61508 File Edit Run View Help

Check Model diagnostic settings 🗹 🛕 Check the display attributes of block names

Hierarchy: rtwdemo IEC61508 🗄 🗟 Simulink Verification and Validation Modeling Standards 🗄 🛅 DO-178B Checks E IEC 61508 Checks

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Verification and Validation

Code Generation

X [A]

Readability

Workflow

Simulation

[A]

# **Modeling Standards Checking MAAB**

Automate for 30+ M

rtwdemo IEC61508/More Info

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Automated rule for 30+ MAAB	e checking V2.0 guidelines	_	CONTROL ALGORITHM MODELING GUIDELINES USING MATLAB <sup>®</sup> , Simulink <sup>®</sup> , and Stateflow <sup>®</sup> Version 2.0 MathWorks Automotive Advisory Board (MAAB) July 27 <sup>th</sup> , 2007
y: rtwdemo_IEC61508	Check the display attributes of block names	6.1.8. ic 000	061: Display of block names
Simulink Verification and Validation	Analysis	ID: Title	ic 0061: Display of block names
Modeling Standards	jc_0061: Display of block names	Driority	pc_0001. Display of block names
DO-178B Checks	Bun This Check	Phonty	Incommended
EC 61508 Checks	Kun This Check	Scope	MAAB
MathWorks Automotive Advisory Board Checks	Result: A Warning	MATLAB	All
Check for blocks that do not use one-based ind	MathWorks Automotive Advisory Board Style Guideline: ic. 0061: Display of block pa	Version	
		Prerequisites	
Check for invalid model directory names	The block name should not be displayed if the block function is	<u> </u>	The block name should be displayed when it provides descriptive information
Check for blocks that are not discrete	appearance.		
Check for prohibited Sink blocks	These (obvious) blocks should not show their name (Menu:Format/Hid		0.057 P
Check for microstopic positioning and configuration	rtwdemo_IEC61508/Compare_To_Constant/Compare		xm ours x <u></u> b Vo
Check for mismatches between names of ports			FuelRateMonitor EngineSpeedFilter ThrottleAdvitation
Check in block names do not appear below bloc	The block name should be displayed when it provides descripti		<ul> <li>The block name should not be displayed if the block function is known from its.</li> </ul>
Check for systems with a mixture of primitive b	These block names should be modified (to be more descriptive) or not		appearance.
Check for improperly positioned Trigger and En			
Check if appotations have drop shadows	rtwdemo_IEC61508/Abs		amin a 1 a south and a suit
	rtwdemo_IEC61508/Abs1	Description	
Check if Stateflow events are not defined at the			
Check if Stateflow data objects with local score	Tewdenio Tecorsos/compare to constant/constant		
- I Check interface Signals and Parameters	These block names should be shown since they appear to have a desc		12 13 14 14 14
Check for Exclusive States, Default States and States	rtwdemo IEC61508/Build ERT		
	rtwdemo IEC61508/Model Advisor		Cal Cal Canter Model Name>

Rationale

Last Change V2.0

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🗹 🥝 Check Icon display attributes for Port blocks 🗹 🛕 Check for usable characters in Subsystem block

![](_page_14_Picture_0.jpeg)

# Model-Based Design for Safety-Critical Automotive Applications

#### Customer's "list of presents"

- 1. Leverage advantages of Model-Based Design and state-of-the-art code generation
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![](_page_14_Picture_8.jpeg)

![](_page_15_Picture_0.jpeg)

## **IEC 61508 Compliance Documentation**

"To conform to this standard it shall be demonstrated that the requirements have been satisfied to the required criteria specified and therefore, for each clause or sub-clause, all the objectives have been met"

 Often involves listing all IEC 61508 requirements with an explanation of how each requirement has been met

Table A.3 – Software design and development: support tools and programming language (see 7.4.4)

	Technique/Measure*	Ref	SIL1	SIL2	SIL3	SIL4	Interpretation in this Application
1	Suitable programming language	C.4.6	HR	HR	HR	HR	?
2	Strongly typed programming language	C.4.1	HR	HR	HR	HR	?
3	Language subset	C.4.2			HR	HR	?
4a	Certificated tools	C.4.3	R	HR	HR	HR	?
4b	Tools: increased confidence from use	C.4.4	HR	HR	HR	HR	?
бa	Certificated translator	C.4.3	R	HR	HR	HR	?
5b	Translator: increased confidence from use	C.4.4	HR	HR	HR	HR	?
8	Library of trusted/verified software modules and components	C.4.5	R	HR	HR	HR	?

![](_page_16_Picture_0.jpeg)

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#### IEC 61508 Compliance Documentation with MathWorks Products

61508-3 © IEC:1998

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Table A.3 – Software design and development: support tools and programming language (see 7.4.4)

	Technique/Measure*	Ref	SIL1	SIL2	SIL3	SIL4	Interpretation ir	n this Application
1	Suitable programming language	C.4.6	HR	HR	HR	HR	?	The MAAB Style Guides and/or organization specific modeling guidelines can be
2	Strongly typed programming language	C.4.1	HR	HR	HR	HR	?	used to define a subset of the modeling language.
3	Language subset	C.4.2			HR	HR.		The Stateflow language can be restricted to Stateflow charts that implement
40	Certificated tools	C.4.3	R	HR	HR	HR	?	pure Mealy or Moore semantics.
4b	Tools: increased confidence from use	C.4.4	HR	HR	HR	HR	?	The Simulink Block Data Type Support table lists the blocks that can be used for
5a	Certificated translator	C.4.3	R	HR	HR	HR	?	Simulink - Model Advicor can be used to partially enforce restricted language
5b	Translator: increased confidence from use	C.4.4	HR	HR	HR	HR	?	subsets.
6	Library of trusted/verified software modules and components	C.4.5	R	HR	HR	HR	?	Model reviews based on reports generated by Simulink Report Generator can be conducted to check language subset considerations on model level.
	Maps IEC 61508-3 objectives onto Model-Based Design and production code generation MisRA-C and/or organization specific coding guidelines can be used to subset of the implementation language.						View can be conducted to check language subset considerations on model level. Code Reviews based on Real-Time Workshop Embedded Coder – Code Generation Reports can be conducted to check language subset considerations on code level. Configuration Sets can be customized to enforce specific settings of the involved Model-Based Design Tools, e.g. diagnostics and optimization settings. MISRA-C and/or organization specific coding guidelines can be used to define a subset of the implementation language.	
	Provides detail 100+ technique	ed es a	sug and	gg I m	est nea	ior asu	ns for res	Third-party products, such as the TASKING compiler tool chain (for Infineon, ARM, and other devices), supported by Link for TASKING®, facilitate MISRA-C compliance checking of generated code. Third-party products, such as PolySpace Desktop, facilitate MISRA-C compliance checking of generated code. Third-party products, such as PolySpace Desktop, can be used to check language subset considerations within the generated code.

![](_page_17_Picture_0.jpeg)

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![](_page_18_Picture_0.jpeg)

# <u>Automotive Code Validation Suite (AVS)</u>

- Independent test suite to validate Real-Time Workshop Embedded Coder and the compiler / linker tool chain
- Initiated by Ford, Conti, and TÜV Rheinland

he MathWorks

- Real-Time Workshop Embedded v4.2 (R14sp2) and v5.0 (R2007b) sucessfully passed AVS
- Validation indicates that Real-Time Workshop Embedded Coder and the target compiler\* can be seen as trusted processes

\* based on a fixed Real-Time Workshop Embedded Coder, Simulink, compiler, and linker configuration and limited number of test cases

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			2008-03-06		JVRheinla cisely Right.	and
			2009-03-06	TÜVRhei Precisely Righ	nland <sup>®</sup>	
	1	2008-03-06		heinland <sup>®</sup> <sup>Right.</sup>		
ß	2008-03-06		<b>TÜVRheinland</b> <sup>eff</sup> Precisely Right.	B		
		Validati Embedde	Short Report on of the MathWorks Real-Time Workshop® d Coder™ product with the Automotive Code Validation Suite (AVS) v4.0			
	Report-No.:		968/EL 525.01/D8			
	Date:		2008-03-06	5		
	Pages:		4			
	Test object:		Real-Time Workshop <sup>®</sup> Embedded Coder™ Version 5.0 (R2007b)			
	Customer:		The MathWorks GmbH Adalperostraße 45 85737 Ismaring Germany			
	Order No./Date		Fax dated 2007-09-28		8	
	Test Institute:		TÜV Rheinland Industrie Service GmbH Automation, Söftware and Information Technology (ASI) Am Grauen Stein 51105 Köln Germany		6 5	
	TÜV Order No.	/Date:	9833214 dated 2007-10-01			
	TÜV Offer No./	Date:	968/176/07 dated 2007-08-25			
	Inspector(s):		DiplIng. (FH) Oliver Busa DiplIng. (FH) Karsten Rotzoll			
	Place of inspe	ction:	see Test Institute	8	6	
	Prepared:		September 2007 - März 2008	2		
	The test results	are exclusively	related to the test samples.			_
	This report mu: Institute.	st not be copie	d in an abridged version without the written permission of the $\ensuremath{Test}$	X.		20

![](_page_19_Picture_0.jpeg)

## MATLAB<sup>®</sup> & SIMULINK<sup>®</sup>

## **AVS for Real-Time Workshop®** Embedded Coder<sup>™</sup>

![](_page_19_Figure_3.jpeg)

![](_page_20_Picture_0.jpeg)

## MATLAB® SIMULINK®

Demo

# AVS for Real-Time Workshop<sup>®</sup> Embedded Coder<sup>™</sup>

![](_page_20_Picture_3.jpeg)

Те	st re	port		<b>TÜVRheinland®</b> Precisely Right.						
	AVS Test Record 06.03.2008									
Current Status	Testcase	Current compare of Simulation results Target results	Previous stored reference (compare status)	Simulation LOG	Testcase Information					
1	EC000000	Wrong number of simulation and target result values.	No reference file	No Error Logfile	EC000000					
•	EC000001	passed	No reference file	No Error Logfile	EC000001					
•	EC000002	passed	No reference file	No Error Logfile	EC000002					
•	EC000003	passed	No reference file	No Error Logfile	EC000003					
•	EC000004	passed	No reference file	No Error Logfile	EC000004					
•	EC000004a	passed	No reference file	No Error Logfile	EC000004a					
•	EC000005	passed	No reference file	No Error Logfile	EC000005					
•	EC000006	passed	No reference file	No Error Logfile	EC000006					
•	EC000007	passed	No reference file	No Error Logfile	EC000007					
•	EC000008	passed	No reference file	No Error Logfile	EC000008					
•	EC000009	passed	No reference file	No Error Logfile	EC000009					
•	EC000010	passed	No reference file	No Error Logfile	EC000010					
	EC000011	passed	No reference file	No Error Lorfile	EC000011					

#### **Benefits**

- Fully automated validation of the entire code generator / compiler / linker tool chain
- Easy re-validation of project-specific tool versions / config sets

![](_page_21_Picture_0.jpeg)

## MATLAB® SIMULINK®

# AVS for Real-Time Workshop<sup>®</sup> Embedded Coder<sup>™</sup>

#### Press Release

![](_page_21_Picture_4.jpeg)

![](_page_22_Picture_0.jpeg)

# Certified Code Generation with Real-Time Workshop<sup>®</sup> Embedded Coder<sup>™</sup>

 Version 5.1 (R2008a) certified fit for purpose to develop safety related software according to IEC 61508

![](_page_22_Picture_4.jpeg)

![](_page_23_Picture_0.jpeg)

# Certified Code Generation with Real-Time Workshop<sup>®</sup> Embedded Coder<sup>™</sup>

#### **Benefits**

Satisfies IEC 61508-3 clause 7.4.4.3 / Table A.3 (5a)

7.4.4.3 To the extent required by the safety integrity level, the programming language selected shall:

 a) have a translator/compiler which has either a certificate of validation to a recognised national or international standard, or it shall be assessed to establish its fitness for purpose;

- Eases certification of generated code
  - Large portion of the test and review activities can be shifted from the code level to the model level
- Facilitates optimizations

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![](_page_24_Picture_0.jpeg)

# Model-Based Design for Safety-Critical Automotive Applications

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- 1. Leverage advantages of Model-Based Design and state-of-the-art code generation
  - Tool-supported IEC 61508 compliant verification and validation
- 2. Eased compliance demonstration
  - IEC 61508 compliance documentation
- 3. Validated / certified tools
  - IEC 61508 certification and AVS validation of Real-Time Workshop Embedded Coder

![](_page_25_Picture_0.jpeg)

# **Key Takeaways**

Model-Based Design with Simulink<sup>®</sup> and Real-Time Workshop<sup>®</sup> Embedded Coder<sup>TM</sup>

Applied successfully to safety-critical applications in multiple domains

☑ Can satisfy the objectives of automotive safety standards (IEC 61508, ISO 26262)

Facilitates highly automated IEC 61508 compliant verification and validation

✓ Provides certified state-of-the-art code generation