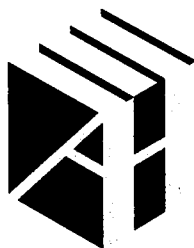


Microstrip Lines and Slotlines

Third Edition

Ramesh Garg
Inder Bahl
Maurizio Bozzi



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