# AN13232 RT-Thread Board Port and Application Development

Rev. 0 — 04/2021

Application Note

## 1 Introduction

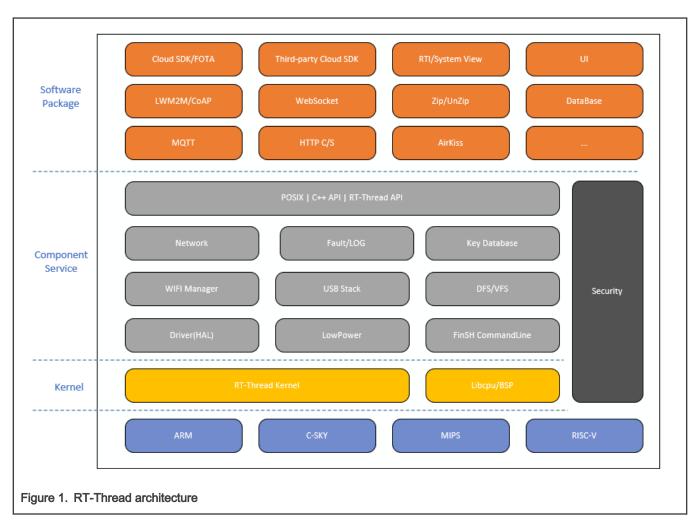
RT-Thread is a multitasking application development platform integrating Real-Time Operating System (RTOS) kernel, middleware component and developer community. It is developed with the strength of open source community. RT-Thread is also an Internet of things operating system with rich components, highly scalable, simple development, ultra-low power consumption and high security. RT-Thread has all the key components required for an IoT OS platform, such as GUI, network protocol stack, secure transport, low-power components, and so on.

After 11 years of cumulative development, RT-Thread has owned the largest embedded open source community in China and quickly got global interests. RT-Thread has been widely used in energy, vehicle-mounted, medical, consumer electronics and other industries, deployed on more than 800 million devices.

## 2 Architecture of RT-Thread

One of the main differences between RT-Thread and many other RTOS, such as FreeRTOS and uC/OS, is that it is a real-time kernel and has a rich set of software components, as shown in Figure 1.

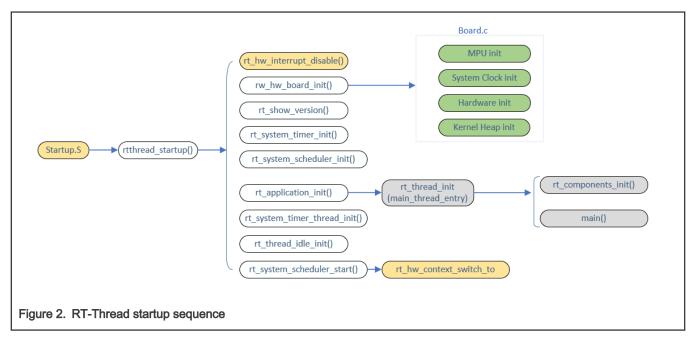




- Kernel layer: RT-thread kernel is the core part of RT-Thread, including the implementation of objects in the kernel system, such as multi-thread and its scheduling, semaphore, mailbox, message queue, memory management, timer, etc. Libcpu /BSP (chip migration related file/board level support package) is closely related to hardware and consists of peripheral drivers and CPU support.
- Component and Service layer: Components are top-level software based on RT-Thread kernel, such as VFS, FinSH
  command line interface, network, device framework, and so on. Modular design is adopted to achieve high cohesion and
  low coupling between components.
- · Software package layer:
  - IOT Software package: Paho MQTT, WebClient, mongoose, WebTerminal
  - Scripts Languange: JerryScript, MicroPython
  - Multimedia: muPDF
  - Tools: CmBacktrace, EasyFlash, EasyLogger, SystemView
  - System related Software: RTGUI, Persimmon UI, Iwext4, partition, SQLite

### 3 RT-Thread startup sequence

Figure 2 shows the RT-Thread startup sequence. The colored blocks require special attention, yellow for libCPU porting and green for board porting.

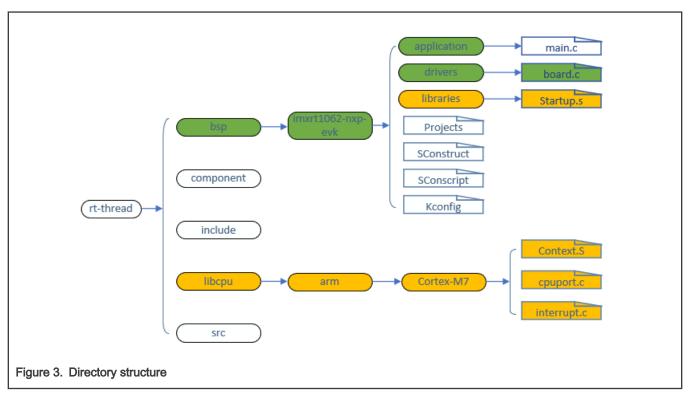


The start entry of RT-Thread is rtthread\_startup(). After chip startup file completes the hardware initialzation (such as clock configuration, interrupt vector table, initializing heap and stack), jump to the start entry of RT-Thread. The startup sequence for RT-Thread is as follows :

- 1. Disable global interrupt, initializing the system hardware.
- 2. Print system information and initialize system or modules such as system tick, scheduler.
- 3. Initialize the main thread.
- 4. Initialize software timer thread, idle thread.
- 5. Start scheduler, enable global interrupt, and switch context to main thread.
- 6. In the main thread, initialize the components, including drivers, network, vfs, and other component services in programmed order and then enter the main function.

## 4 Directory structure

In the RT-Thread source code, Figure 3 shows the porting-related files located in the colored path, yellow for libCPU-related files and green for board-related files.



• bsp

- hardware related files
- component

- rtthread components such as: finsh, libc, cplusplus, net ...

- include
  - rtthread header files
- libcpu

- cpu related files

- src
  - rtthread kernel source codes

## 5 Start porting

#### 5.1 Source code

Download source codes from rt-thread

#### 5.2 Libcpu Porting

RT-Thread's libCPU abstraction layer provides a set of unified CPU architecture abstraction interface. This part of the interface contains the global interrupt control function, Thread context switching function, the configurations of clock tick and interrupt function, Cache, and so on.

• Context switch: context\_xx.s

A context switch represents a CPU switch from one thread to another, or between threads and interrupts, and so on. During a context switch, the CPU typically stops processing the currently running code and saves the exact location where the current program is running so that it can resume later. Table 1 describes key functions to be implemented.

#### Table 1. Functions to be implemented

<pre>rt_base_t rt_hw_interrupt_disable(void);</pre>	Disable global interrupt
<pre>void rt_hw_interrupt_enable(rt_base_t level);</pre>	Enable global interrupt
<pre>void rt_hw_context_switch_to(rt_uint32 to);</pre>	Switch context, called while starting thread or used in signal
<pre>void rt_hw_context_switch(rt_uint32 from, rt_uint32 to);</pre>	Switch <b>from</b> thread to <b>to</b> thread
<pre>void rt_hw_context_switch_interrupt(rt_uint32 from, rt_uint32 to);</pre>	Called in interrupt, switch <b>from</b> thread to <b>to</b> thread

• Thread initialization: cpuport.c

In RT-Thread, the Thread has an independent stack. When the Thread is switched, the context of the current Thread will be stored in the stack. When the Thread wants to resume running, the context information will be read from the stack for recovery.

This file implements the initialization of the thread stack  $rt_hw_stack_init$  () and the hard Fault exception handler. Table 2 describes key functions to be implemented.

#### Table 2. Functions to be implemented

rt_hw_stack_init()	Initializing stack of a thread
rt_hw_hard_fault_exception()	Exception handler for hardfault

#### 5.3 Libraries porting

#### 5.3.1 Startup file

Startup.s provided by SDK of chip handles the following tasks:

- · Initialize clock and the configuration of interrupt vector.
- Initialize global/static variables.
- · Initialize the stack.
- Initialize library functions.
- · Jump to next stage startup.

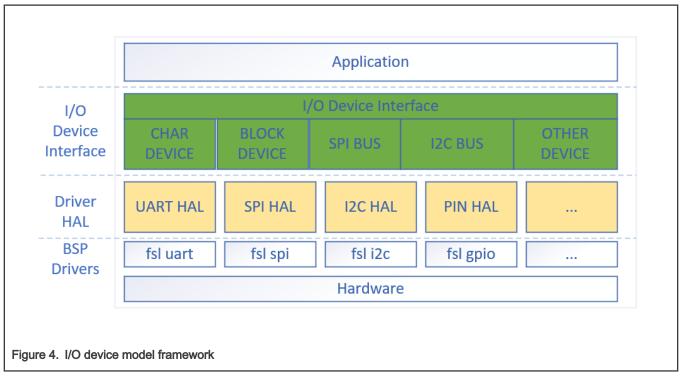
Under Keil MDK or IAR, without any modifications, the program will jump to rtthread\_startup(). But under gcc compiler, you need to change **bl main** to **bl entry**.

```
int entry(void)
{
    rtthread_startup();
    return 0;
}
```

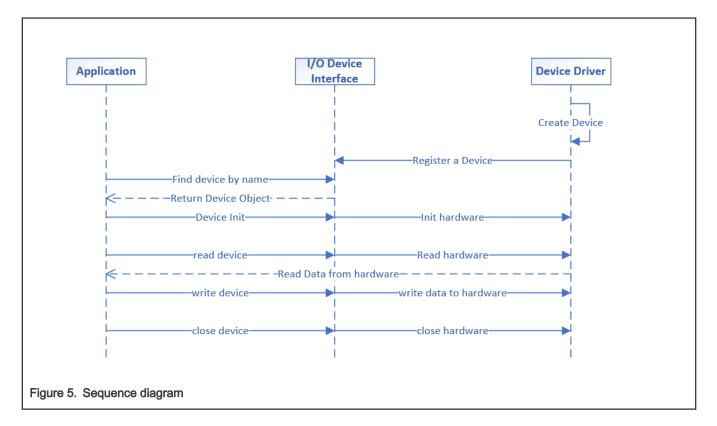
#### 5.4 Drivers porting

#### 5.4.1 RTT device framework

RT-Thread provides a simple I/O device model framework, as shown in Figure 4, between the hardware and the application. It falls into three layers, from top to bottom, I/O device interface layer, device driver framework layer (HAL), and BSP driver layer.

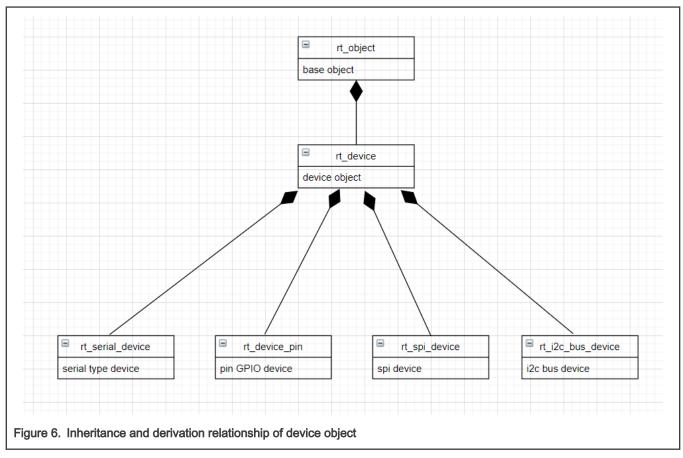


The device driver layer is a set of programs that drive hardware devices to work and provide the functions to access hardware devices. It is responsible for creating and registering I/O devices. For devices with simple operation logic (init, read, write, close), the device can be registered directly into the I/O device manager without going through the device driver framework (HAL) layer. Figure 5 shows the sequence diagram.



#### 5.4.2 RTT device structure

RT-Thread's device model is based on the kernel object model. Devices are considered as a class of objects and are included in the category of object manager. Each device object is derived from the base object, and each concrete device can inherit the properties of its parent class object and derive its new properties. Figure 6 shows the inheritance and derivation relationship of device object.



The device object is specifically defined as follows:

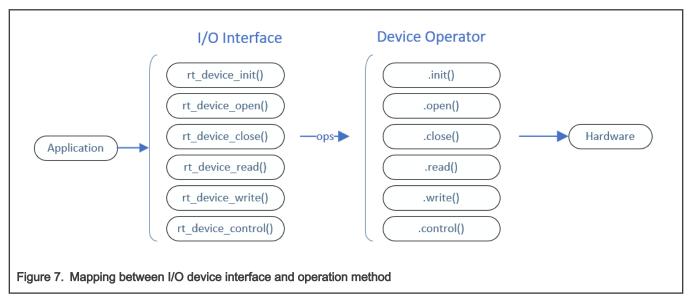
```
struct rt_device
{
    struct rt_object parent; /* kernel base object */
    enum rt_device_class_type type; /* device type */
    rt_uint16_t flag; /* device param */
    rt_uint8_t ref_count; /* device open flag */
    rt_uint8_t device_id; /* device open count */
    rt_uint8_t device_id; /* device ID, 0 - 255 */
    /* data transfer call back */
    rt_err_t (*rx_indicate) (rt_device_t dev, rt_size_t size);
    rt_err_t (*tx_complete) (rt_device_t dev, void *buffer);
    const struct rt_device_ops *ops; /* device operator pointer*/
    /* device data */
    void *user_data;
};
```

RT-Thread supports a variety of I/O device types. The main device types are as follows:

```
RT_Device_Class_Char/* char device*/RT_Device_Class_Block/* block device*/RT_Device_Class_NetIf/* network device*/RT_Device_Class_MTD/* MTD device*/RT_Device_Class_RTC/* RTC device*/RT_Device_Class_Sound/* sound device*/
```



The application accesses the hardware device through the I/O device operator interface, which must be implemented by a underlying device driver. Figure 7 shows the mapping between the I/O device interface and the operation method.



#### 5.4.3 Example: A simple UART driver

Figure 8 shows an code example of a simple UART driver.

```
138 int rt_simple_uart_init(void)
     139 🕀 {
     140
               int i;
     141
              rt uint32 t flag;
     142
              rt_err_t ret = RT_EOK;
     143
              struct rt device *device;
     144
              static struct serial_configure config = RT_SERIAL_CONFIG_DEFAULT;
     145
     146
              flag = RT DEVICE FLAG RDWR | RT DEVICE FLAG INT RX;
     147
              device = rt device create(RT Device Class Char, 1);
              if (device == NULL)
     148
                  return RT_ERROR;
     149
     150
     151
              device->init
                                  = s uart init;
     152
                                = s_uart_close;
= s_uart_close;
                                  = s_uart_open;
              device->open
     153
              device->close
     154
              device->read
                                 = s uart read;
              device->write = s_uart_write;
device->control = s_uart_control;
     155
     156
     157
              device->user data = &config;
     158
     159
              /* register a character device */
              ret = rt_device_register(device, "s_uart", flag);
     160
     161
     162
              return ret;
     163 1
     164 /* board init routines will be called in board_init() function */
     165 INIT BOARD EXPORT(rt simple uart init);
Figure 8. Example code of a simple UART driver
```

In the <code>rt\_simple\_uart\_init()</code> function, the <code>s\_uart</code> device is initialized and registered.

```
32 rt_err_t s_uart_init(rt_device_t dev)
      33 ⊟ {
      34
              struct imxrt uart *uart;
      35
              lpuart_config_t config;
      36
              RT_ASSERT(dev != RT_NULL);
      37
      38
              struct serial configure *cfg = (struct serial configure *)dev->user data;
      39
              RT_ASSERT(cfg != RT_NULL);
      40
      41
              LPUART GetDefaultConfig(&config);
      42
              config.baudRate_Bps = cfg->baud_rate;
      43
              switch (cfg->data_bits)
      44
      45 m
      55
              switch (cfg->stop_bits)
      56 🗄
      65
              switch (cfg->parity)
      66
              config.enableTx = true;
      78
      79
              config.enableRx = true;
      80
      81
              LPUART_Init(LPUART1, &config, GetUartSrcFreq());
      82
      83
              return RT_EOK;
      84
          }
Figure 9.
```

In  $s_uart_init()$  function, LPUART hardware is initialized. This function is called by  $rt_device_init$  when user code initializes the board with the  $rt_components_board_init()$  function.

Figure 10 shows the data read from UART.

```
86 rt size t s uart read(rt device t dev, rt off t pos, void *buffer, rt size t length)
      87 🕀 {
      88
              int ch;
      89
              int size;
              rt uint8 t *data = (rt uint8 t *)buffer;
      90
              RT ASSERT (dev != RT NULL);
      91
      92
              size = length;
      93
      94
              while (length)
      95 🖨
              {
                  if (LPUART GetStatusFlags(LPUART1) & kLPUART RxDataRegFullFlag)
      96
      97 Ė
                   {
      98
                       ch = LPUART ReadByte(LPUART1);
      99
                  }
     100
                  if (ch == -1) break;
     101
     102
                  *data = ch;
     103
                  data ++; length --;
     104
     105
                  if (ch == '\n') break;
     106
              }
     107
     108
              return size - length;
     109 }
Figure 10. Data read from UART
```

In the s\_uart\_read function, the s\_uart device reads data from hardware and this function is called in rt\_device\_read.

UART write is similar too.

```
111 rt_size_t s_uart_write(rt_device_t dev, rt_off_t pos, const void *buffer, rt_size_t length)
     112 🕀 {
     113
              int size;
              RT ASSERT(dev != RT NULL);
     114
              rt_uint8_t *data = (rt_uint8_t *)buffer;
     115
     116
              size = length;
     117
              while (length)
     118 🗄
              {
     119
                  if (*data == '\n' && (dev->open_flag & RT_DEVICE_FLAG_STREAM))
     120
                  {
                      LPUART WriteByte(LPUART1, '\r');
     121
                      while (!(LPUART_GetStatusFlags(LPUART1) & kLPUART_TxDataRegEmptyFlag));
     122
     123
     124
                  LPUART WriteByte(LPUART1, *data);
                  while (!(LPUART_GetStatusFlags(LPUART1) & kLPUART_TxDataRegEmptyFlag)){};
     125
     126
     127
                  ++ data;
     128
                  -- length;
     129
              }
     130
     131
              return size - length;
     132
Figure 11. Data write from UART
```

In the s uart write function, the s uart writes data to hardware and this function is called in rt device write.

#### 5.5 Board porting

Board level hardware resource initialization is realized by the RT\_HW\_BOARD\_INIT function, which completes the initialization of system devices, such as:

- · MPU configuration
- · Pin function configuration
- · System clock configuration
- · kernel heap initialization

Component board initialization

#### 5.6 Project construction

Env is a development tool launched by RT-Thread and can be downloaded on RT-Thread Env. It provides compilation and build environment, graphical system configuration and package management functions for projects based on RT-Thread operating system. Its built-in menuconfig provides easy-to-use configuration tools and can configure the kernel, components and software packages.

Key features of RT-Thread construction tool include:

- Menuconfig: A graphical configuration interface, good interactivity. On exit, it generates rtconfig.h automatically.
- A variety of highly reliable, modular software packages that are loosely coupled, good maintained. The software package can be downloaded online by env tool.
- scons: The default build tool, easy to use. scons can both generate projects for IAR, MDK and invoke the GCC tool chain to build.

SCONS is an open source build system written in Python similar to **GNU Make**. It takes a different approach: instead of processing a Makefile, it uses *SConstruct* and *SConscript* files to guide the build process. These files are also Python scripts that can be written using standard Python syntax. Therefore, you can call the Python standard library in *SConstruct* and *SConscript* files for all kinds of complex processing, not limited to the rules set by the Makefile. For more documents about Scons, see Reference.

SCONS uses *SConscript* and *SConstruct* files to organize the source code structure. Typically, there is only one *SConstruct* file for a project, but there can be multiple *SConscript* files. In general, there is a *SConscript* file in each subdirectory where the source code is stored. SConscript files are the backbone of the organization's source code.

To make it easier for RT-Thread to support multiple compilers and to adjust compilation parameters, RT-Thread creates a separate file named rtconfig.py for each BSP. Therefore, each RT-thread BSP directory contains the following three files: rtconfig.py, SConstruct, and SConscript, which control the compilation of the BSP. There is only one SConstruct file in a BSP but multiple SConscript files. As shown in Directory structure, the project SConstruct SConsript Kconfig files are located in imxrt1062-nxp-evk.

RT-Thread has *Sconscript* files in most source folders. These script files are linked with the Sconscript of BSP directory to add the source code for the macros defined in *rtconfig.h* to the compiler. We will take **imxrt1062-nxp-evk BSP** as an example to explain how to build a project with SCONS in the below.

#### 5.6.1 SConstruct

As mentioned above, BSP has only one *SConstruct* file which controls the compilation process. Figure 12 shows an example of SConstruct.

```
1
            import os
        2
            import sys
        3
            import rtconfig
        4
        5
          □if os.getenv('RTT ROOT'):
        6
                RTT ROOT = os.getenv('RTT ROOT')
        7
          else:
        8
                RTT ROOT = os.path.normpath(os.getcwd() + '/../..')
       9
       10
          sys.path = sys.path + [os.path.join(RTT ROOT, 'tools')]
       11
          □try:
          L
       12
                from building import *
       13 ⊞except:
       17
          TARGET = 'rtthread.' + rtconfig.TARGET EXT
       18
       19
          DefaultEnvironment(tools=[])
       20 ⊞if rtconfig.PLATFORM == 'armcc':
       29 ⊞else:
       37
          env.PrependENVPath('PATH', rtconfig.EXEC PATH)
       39
       40 ⊞if rtconfig.PLATFORM == 'iar':
       44
       45
          Export ('RTT ROOT')
            Export ('rtconfig')
       46
       47
       48
            # prepare building environment
       49
            objs = PrepareBuilding(env, RTT ROOT, has libcpu=False)
       50
       51
            objs = objs + SConscript('../libraries/drivers/SConscript')
       52
           objs = objs + SConscript('../libraries/drivers/wlan/SConscript')
            objs = objs + SConscript('../libraries/MIMXRT1062/SConscript')
       53
       54 objs = objs + SConscript('../libraries/sensors/SConscript')
            objs = objs + SConscript('../components/SConscript')
           objs = objs + SConscript('../ml_demos/SConscript')
       56
       57
            # make a building
       58
            DoBuilding (TARGET, objs)
       59
Figure 12. Example of SConstruct
```

#### 5.6.2 SConscript

*Sconscript* files connect all the source files, almost every source directory has one *Sconscript* file. There are two typical usages for SConscript files.

• The *SConcript* file in the **imxrt1062-nxp-evk BSP** directory manages all other *SConscript* files under the BSP, as shown in Figure 13.

```
2
           import os
       3
           from building import *
       4
       5
           cwd = GetCurrentDir()
       6
           objs = []
      7
          list = os.listdir(cwd)
      8
      9
         ∃for d in list:
      10
               path = os.path.join(cwd, d)
      11
               if os.path.isfile(os.path.join(path, 'SConscript')):
      12
                    objs = objs + SConscript(os.path.join(d, 'SConscript'))
      13
      14
           Return('objs')
     1.5
Figure 13. SConscript files
```

As shown in Figure 13, it involves all the SConstruct files of its subdirectories.

• The SConcript file in the Application directory manages the source code under the Application directory.

```
1
        import rtconfig
   2
       from building import *
   3
       cwd = GetCurrentDir()
   4
   5
       src = Glob('main.c')
   6
       src += Glob('app simple uart.c')
   7
        src += Glob('simple uart driver.c')
   8
       CPPPATH = [cwd]
   9
  10 # add for startup script
  11 LOCAL CCFLAGS=''
  12 [if rtconfig.CROSS TOOL == 'gcc':
  13 L
          LOCAL CCFLAGS = ' -std=c99 START=entry'
  14 [elif rtconfig.CROSS TOOL == 'keil':
  15 L
            LOCAL CCFLAGS = ' --c99 --gnu'
  16
  17
       group = DefineGroup('Applications', src, depend = [''], CPPPATH = CPPPATH)
  18
  19
       Return('group')
Figure 14. Sconscript file in Application directory
```

As shown in Figure 14, the script creates a group named **Application**, including **main.c**, **app\_simple\_uart.c** and **simple\_uart\_driver.c**.

For complex and large systems, it is obvious that more than just a few files in a directory are required. It is likely to be composed of several folders level by level.

In SCons, you can write **SConscript** scripts to compile files in these relatively separate directories and the **Export and Import** functions in SCons to share data between *SConstruct* and *SConscript* files (that is an object data in Python). For more information on how to use Scons, see SCons document.

#### 5.6.3 Rtconfig.py

*Rtconfig.py* is a standard compiler configuration file for RT-Thread that controls most of the compilation options. It is a script file written in Python that performs the following:

- Specify a compiler (choose the one you are using from the supported compilers).
- Specify compiler parameters, such as compile options, link options, and so on.

When compiling a project using the scons command, we compile the project according to the compiler configuration options of rtconfig.py. The following code is part of the code for *rtconfig.py* in the **imxrt1062-nxp-evk BSP** directory.

```
1
             import os
         2
         3
             # toolchains options
         4
            ARCH='arm'
         5
             CPU='cortex-m7'
            CROSS TOOL='gcc'
         6
         7
         8
           □if os.getenv('RTT CC'):
         9
               CROSS TOOL = os.getenv('RTT CC')
        10
           □if os.getenv('RTT ROOT'):
        11
                 RTT ROOT = os.getenv('RTT ROOT')
        12
        13
             # cross tool provides the cross compiler
        14 # EXEC PATH is the compiler execute path, for example, CodeSourcery, Keil MDK, IAR
        15 Fif CROSS TOOL == 'gcc':
        16
                 PLATFORM = 'gcc'
        17
                 EXEC PATH = r'C:\Users\XXYYZZ'
        18 Eelif CROSS TOOL == 'keil':
                 PLATFORM = 'armcc'
        19
        20
                 EXEC PATH = r'C:/Keil v5'
                 #EXEC PATH = r'D:/Keil v5'
        22 Pelif CROSS TOOL == 'iar':
                 PLATFORM = 'iar'
        23
                 EXEC PATH = r'C:/Program Files (x86)/IAR Systems/Embedded Workbench 8.1"
        24
            □if os.getenv('RTT EXEC PATH'):
        26
        27
                 EXEC PATH = os.getenv('RTT EXEC PATH')
        28
        29
             #BUILD = 'debug'
        30
             BUILD = 'release'
Figure 15. Code for rtconfig.py
```

#### 5.6.4 Kconfig

Kconfig is used to configure the kernel. The menuconfig command generates a configuration interface for users to configure the kernel by reading various Kconfig files of the project. The output of menuconfig is the *rtconfig.h*: all configurations related macro definitions will be automatically saved to the *rtconfig.h* file in the BSP directory. Each BSP has a *rtconfig.h* file, which is the configuration information of the board.

After entering the **imxrt1062-nxp-evk BSP** directory with the Env tool, you can see the configuration menu of the main page with the menuconfig command, as shown in Figure 16.

config - RT-Thread Configuration		? 🗄 ▾ 🔝 ▾ 🔒 🛄
	RT-Thread Configuration <enter> selects submenus&gt; (or empty submenus). Highlighted letter dularizes features. Press <esc><esc> to exit, <? > for Help,  for Search. le capable</esc></esc></enter>	
	RT-Thread Kernel> RT-Thread Components> RT-Thread online packages> Hardware Drivers> NXP Software Component Config>	

Save the configurations, exit the configuration interface and open the *rtconfig.h* file under the **imxrt1062-nxp-evk** directory. All the configuration information is already available.

**NOTE** DO NOT modify it manually.

Make sure to use the scons --target=mdk5 command to generate a new KEIL project every time when the menuconfig configurations is complete.

## 6 How to implement application

In the RT-Thread source code, the application locates in the **Application** directory. For example, to create a demo code to use the simple **uart** driver, name it as **app\_simple\_uart.c** and put it into the **Application** directory. Figure 17 shows the code.

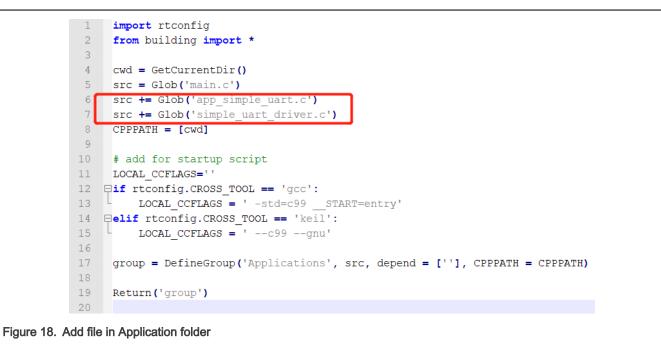
```
1 #include <rtthread.h>
            2 #include "rthw.h"
            3 #include <rtdevice.h>
            4
            5 void app_simple_uart()
            6 🗆 {
            7
                   char buffer[128];
            8
                   char test_str[] = "\r\n[Hello World!]\r\n";
            9
                   rt device t device = rt device find("s uart");
           10
                   if (device == RT NULL) {
           11
                       return;
           12
                   }
           13 🗄
                   if (device->open(device, 0) != RT EOK) {
           14
                       return;
           15
                   }
                  if (device->init(device) != RT EOK) {
           16 白
           17
                       return;
           18
                   }
           19
           20
                   device->write(device, 0, test str, sizeof(test str));
           21
           22
                   device->close(device);
           23 L}
           24 //register app simple uart into msh command
           25 MSH CMD EXPORT(app_simple_uart, app_simple_uart);
Figure 17. Demo code
```

As shown in Figure 17, there are two methods to call application API in RT-Thread:

- · Call API in the main thread
- Call API in the msh shell command

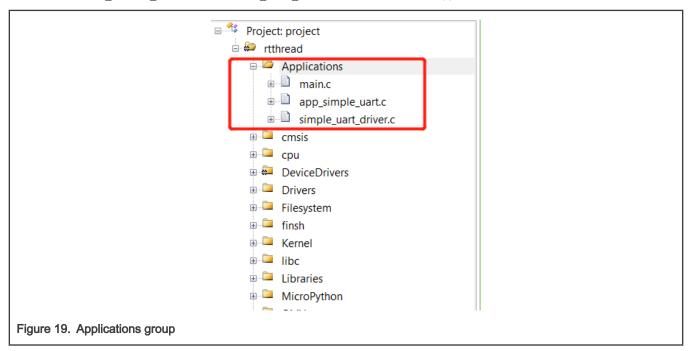
## 7 Build and run

After implementing the application codes, add the file into the project. As mentioned in Project construction, RT-Thread uses **scons** to generate the project file, so we need to add file into SConscript in the Application folder.



Use env tool to generate keil project by the scons --target=mdk5 command.

In the project, app\_simple\_uart.c and simple\_uart\_driver.c are included in Applications group.



After building the project, download the program by jLink and run. As shown in Figure 20, the program runs into the msh command line. Type the **app\_simple\_uart** command to run the example. The log shows the result: the test string came out through UART hardware.

- RT - Thread Operating System
/   \ 4.0.3 build Jan 15 2021
2006 - 2019 Copyright by rt-thread team
[I/I2C] I2C bus [i2c1] registered
RAM file system initializated!
msh />[I/SDIO] SD card capacity 15122432 KB.
found part[0], begin: 4194304, size: 14.428GB
[I/SDIO] try to mount file system!
USB MSC Connected with SD
USB MSC kUSB_DeviceMscEventReadCapacity SD
≣sh />app_simple_uart
[Hello World!]
msh />
Figure 20. Log

## 8 Reference

- RT-Thread document
- SCons document

## 9 Revision history

Revision number	Date	Substantive changes
0	04/2021	Initial release

How To Reach Us Home Page: nxp.com Web Support: nxp.com/support Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: nxp.com/SalesTermsandConditions.

**Right to make changes** - NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Security — Customer understands that all NXP products may be subject to unidentified or documented vulnerabilities. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately. Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP. NXP has a Product Security Incident Response Team (PSIRT) (reachable at PSIRT@nxp.com) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, COOLFLUX, EMBRACE, GREENCHIP, HITAG, ICODE, JCOP, LIFE, VIBES, MIFARE, MIFARE CLASSIC, MIFARE DESFire, MIFARE PLUS, MIFARE FLEX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TRENCHMOS, UCODE, Freescale, the Freescale logo, AltiVec, CodeWarrior, ColdFire, ColdFire+, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorlQ, QorlQ Qonverge, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, Tower, TurboLink, EdgeScale, EdgeLock, elQ, and Immersive3D are trademarks of NXP B.V. All other product or service names are the property of their respective owners. AMBA, Arm, Arm7, Arm7TDMI, Arm9, Arm11, Artisan, big.LITTLE, Cordio, CoreLink, CoreSight, Cortex, DesignStart, DynamIQ, Jazelle, Keil, Mali, Mbed, Mbed Enabled, NEON, POP, RealView, SecurCore, Socrates, Thumb, TrustZone, ULINK, ULINK2, ULINK-ME, ULINK-PLUS, ULINKpro, µVision, Versatile are trademarks or registered trademarks of Arm Limited (or its subsidiaries) in the US and/or elsewhere. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org.

© NXP B.V. 2021.

#### All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

> Date of release: 04/2021 Document identifier: AN13232

# arm